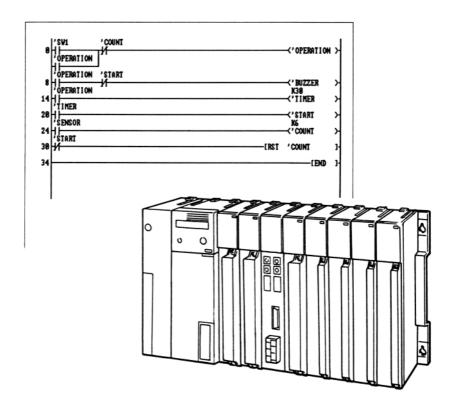
MITSUBISHI

QnA Series

QnACPU

GUIDEBOOK





Mitsubishi Programmable Controller

REVISIONS

*The manual number is given on the bottom left of the back cover.

Print Date	*Manual Number	Revision
Jul., 1996	IB (NA) 66606-A	First edition
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		·

SAFETY CAUTIONS

(You must read these cautions before using the product)

In connection with the use of this product, in addition to carefully reading both this manual and the related manuals indicated in this manual, it is also essential to pay due attention to safety and handle the product correctly.

The safety cautions given here apply to this product in isolation. For information on the safety of the PC system as a whole, refer to the CPU module User's Manual.

These SAFETY CAUTIONS are classified into two grades: "DANGER" and "CAUTION".



DANGER

Safety caution given when incorrect handling could result in hazardous situations involving the possibility of death or serious injury.

CAUTION

Safety caution given when incorrect handling could result in hazardous situations involving the possibility of moderate or light injury or damage to property.

Both of these classes of safety caution are very important and must be observed. Store this manual carefully in a place where it is accessible for reference whenever necessary, and forward a copy of the manual to the end user.

DANGER

- Safety circuits should be installed external to the programmable controller to
 ensure that the system as a whole will continue to operate safely in the event
 of an external power supply malfunction or a programmable controller failure.
 Erroneous outputs and operation could result in an accident.
 - 1) The following circuitry should be installed outside the programmable controller:
 - Interlock circuitry for the emergency stop circuit protective circuit, and for reciprocal operations such as forward/reverse, etc., and interlock circuitry for upper/lower positioning limits, etc., to prevent machine damage.
 - 2) When the programmable controller detects an abnormal condition, processing is stopped and all outputs are switched OFF. This happens in the following cases:
 - When the power supply module's over-current or over-voltage protection device is activated.
 - When an error (watchdog timer error, etc.) is detected at the PC CPU by the self-diagnosis function.
 - Some errors, such as input/output control errors, cannot be detected by the PC CPU, and there may be cases when all outputs are turned ON when such errors occur. In order to ensure that the machine operates safely in such cases, a failsafe circuit or mechanism should be provided outside the programmable controller. Refer to the CPU module user's manual for an example of such a failsafe circuit.
 - 3) Outputs may become stuck at ON or OFF due to an output module relay or transistor failure. An external circuit should therefore be provided to monitor output signals whose incorrect operation could cause serious accidents.
- A circuit should be installed which permits the external power supply to be switched ON only after the programmable controller power has been switched ON. Accidents caused by erroneous outputs and motion could result if the external power supply is switched ON first.
- When a data link communication error occurs, the status shown below will be
 established at the faulty station. In order to ensure that the system operates
 safely at such times, an interlock circuit should be provided in the sequence
 program (using the communication status information).

Erroneous outputs and operation could result in an accident.

- 1) The data link data which existed prior to the error will be held.
- 2) All outputs will be switched OFF at MELSECNET (II, /B, /10) remote I/O stations.
- At the MELSECNET/MINI-S3 remote I/O stations, all outputs will be switched OFF or output statuses will be held, depending on the E.C. mode setting.

For details on procedures for checking faulty stations, and for operation statuses when such errors occur, refer to the appropriate data link manual.

[System Design Precautions]

↑ CAUTION

• Do not bundle control lines or communication wires together with main circuit or power lines, or lay them close to these lines.

As a guide, separate the lines by a distance of at least 100 mm, otherwise malfunctions may occur due to noise.

When file register R that are outside the range are read, e.g. by a MOV instruction, the file register data will become FFFF_H and use of this data will cause malfunctions. Take care not to use file registers that are outside the range when designing programs.

For details on instructions, refer to the Programming Manual.

[Cautions on Mounting]

inputs/outputs.



CAUTION

- Use the PC in an environment that conforms to the general specifications in the manual.
 - Using the PC in environments outside the ranges stated in the general specifications will cause electric shock, fire, malfunction, or damage to/deterioration of the product.
- Make sure that the module fixing projection on the base of the module is properly engaged in the module fixing hole in the base unit before mounting the module.
 - Failure to mount the module properly will result in malfunction or failure, or in the module falling.
- Extension cables should be securely connected to base unit and module connectors. Check for loose connection after installation.
 A poor connection could result in contact problems and erroneous
- Plug the memory card firmly into the memory card mounting connector. Check for loose connection after installation.

A poor connection could result in erroneous operation.

DANGER

- Switch off the external power supply before staring installation and wiring work.
 - Failure to do so could result in electrical shocks and equipment damage.
- After installation and wiring is completed, be sure to attach the terminal cover before switching the power ON and starting operation.
 Failure to do so could result in electrical shocks.

CAUTION

- Be sure to ground the FG and LG terminals, carrying out at least class 3 grounding work with a ground exclusive to the PC.
 Otherwise there will be a danger of electric shock and malfunctions.
- Carry out wiring to the PC correctly, checking the rated voltage and terminal arrangement of the product.
 Using a power supply that does not conform to the rated voltage, or carrying out wiring incorrectly, will cause fire or failure.
- Outputs from multiple power supply modules should not be connected in parallel. Failure to do so could cause the power supply module to overheat, resulting in a fire or module failure.
- Tighten the terminal screws to the stipulated torque.
 Loose screws will cause short circuits, fire, or malfunctions.
- Make sure that no foreign matter such as chips or wiring offcuts gets inside the module.
 It will cause fire, failure or malfunction.
- Connectors for external connections should be crimped, pressure welded, or soldered in the correct manner using the correct tools.
 For details regarding crimping and pressure welding tools, refer to the input/output module user's manual.
 A poor connection could cause shorts, fire, and erroneous operation.

[Cautions on Startup and Maintenance]

DANGER

- Do not touch terminals while the power is ON.
 This will cause malfunctions.
- Make sure that the battery is connected properly. Do not attempt to charge or disassemble the battery, do not heat the battery or place it in a flame, and do not short or solder the battery.
 Incorrect handling of the battery can cause battery heat generation and ruptures which could result in fire or injury.
- Switch the power off before cleaning or re-tightening terminal screws.
 Carrying out this work while the power is ON will cause failure or malfunction of the module.

A CAUTION

- In order to ensure safe operation, read the manual carefully to acquaint
 yourself with procedures for program changes, forced outputs, RUN, STOP,
 and PAUSE operations, etc., while operation is in progress.
 Incorrect operation could result in machine failure and injury.
- Do not disassemble or modify any module.
 This will cause failure, malfunction, injuries, or fire.
- Switch the power OFF before mounting or removing the module.
 Mounting or removing it with the power ON can cause failure or malfunction of the module.
- When replacing fuses, be sure to use the prescribed fuse. A fuse of the wrong capacity could cause a fire.

[Cautions on Disposal]



Dispose of this product as industrial waste.

INTRODUCTION

Thank you for choosing the Mitsubishi MELSEC-QnA Series of General Purpose Programmable Controllers. Please read this manual carefully so that the equipment is used to its optimum. A copy of this manual should be forwarded to the end User.

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About Manuals

The manuals related to the QnACPU are listed in the table below. Please order those you require.

Related Manuals

Manual Name	Manual Number
Q2A(S1)/Q3A/Q4ACPU User's Manual Describes the performance, functions, and handling of the Q2ACPU(S1), Q3ACPU, and Q4ACPU, and the specifications and handling of memory cards and base units.	IB-66608
QnACPU Programming Manual (Fundamentals) Describes how to create programs, the names of devices, parameters, and types of program. (Purchased separately)	IB-66614
QnACPU Programming Manual (Common Instructions) Describes how to use sequence instructions, basic instructions, and application instructions. (Purchased separately)	IB-66615
QnACPU Programming Manual (Special Function) Describes the dedicated instructions for special function modules available when using the Q2ACPU(S1), Q3ACPU, and Q4ACPU. (Purchased separately)	IB-66616
QnACPU Programming Manual (AD57 Instructions) Describes the dedicated instructions for controlling an AD57(S1) type CRT controller module available when using the Q2ACPU(S1), Q3ACPU, or Q4ACPU. (Purchased separately)	IB-66617
QnACPU Programming Manual (PID Control Instructions) Describes the dedicated instructions for PID control available when using the Q2ACPU(S1), Q3ACPU, or Q4ACPU. (Purchased separately)	IB-66618
Type SW0IVD-GPPQ GPP Function Operating Manual (Offline)	IB-66623
Type SW0IVD-GPPQ GPP Function Operating Manual (Online)	IB-66624

1. USING THIS MANUAL

This manual explains:

- What type of CPU the Q2ACPU(S1), Q3ACPU, and Q4ACPU are;
- The operating procedures when using QnA;
- And examples of use of the QnA features.

It includes descriptions of the operations of the SW IVD-GPPQ GPP function software package (hereafter abbreviated to "GPPQ").

See the manuals listed in the table of related manuals for more details about information and operations described in this manual. Refer to the related manuals for a deeper understanding of the QnACPU functions and GPPQ functions.

The structure of this manual is as follows:

- Chapter 1 USING THIS MANUAL
 - Describes the terms and abbreviations used in this manual and the basic key operations.
- Chapter 2 QnACPU GENERAL DESCRIPTION

 Describes the structured files which are a feature of QnACPU

 and provides a simple explanation of structured programs in

 multiple files.
- Chapter 3 OPERATING PROCEDURE USING THE QnACPU
 Describes the QnACPU operation flow using an example system configuration and describes the GPPQ operations for programming.
 The operations described include installing the software in the peripheral device, programming, CPU operation, and printing out.
- Chapter 4 MAKING THE MOST OF YOUR QnACPU (1)
 Gives tips on programming with multiple files, which is a feature of QnACPU.
- Chapter 5 MAKING THE MOST OF YOUR QnACPU (2)
 Describes programming with labels, which is a feature of QnACPU.
 Also describes how to use macro instructions.

1.1 Terms and Abbreviations Used in This Manual

The following abbreviations are used in this manual. (1) QnACPU An abbreviation for a Q2ACPU, Q2ACPU-S1, Q3ACPU, or Q4ACPU CPU module. (2) GPPQ (GPP function software) Abbreviation for the SWIIVD-GPPQ GPP function software package. IBM PC/AT An IBM PC/AT computer, or 100% compatible (*1). Peripheral device capable of GPP functions . . . General term for an IBM PC/AT, or other peripheral device, which operates the GPP function software. Internal memory QnACPU internal RAM for (5) storing sequence programs, etc. Memory card Abbreviation for a (6)Q1MEM- | memory card. (7) ACPU..... General term for MELSEC-A Series PC CPUs. *1 IBM is a registered trademark of International Business Machines

POINT

Corporation.

The RAM memory built into the QnACPU is referred to as "internal memory" in this manual.

The term "internal RAM" appearing in SW ☐ IVD-GPPQ displays also refers to this internal memory.

1.2 Basic Key Specifications

The key applications with the GPP functions are listed in the table below.

(1) Key Applications

Key Name	Application	Key Name	Application
[Esc]	Closing windows, halting execution, selecting an instruction Opening/closing windows	[F11]	Opens the mode select window
[Tab]	Tab code input, rapid cursor	[↑][↓][←][→]	Moves the cursor or scrolls through lines of a ladder or list display
[Ctrl] + [Tab]	movement	1	([↑] [↑])
[Ctrl]	Used in combination with alphanumeric keys and functions keys	[Back Space]	Deletes the character to the left of the cursor
[Shift]	Selects the character at the Shift position	[Enter]	Inputs the carriage return
[Caps Lock]	Switches between uppercase and lowercase characters	[Ctrl] + [Home]	Moves the cursor to step 0 in the ladder or list mode
[Alt]	Selects the menu	[Ctrl] + [End]	Moves the cursor to the END instruction in the ladder or list mode
[Page Up]	Scrolls page up (- direction) one screen for ladder, list, or help display	[Print Screen]	Copies the screen
[Page Down]	Scrolls page down (+ direction) one screen for ladder, list, or help display	[Scroll Lock]	Disables scrolling up or down
[insert]	Inserts a space at the cursor position	[Num Lock]	Sets the numeric keypad for numeric input only
[Delete]	Deletes the character at the cursor position (clears entire set contents)	[F12]	Opens the HELP window
[Home]	Moves the cursor to the home position		

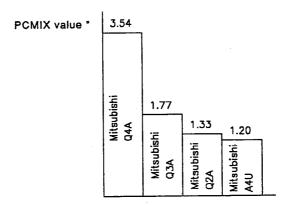
2. QnACPU GENERAL DESCRIPTION

2.1 General Description

The QnACPU is a PC CPU which is both easier to use and more sophisticated than the previous ACPU. In particular, programming efficiency is enhanced by structured programs and instruction definition with macro instructions. QnACPU offers the following features:

- High-speed Processing
- (1) Increased processing speed for both basic and application instructions realizes a further dramatic jump in operation speed (approximately 3-times increase for PCMIX value * ratio using Mitsubishi AnU). This realizes dramatic reductions in tact time and allows high-speed processing of complex data.

	A4UCPU		Q4ACPU
Basic instructions	0.15 μs	\rightarrow	0.075 μs (2 x)
Transfer instructions	0.90 μs	\rightarrow	0.225 μs (4 x)



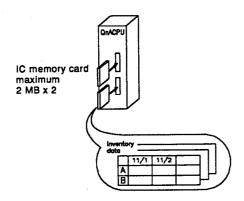
* PCMIX value: The average number of instructions executed in 1 μs.

High Memory Capacity

Serial execution of 124 k steps maximum in internal memory (4-times increase)

Maximum memory card capacity: 2 MB x 2 (4-times increase)

(2) The highly integrated IC memory card allows large volumes of data to be managed (2 MB x 2 max.).



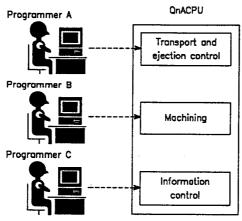
- The application of highly integrated IC memory cards has significantly increased the capacity of the expansion data memory (approximately 5-times increase using Mitsubishi AnUCPU).
 This makes it easier to handle large vol-
 - This makes it easier to handle large volumes of data.
- 2) The cards can also be used to save multiple programs and store comments.

Program Management

Decentralized control of programs and data is possible with a maximum of 128 files.

This promotes structured programming and allows debugging and program maintenance by multiple programmers.

(1) Multiple program management



A program can be stored in the CPU as multiple smaller programs.

Dividing the program by programmer, by function, or by process, allows multiple programmers to simultaneously develop a program and simplifies re-using the program.

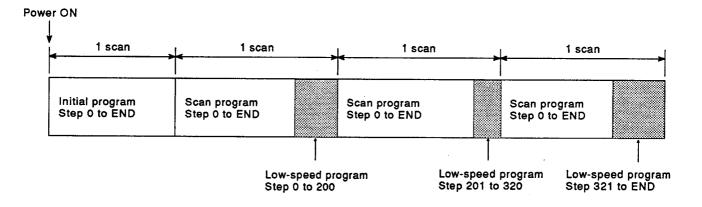
When a program is modified, only the relevant file needs to be changed, which means that unwanted effects on other parts of the program can be minimized.

To simplify program maintenance, a comment and time-stamp is appended to each modification.

Structured Programs

Programs can be selected from four types: initial execution, scan execution, low-speed execution, and standby.

These programs can be set to run when required, thereby reducing the scan time of the permanently executing scan program.

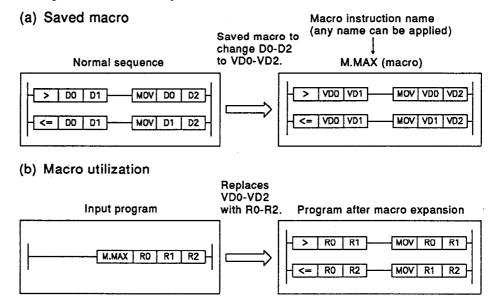


Program Standardization

(1) Macro instructions

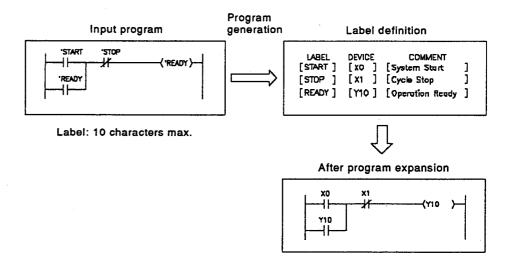
This function allows user-created instructions to be created by combining sequence instructions.

Using macro instructions allows the device numbers to be easily changed to match the system used, as shown below.



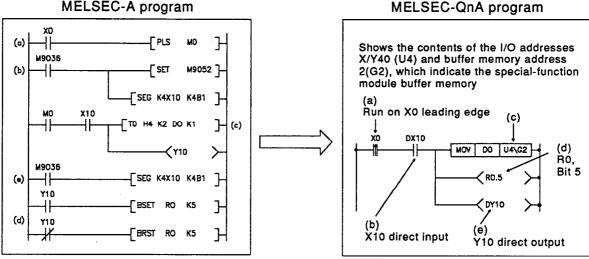
(2) Programming with labels (program standardization)

If the CPU type is set to "Xtype", standard programs can be created using labels, without considering the CPU model name, I/O addresses, or device number allocation. These standardized programs can be used by allocating devices to the labels to match the system used.

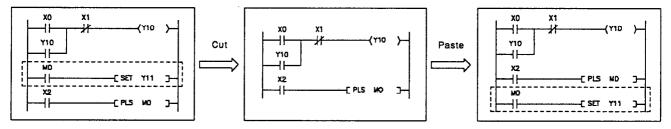


 Reduced Programming Time and Effective Memory Utilization A number of convenient instructions are provided to reduce programming time. The number of device points can be allocated to match the system, such that each required device occupies only the required number of points, and unnecessary devices can be removed from the 28 k words of device memory.

MELSEC-A program



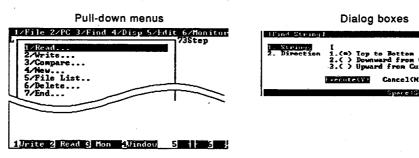
- Flexible Operation
- (1) Comprehensive editing functions
 - (a) Simultaneous, parallel editing of four programs.
 - (b) One-touch switching of the edited program.
 - (c) Cutting and pasting between edited programs.
 - (d) Special features, such as vertical and horizontal line connections and parallel coil insertion, for easy creation of ladder programs.



- Transparent operation
 - (a) The pull-down menus and dialog boxes offer ease of operation equivalent to commercial software packages. Frequently used functions can be allocated to function keys to ensure correct operation every time.

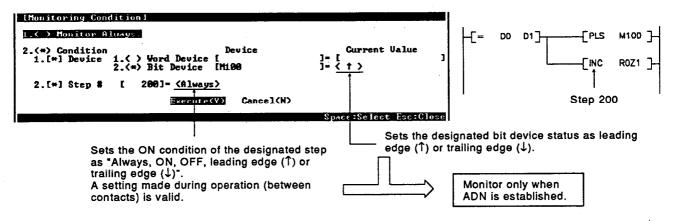
Cancel(N)

pace:Select Esc:Close



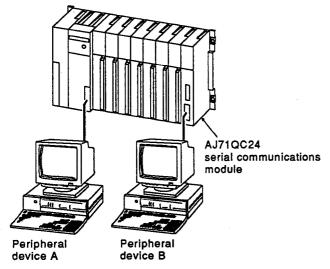
- Comprehensive Debugging Functions
- (1) Comprehensive ladder monitor functions
 - (a) Index-qualified devices and expansion file registers can be monitored.
 - (b) The peripheral device designates the QnACPU monitoring timing, to allow precise monitoring timing. At the designated timing, the peripheral device reads and displays the monitored information.
 - <Example>

Monitoring timing set at 200 steps and M100 leading edge.



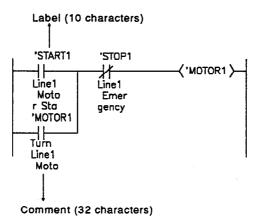
(2) Debugging by multiple people

Debugging is possible using multiple peripheral devices connected to a single QnACPU.



- (a) If each peripheral device debugs a separate file name, monitoring, testing, and online program change (write during RUN) can be conducted freely, without affecting any other peripheral device. For example, if peripheral device A debugs "Machining program" and peripheral device B debugs "Assembly program."
- (b) Also, online program change (write during RUN) is possible for the same file from multiple peripheral devices.
 A program pointer must be pre-designated for the online program change (write during RUN) programs.

- Simple Programming
 Powerful document-creation support
- (1) Integral management of statements and notes with the program.
- (2) Comments can be added to all devices.
- (3) Comments up to 32 characters and labels up to 10 characters improve program readability.

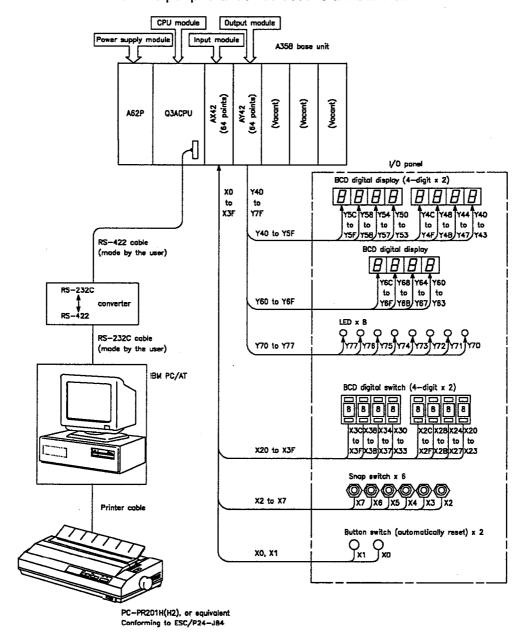


3. OPERATING PROCEDURE USING THE QNACPU

This chapter describes the basic operation of the QnACPU and the preparations and procedures required before operation.

3.1 Applicable System Configuration

The diagram shows the example system used for the following explanations. The peripheral device used is an IBM PC/AT.

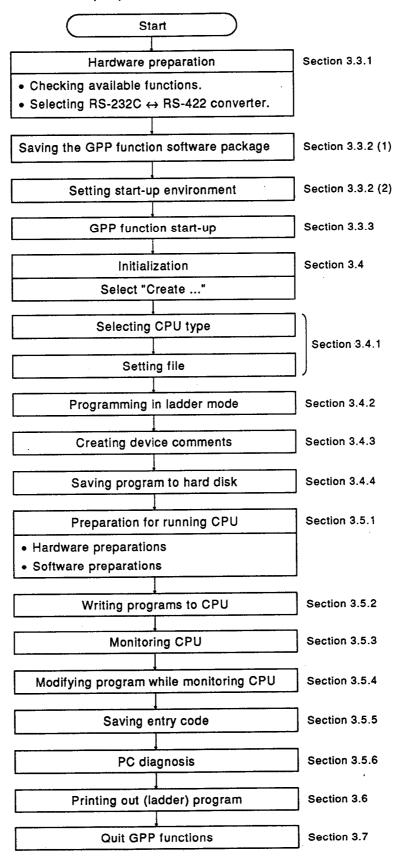


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This guide uses an example of connection to the PC CPU via the built-in RS-232C port.

3.2 General Procedure for Overall Operation

The overall procedure for the basic QnACPU operations described from Section 3.3. The peripheral device is an IBM PC/AT.

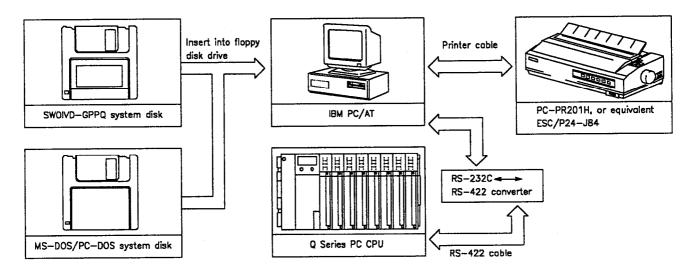


3.3 Starting Up GPP Functions after Completing Peripheral Device Preparations

The peripheral device hardware settings must be made and the GPP function software package must be set before running the GPP functions.

3.3.1 Preparing the IBM PC/AT hardware

The system configuration is shown below.



(1) Applicable IBM PC/AT

The following IBM PC/AT specifications can be used.

CPU: 80486SX(20 MHz), or higher (recommended: 80486DX2

(66 MHz))

EMS : 4 MB min.

Hard disk free space : 20 MB min.

Other: Install HIMEM and SMARTDRV drivers.

(2) MS-DOS/PC-DOS operating system disk

Start the operating system disk before starting the personal computer. This system uses the MS-DOS or PC-DOS operating system. Check that the version of the MS-DOS/PC-DOS operating system is version 5.0 or later.

(3) Free space in main memory

At least 540 k bytes free space is required in the main memory of the IBM PC/AT used.

(4) SW0IVD-GPPQ software package

This system disk provides the software package which is installed onto the IBM PC/AT hard disk to run the GPP functions (sequence program environment part) and SFC functions.

The software package is contained in the following disks.

3.5-inch disks

SW0IVD-GPPQ-1 2 floppy disks SW0IVD-GPPQ-2 1 floppy disk SW0IVD-GPPQ-3 1 floppy disk

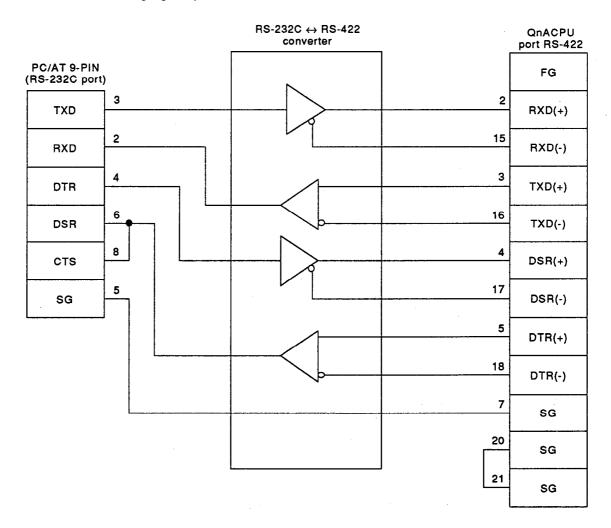
SW0IVD-GPPQ-1 is copy protected.

Installation of the software package requires 8 M byte minimum of free space on the hard disk.

(5) RS-232C ↔ RS-422 converter

The computer and the QnACPU are connected by means of an RS-232C \leftrightarrow RS-422 converter.

Shown below is an example of the connections between the computer and the QnACPU through the RS-232C \leftrightarrow RS-422 converter. (Connect the wires to the RS-232C \leftrightarrow RS-422 as illustrated in the following figure.)



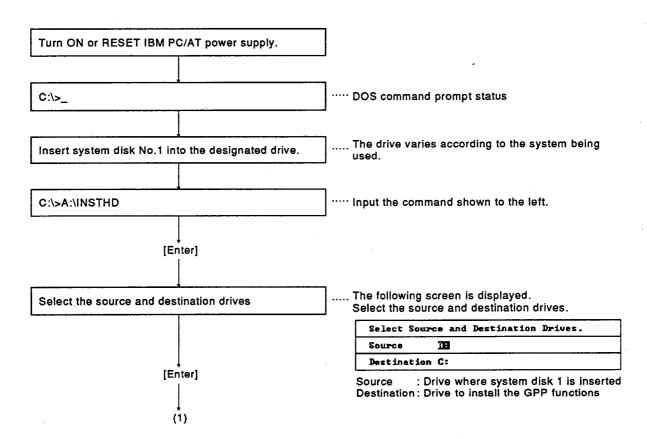
3.3.2 Installing the GPP function software package

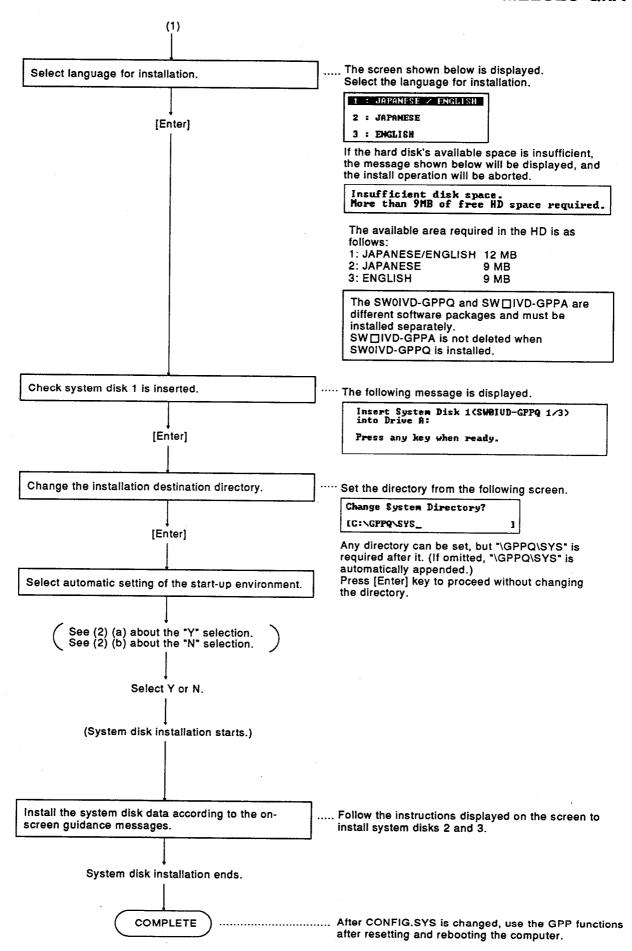
(1) Installation procedure

This section describes the procedure to install SW0IVD-GPPQ on the IBM PC/AT hard disk.

The installation is based on the conditions shown below.







(2) Setting the Start-up environment

Environment settings using CONFIG.SYS are required before starting the GPP functions.

(a) Automatic set-up

The following message is displayed during installation of the software package.

Sets Environment Uariables.
Modifies CONFIG.SYS.
Perform Modification(Y/N)?

Press [Y] then [Enter] to change FILES (number of files) in CON-FIG.SYS to 30.

However, if FILES is already greater than 30, no change is made and the original data is used.

The CONFIG.SYS settings before the changes are saved as CONFIG.ORG.

Note that any existing CONFIG.ORG file is overwritten.

(b) User set-up

The user must set the GPP function start-up environment if "N" is selected in response to the message prompting whether to make the automatic settings after the software package is installed.

The following CONFIG.SYS, AUTOEXEC.BAT settings are required to start up the GPP functions.

<Example>PC-DOS Ver. 5.0

[CONFIG.SYS]
FILES=30
BUFFERS=20
DOS=HIGH, UMB*1
COUNTRY=081, 932, C:\DOS\CONTRY.SYS
SHELL=C:\DOS\COMMAND.COM /P /E:512
DEVICE=C:\DOS\\$FONT.SYS /24=ON
DEVICE=C:\DOS\HIMEM.SYS
DEVICE=C:\DOS\EMM386.EXE 4096 RAM FRAME=E000*2
DEVICE=C:\DOS\DISP.SYS
DEVICE=C:\DOS\\$IAS.SYS
DEVICE=C:\DOS\\$IAS.SYS /X

[AUTOEXEC.BAT]
@ECHO OFF
C:\DOS\SMARTDRV.EXE /X*3
PATH C:\WINDOWS;C:\DOS;C:\
SET TEMP=C:\TMP
SET DOSD!R=C:\DOS

- *1: XMS manager (required for EMM386/SMARTDRV)
- *2: Reserve 4 MB for EMS
- *3: Enable the disk cache.

(c) Batch files

To start up the GPP functions, copy the following GPPQ.BAT batch file to the root directory.

The drive name is replaced by the drive name designated during system installation.

[GPPQ.BAT]
C:
CD C:\GPPQ\SYS
GPPQ.EXE

- (3) Cautions about environment set-up
 - (a) A minimum of 540 kbytes free space is required in main memory. Execution speed increases if more free space is available.
 - (b) The conditions that have to be set in the CONFIG.SYS file to start up the GPP functions are as follows: FILES: 30 or more

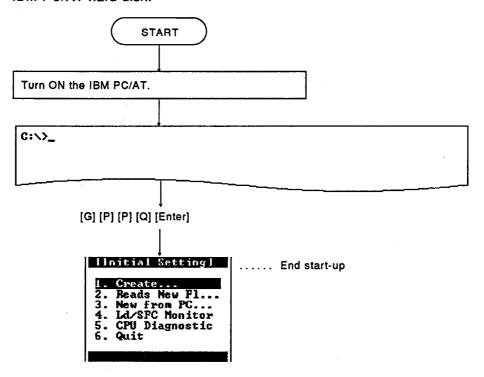
No other specific settings are required.

- (c) It is not necessary to incorporate PRINT.SYS (printer driver) and RSDRV.SYS (PC communications driver) into CONFIG.SYS. They are included in the GPP functions.
- (d) Incorporate PRINT.SYS into CONFIG.SYS to make hard copies.
- (e) More than 4 MB of EMS memory is required.

 Configure your memory settings according to your system.

3.3.3 Starting the GPP functions

This section describes how to start up the GPP functions installed on the IBM PC/AT hard disk.



3.3.4 Corrective action when an error occurs

(1) Corrective action if the GPP functions cannot be installed.

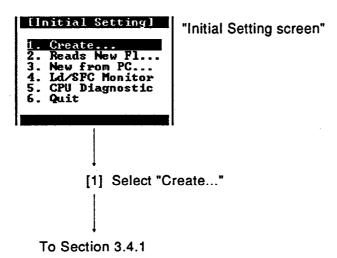
Cause	Installation from a copied floppy disk.
Corrective action	The system floppy disk for the GPP functions is copy-protected. Correct installation is not possible from a copied floppy disk. Reinstall from the master floppy disk.
nstallation stops and a n	nessage indicates that insufficient free space remains on the hard disk.
Cause	The hard disk is too full to install the GPP functions.
Corrective action	Installation of the GPP functions requires at least 8 MB free space on the hard disk. Move user data to floppy disks to ensure sufficient free space on the hard disk.
A message indicates that	normal installation was not possible.
Cause	The system floppy disk or hard disk is damaged.
Corrective action	Replace the system floppy disk or hard disk.
A message during installa	ation indicates that the directory could not be created.
Cause	Incorrect directory designation.
• Directory designation cannot contain special symbols, such as: "\", ":", or ">". Designate the directory using alphanumeric characters.	

(2) Corrective action if the GPP functions will not start up

Cause	Functions were installed with the copy command instead of the INSTHD command.
Corrective action	The functions cannot be correctly installed using the copy command. Install the GPP functions again by executing the "INSTHD" command.
message indicates that	the main memory area is insufficient on start-up, or system goes out of control during GPP
Cause	Insufficient free space in main memory (540 k byte min.) The "BUFFERS" item is set to 20 or less in CONFIG.SYS.
	Use the MEM or CHKDSK command to check the main memory free space. Delete unwanted drivers if the main memory free space is insufficient (see the description of CONFIG.SYS in Section 3.3.2). Use the TYPE command to check the CONFIG.SYS settings.
Corrective action	How to check the CONFIG. SYS settings. C:\TYPE CONFIG.SYS FILES=30 BUFFERS=20 BUFFERS size
	If the BUFFERS size is set to 20 or less, use the EDIT, EDLIN, or TE command to mode the CONFIG.SYS contents.

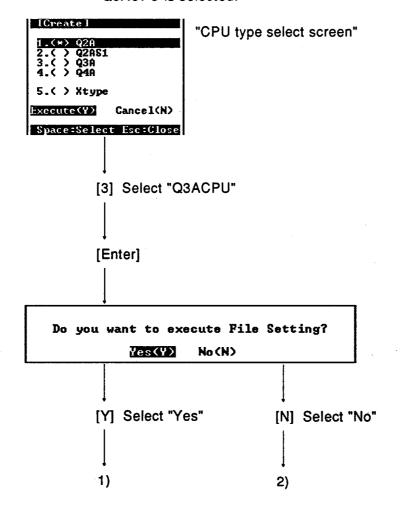
3.4 Creating Programs

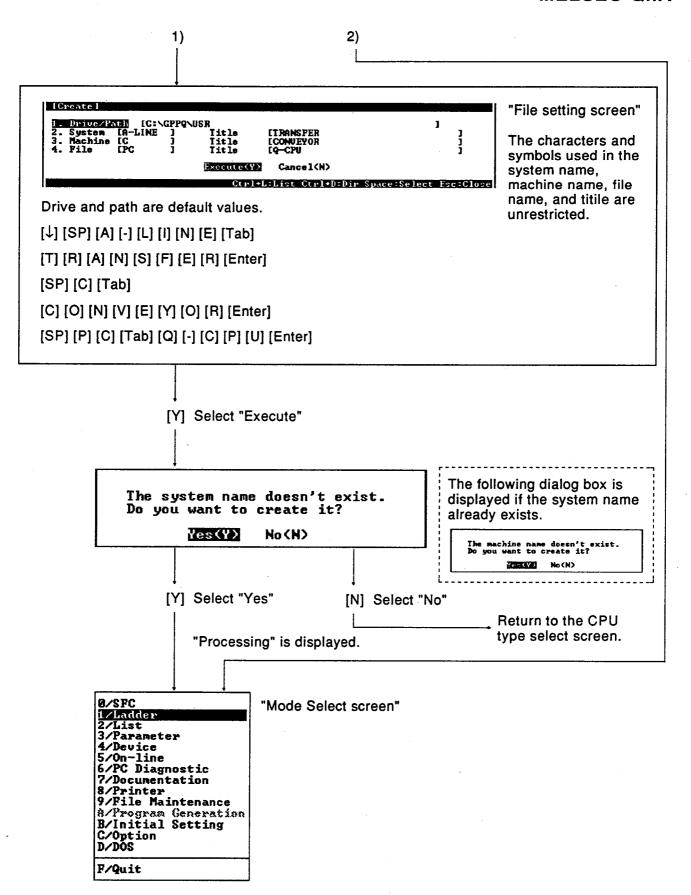
Use screen operations after starting GPPQ.



3.4.1 Selecting CPU type used and setting file

The following screen is displayed after "Create..." is selected above. Press the key corresponding to the CPU type used. In this example, Q3ACPU is selected.





3.4.2 Programming in ladder mode

The procedure for programming in the ladder mode is shown below.

(1) About the program

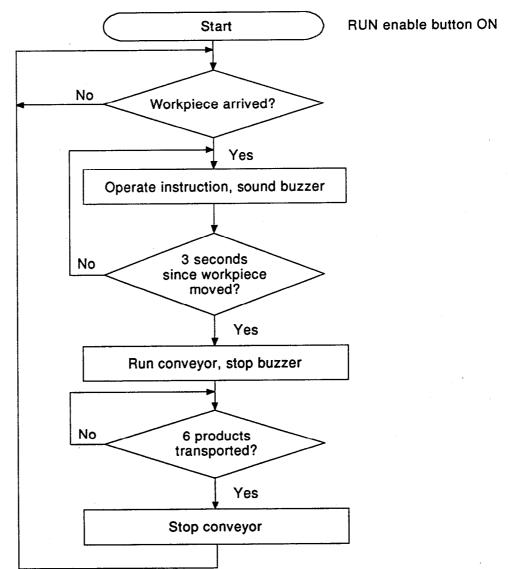
The program created to automatically run the conveyor is shown in Fig. 3.2.

(2) Description of operation

The operation of the program to automatically run the conveyor is outlined below.

When the workpiece (pallet) stops in front of the conveyor, a warning buzzer sounds for 3 seconds and the conveyor automatically starts. The conveyor stops after it has transported 6 products onto the workpiece. The workpiece is automatically transferred to the next process, the products unloaded, and then returns to the position in front of the conveyor.

The flowchart for this procedure appears as follows.



(3) Allocation of devices

• X0 RUN enable button	M0 Operation command
• X1 STOP button	M1 Operation record
X2 Work sensor	 M10 Operating enable
 X3 Product sensor 	• M11 Record operation enable
 Y70 Buzzer output 	• T0 Warning timer
 Y71 Conveyor Operation 	C0 Product count

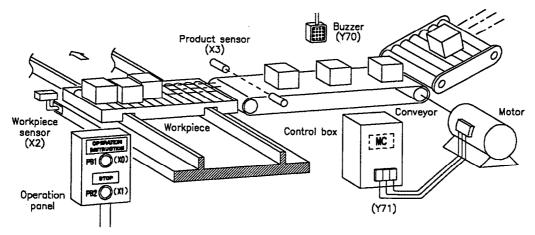


Fig. 3.1 Automatic Transport Equipment

(4) Program for automatic conveyor operation

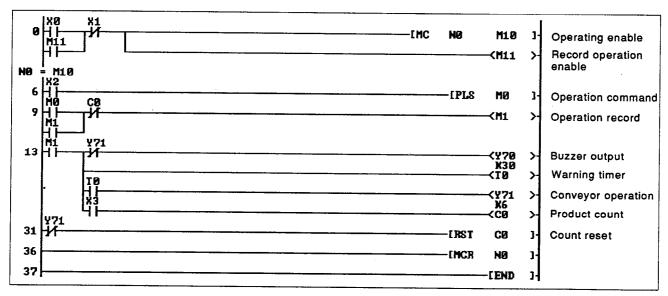
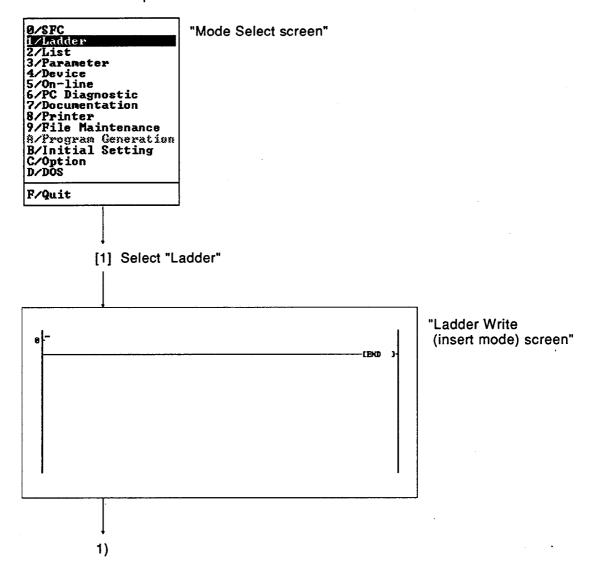
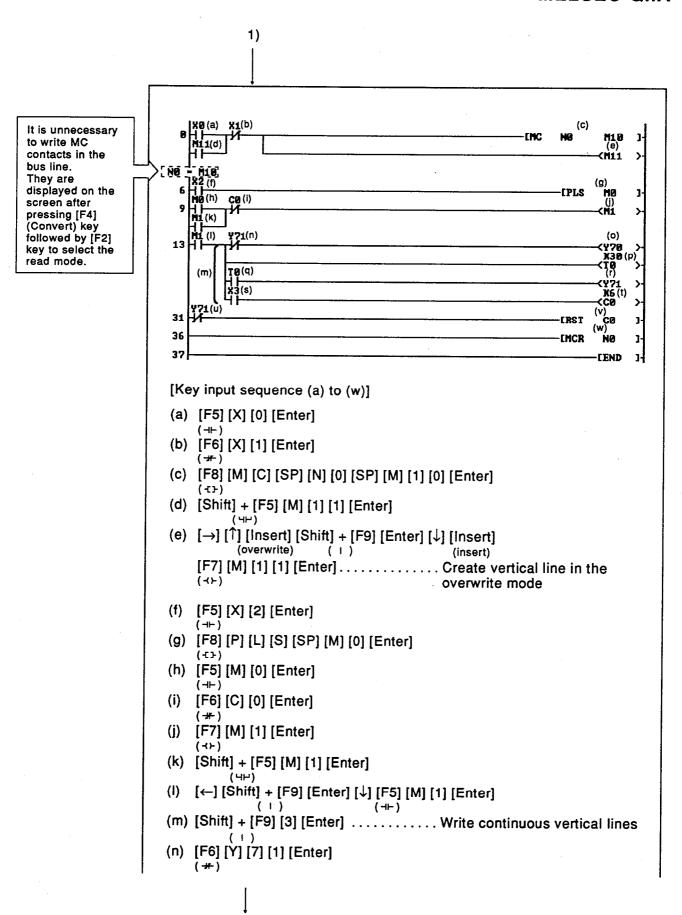


Fig. 3.2 Program for Automatic Conveyor Operation

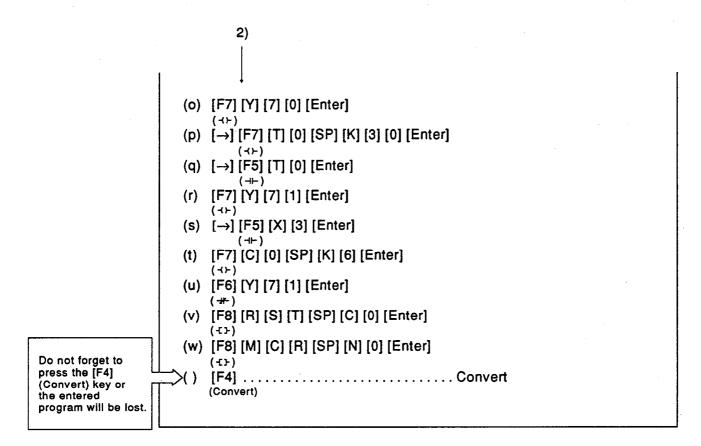
(5) Programming procedure

The program is created using ladder symbols. Operation starts from the mode select screen.





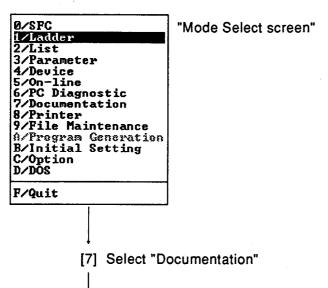
2)

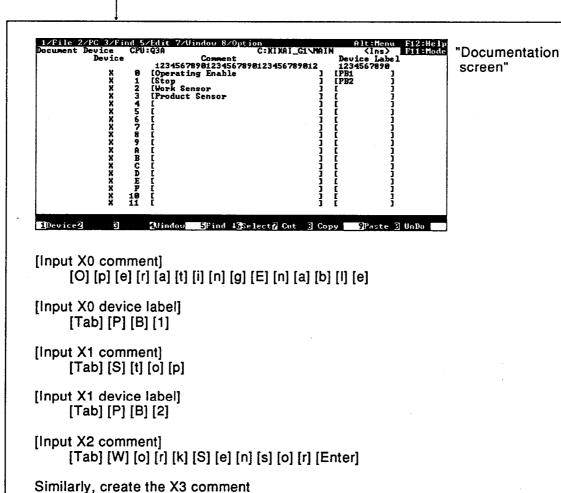


3.4.3 Appending comments to devices

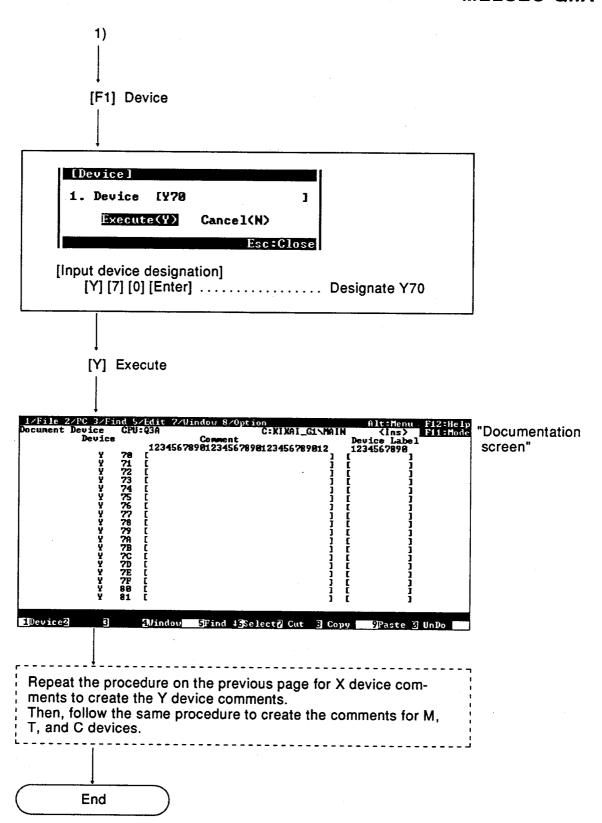
Operation from the mode select screen.

To conduct further operations after creating a sequence program, press [F11] key to display the mode select screen.



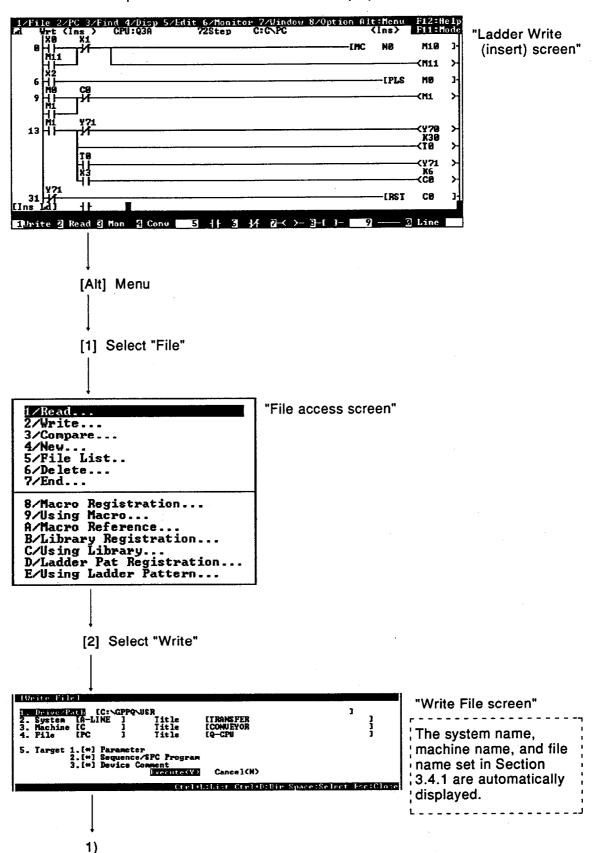


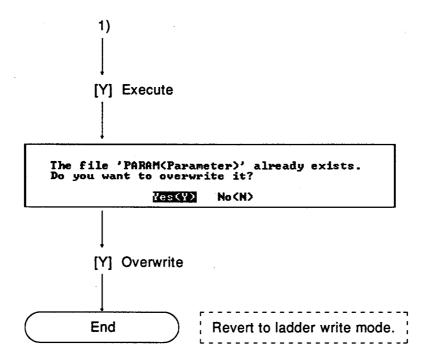
1)



3.4.4 Saving program to hard disk

Save the sequence program created in Section 3.4.2 to the hard disk. This operation starts from the screen displayed after conversion.





POINT

The following system name, machine name, and file name defaults are used if no file is set:

System name

[SYSTEM]

Machine name

[KIKAI_G1]

• File name

[MAIN]

The default drive and path is "C:\GPPQ\USR".

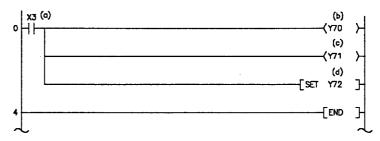
3.4.5 Convenient functions for programming

The parallel coil, line, and cut and paste editing are convenient functions for programming in the ladder mode.

(1) Parallel coils

When coil-equivalent instructions $(\langle + \rangle, \langle + \rangle)$ are input, the vertical lines and parallel coils are created automatically to create a ladder block. The ladder block below was created using parallel coils.

Operations in the ladder write (insert) mode.

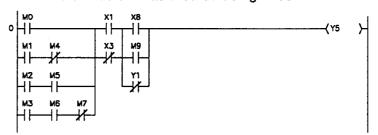


[Key input sequence (a) to (d)]

- (a) [F5] [X] [3] [Enter]
- (b) [F7] [Y] [7] [0] [Enter]
- (c) $[\rightarrow]$ [7] [7] [7] [1] [Enter]........... Write parallel coils
- (d) [→] [↑] [F8] [S] [E] [T] [SP] [Y] [7] [2] [Enter] (-:-) Write parallel coils
- (e) [F4] Convert

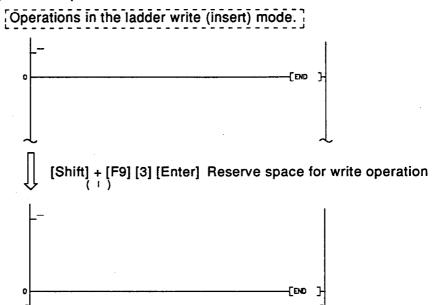
(2) Lines

A ladder block can be created using lines. The ladder block below was created using lines.

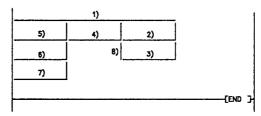


Copy line (2)

(a) Provide space before the END instruction for the ladder.



(b) Draw lines as shown in the diagram below.

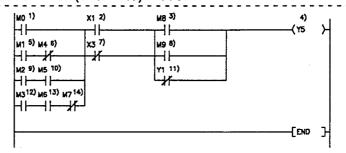


[Key input sequence 1) to 8)]

- 1) [F10] [Enter] $[\rightarrow]$ $[\rightarrow]$ [Enter] (Line) (Start) (Designate line, repeat 9 timese) (End)
- 2) [Esc] [Esc] [Enter] $[\downarrow]$ [\leftarrow] [\leftarrow] [Enter] (Continue) (Start) (Designate line) (End)
- 3) [→] [→] [→] [Enter] (Designate copy destination)
- 4) [↑] [←] [←] [Enter]
 (Designate copy destination)
- 5) [←] [←] [Enter] (Designate copy destination)
- 6) [↓] [Enter]
- (Designate copy destination)
 7) [↓] [Enter]
 (Designate copy destination)
- 8) [Esc] [Esc] $[\rightarrow]$ $[\rightarrow]$ [Enter] $[\uparrow]$ [Enter] (Continue) (Create position) (Start) (End)

(c) Write contacts and coils on lines

Operations in the ladder write (overwrite) mode.
To continue after operation (b), press the [Esc] key three times to cancel the line mode and press the [Insert] key to select the ladder write (overwrite) mode.



[Key input sequence 1) to 14)]

- 1) [F5] [M] [0] [Enter]
- 2) [→] [→] [F5] [X] [1] [Enter]
- 3) [→] [→] [F5] [M] [8] [Enter]
- 4) [→] [→] [F7] [Y] [5] [Enter]
- 5) [F5] [M] [1] [Enter]
- 6) [F6] [M] [4] [Enter]
- 11) [→] [→] [F6] [Y] [1] [Enter] (Input 5 times)
- 13) [F5] [M] [6] [Enter]
- 14) [F6] [M] [7] [Enter]
 -) [F4]..... Conversion (Convert)

COMMENT

It is also possible to first draw the contact and coil instruction symbols and subsequently draw the lines. In this case, lines cannot overwrite instruction symbols.

POINTS

- Horizontal, vertical, ____ line, and ____ lines can be drawn by a single operation. Rectangles, ____ lines, and ____ lines cannot be drawn using a single operation.
- Lines can be copied to any position by designating the destination.
- Lines and contact positions can be drawn as required. Line lengths and contact positions are corrected during conversion.

(3) Cutting and pasting

Parts of ladder programs can be cut and pasted.

The cutting and pasting operations can be conducted in the following units:

- ladder lines
- ladder blocks
- ladder program sections
- (a) Cutting and pasting ladder lines

These operations are possible by selecting Edit from the menu or by selecting the ladder edit (insert/overwrite) mode with the functions keys.

1) Operations by selecting Edit from the menu

(Convert)

Select the ladder write (insert/overwrite) mode. (The operations are also possible in the ladder edit (insert/overwrite) mode.)

- 2) Operations by selecting the ladder edit (insert/overwrite) mode with the functions keys
- (b) Cutting and pasting ladder blocks

Operation by selecting Edit from the menu.

Select the ladder read mode.

• Cut $[Alt] \to [5] \to [1] \to [\uparrow]/[\downarrow] \to [Enter] \\ (Menu) \quad (Edit) \quad (Cut) \quad (Designate start) \\ \to [\downarrow]/[\uparrow] \to [Enter] \\ (Designate range) \quad (Cut)$ • Paste $[Alt] \to [5] \to [3] \to [\uparrow]/[\downarrow] \\ (Menu) \quad (Edit) \quad (Paste) \quad (Designate paste destination) \\ \to [Enter] \\ (Paste)$

 Conversion Not required (Cut and paste changes are directly reflected in memory when [Enter] key is pressed.)

REMARK

The "Close up" setting affects the program after cut and paste, as follows:

"Close up" set...... The part where a program is cut is closed up.

(The number of program steps remains unchanged after the

editing.)

The part where a program is cut is not closed up but filled with NOP instructions. (The number of program steps after the editing

increases by the number of steps cut and pasted.)

Method for closing up [Alt] \rightarrow [8] \rightarrow [1] \longrightarrow [1] Close up the program after conversion.

• [2] Do not close up the program after conversion.

The default is "Do not close up".

(c) Cutting and pasting ladder program sections

Operations by selecting the ladder edit (insert/overwrite) mode with the function keys

 $\begin{array}{lll} \bullet \mbox{ Cut } & [\mbox{Shift}] + [\mbox{F1}] \rightarrow [\mbox{f}]/[\mbox{\downarrow}]/[\mbox{\leftarrow}]/[\mbox{\downarrow}]/[\mbox{\leftarrow}]/[\mbox{\downarrow}]/[\mbox{\leftarrow}]/[$

(Write) (Convert)

After cutting a pasting a program section, write the vertical and horizontal lines in the program section before conversion. Otherwise, a message will be displayed indicating that the ladder program is incomplete.

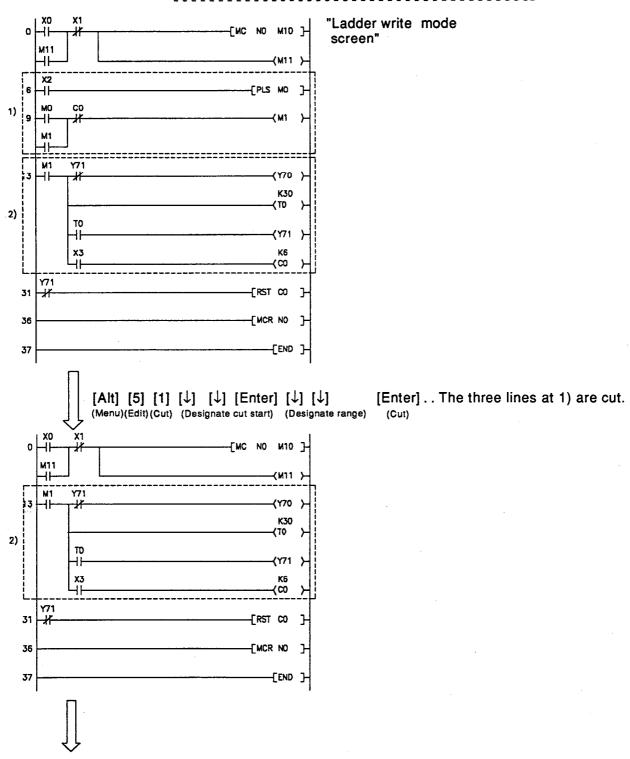
COMMENT

- A cut operation alone is equivalent to deleting ladder blocks, except that the cut ladder blocks are stored in buffer memory.
- A paste operation after a cut operation is equivalent to copying the cut ladder blocks from the buffer memory to the designated position.
- After a cut operation, pasting can be carried out any number of times. This allows the cut ladder blocks to be inserted into any number of programs.
- Cutting and pasting are also possible for instruction lists.

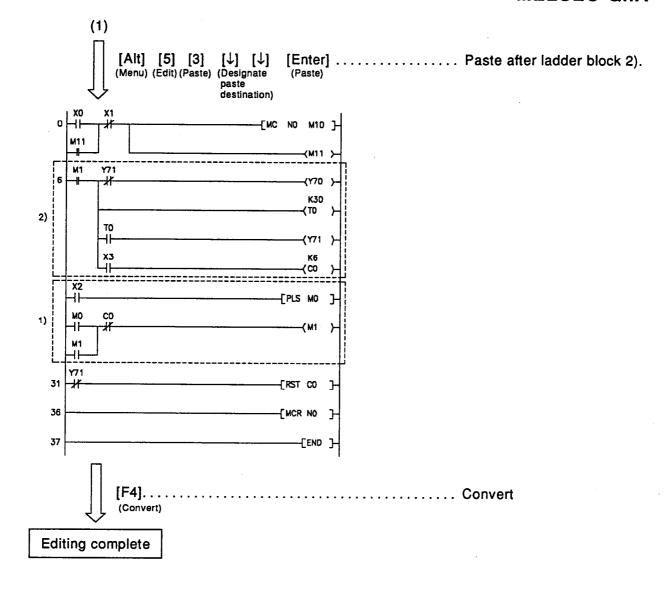
(d) Examples of cutting and pasting

As an example, cutting and pasting will be shown for the ladder program below. All operations act on units of ladder lines. The three lines at 1) are cut and pasted after ladder block 2). The operations are carried out in this example by selecting Edit from the menu.

Operations in the ladder write (insert/overwrite) mode.



(1)



COMMENT

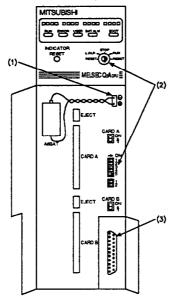
Press [F10] (Undo) key in the ladder write (insert/overwrite) mode during a cut or paste operation to revert to the previous status (to change the cut or paste destination). Only the previous operation can be undone. The undo operation is not possible after conversion. No undo function is available for cut and paste operations on units of ladder blocks in the ladder read mode.

3.5 Operating the CPU for Monitoring and Testing

After setting the switches and internal clock, the program created in Section 3.4.2 is written to the CPU and the CPU is operated to monitor and test the program.

3.5.1 Preparations for operating the CPU

Connect the connectors and set the switches (1) to (3) in the diagram below.



(1) Connect the battery

The battery is unconnected when the CPU is shipped from the factory. Connect the battery, ensuring that the polarity (positive and negative) is correct.

(2) Set the switches

The system setting DIP switches and the RUN/STOP key switch have to be set.

(a) Setting the system setting DIP switches

Set DIP switch #4 to the OFF position.

DIP switch #4 disables writing to the CPU and control instructions.

It must be set OFF to remove this protection.

All other DIP switches can be in either position.

(b) Setting the RUN/STOP key switch

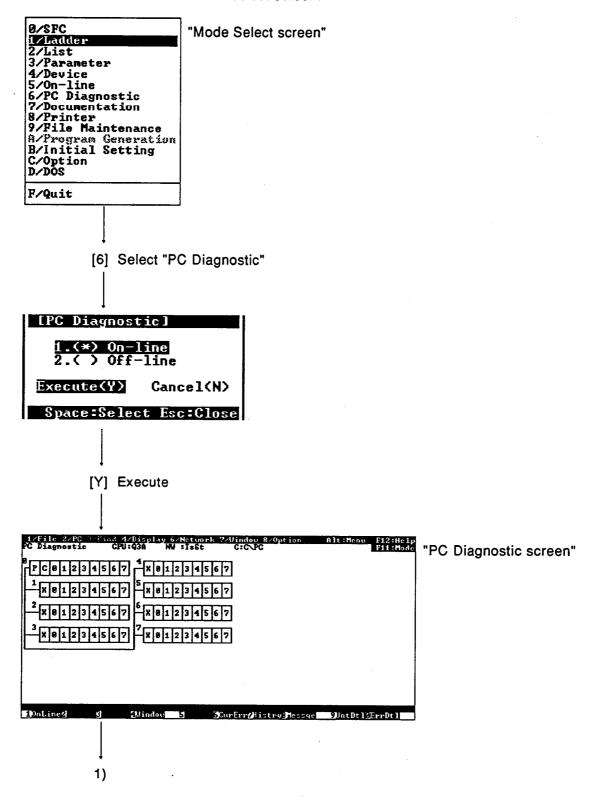
Set to the STOP position.

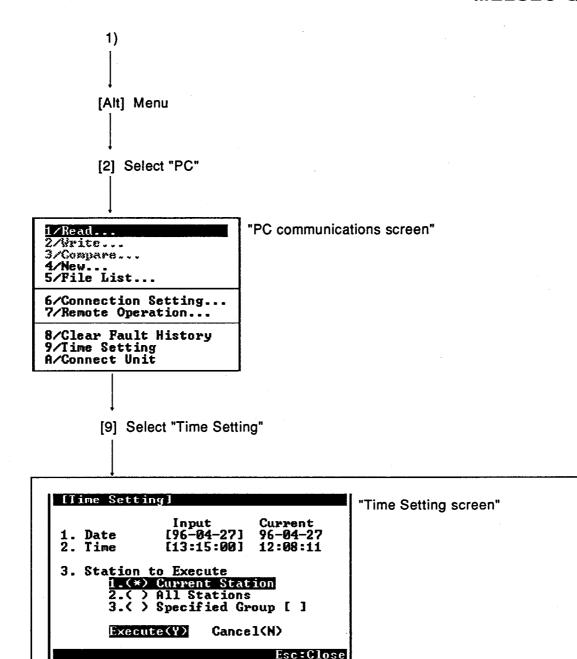
(3) Connect the RS-422 cable

Connect the cable to the RS-232C/RS-422 converter connected to the IBM PC/AT RS-422 connector.

(4) Set the QnACPU internal clock

Set the QnACPU internal clock to the current time using operations from the mode select screen.





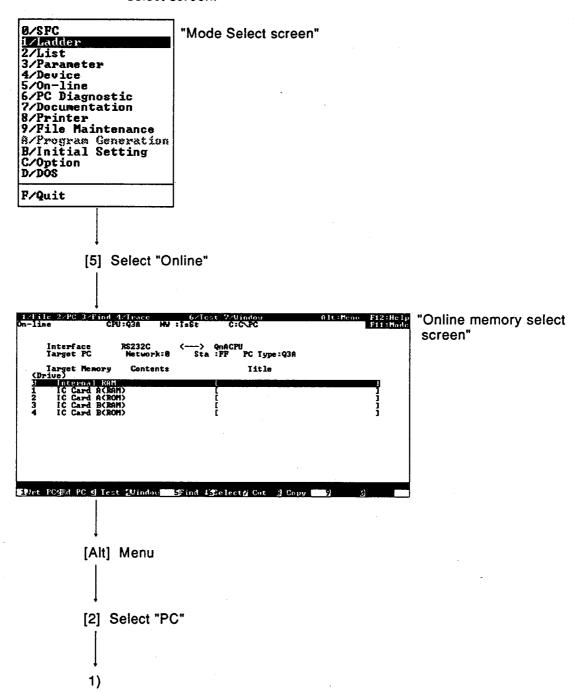
Setting the date [SP] [9] [6] [-] [0] [4] [-] [2] [7] [Enter]

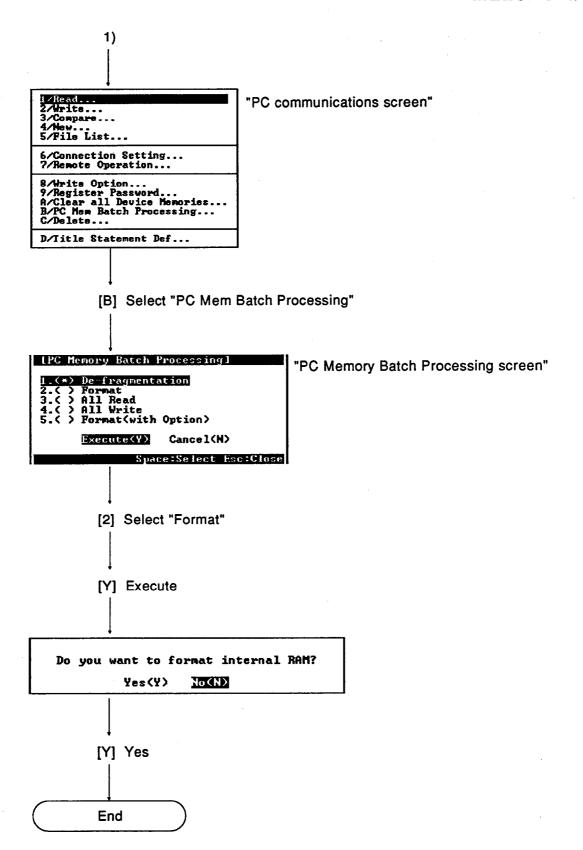
Setting the time [SP] [1] [3] [Shift] + [:] [1] [5] [Shift] + [:] [0] [0] [Enter]

Set the time and date for the current designated station (connected PC). [SP] ([1]) [Y]

(5) Format QnACPU internal RAM

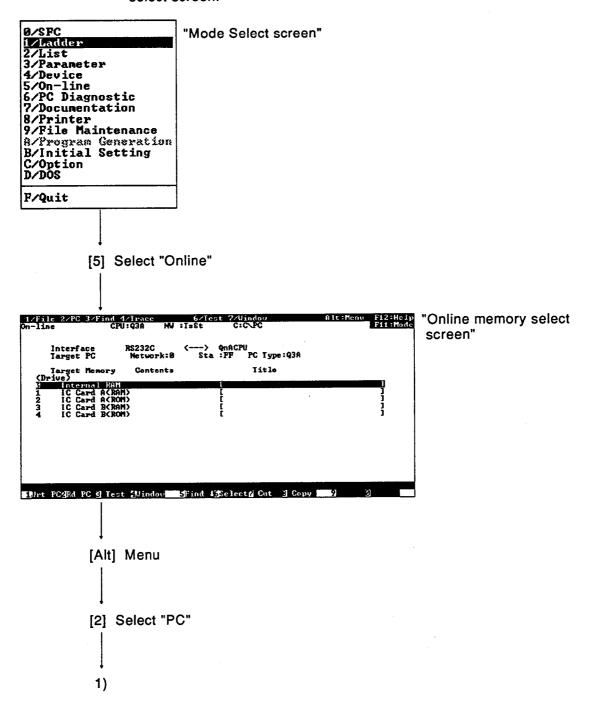
Format the QnACPU internal RAM using operations from the mode select screen.

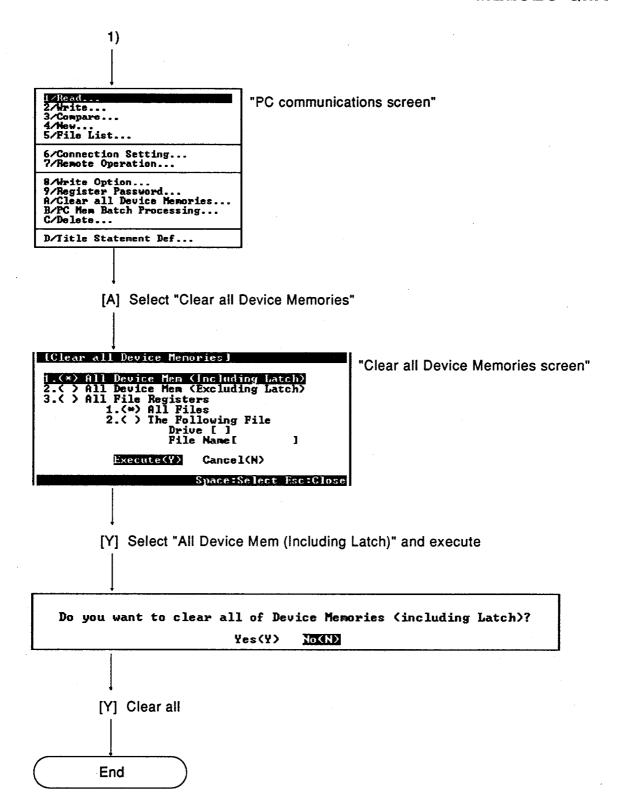




(6) Clear the QnACPU latch devices

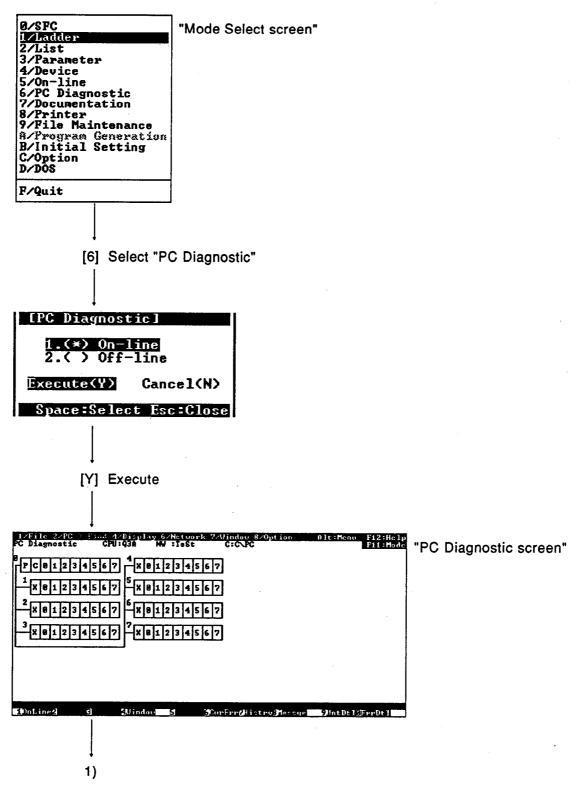
Clear the QnACPU latch devices using operations from the mode select screen.

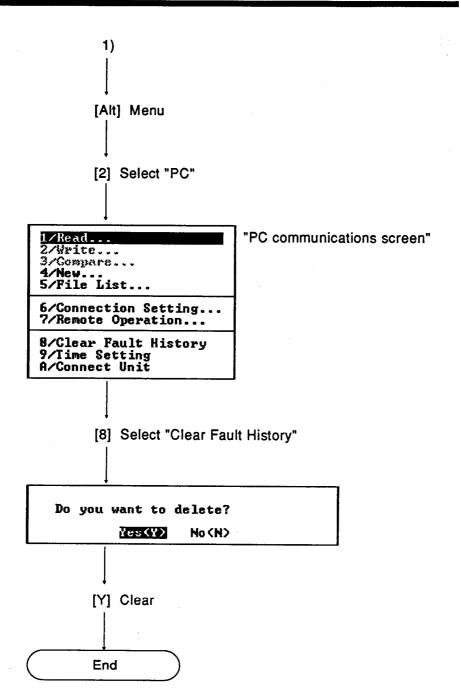




(7) Clear the QnACPU fault history

Clear the fault history data in the QnACPU internal RAM memory using operations from the mode select screen.

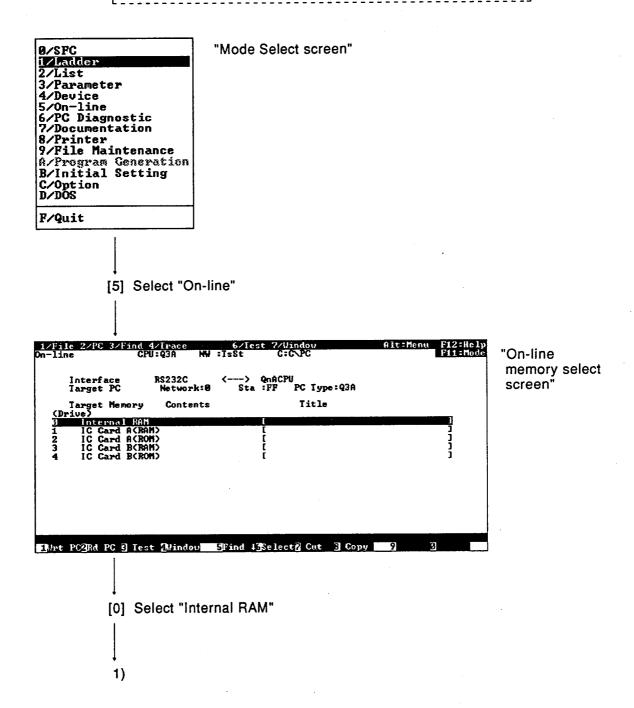


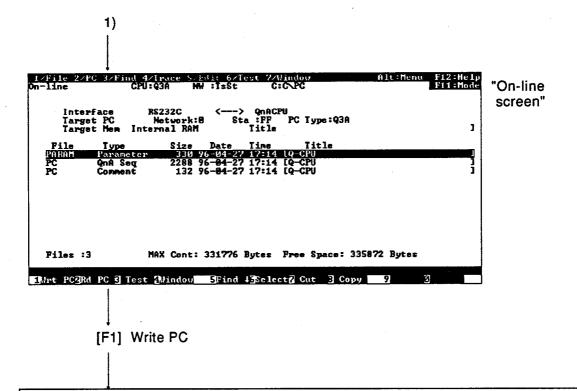


3.5.2 Writing program to CPU

Write the program created in Section 3.4.2 plus the comments created in Section 3.4.3 to the QnACPU using operations from the mode select screen.

Set the QnACPU RUN/STOP key switch to the STOP position.







"Write to PC screen"

- Write destination is internal RAM.
- The PC selected with "Create..." is automatically displayed as the file name.
- The default values are automatically set for the write data parameters and sequence program (Whole Range).
- Add device comments as write data and write them to the QnACPU.
 [SP] [↓] [SP] [3] [Y]

A message is displayed in the message field during writing to the PC and another message indicates when the write operation is complete.

Return to the online memory select screen

POINT

The QnACPU will not run when the key switch is set to the RUN position immediately after a new program is written to the QnACPU. The program is first checked and a message is displayed, as follows:

Q2A(-S1) RUN LED flashes

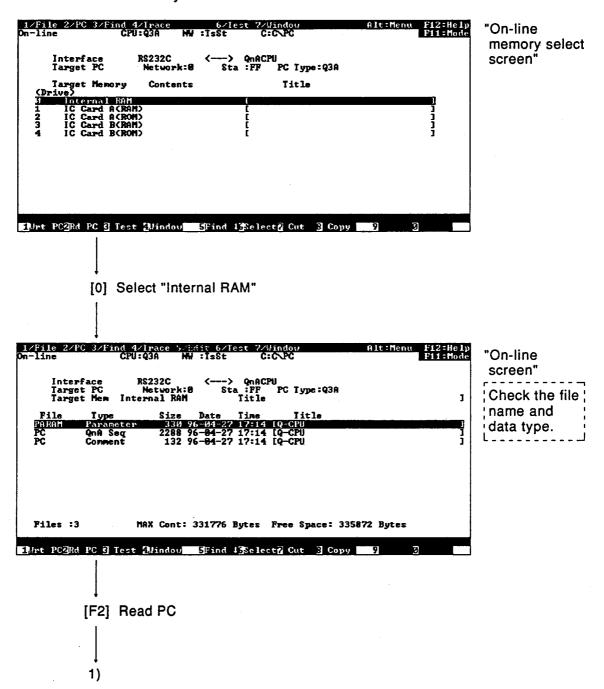
Q3A, Q4A "PRG.CHECK!!" displayed

First set to RESET, then to RUN.

3.5.3 Operating the CPU for monitoring

Read the program (written in Section 3.5.2) from the QnACPU before operating and monitoring the QnACPU.

(1) Read the program from the QnACPU using operations from the online memory select screen.



1)



"Read from PC screen"

Setting the file name as "PC". [SP] [SP] [P] [C]

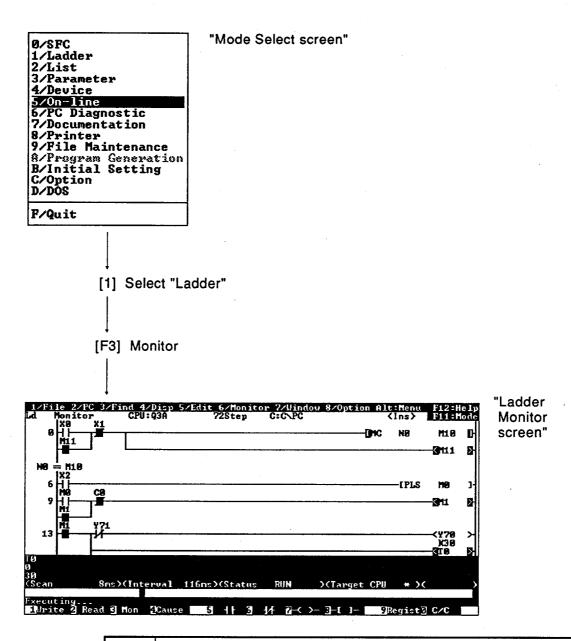
Read program from QnACPU, excluding device initial values and file register data. [Tab] [Tab] [SP] [4] [5] [Y]

A message is displayed in the message field during reading from the PC and another message indicates when the read operation from QnACPU is complete.

Return to the online memory select screen

Monitor QnACPU using ladder monitor operations from the mode select screen.

Set the QnACPU RUN/STOP key switch to the RUN position.



POINT

Use the registered device monitor to monitor the contents of a word device which is not displayed on the ladder monitor screen. The example below monitors the counter Co.

Registered device monitor: [F9] [SP] [C] [0] [Enter] [SP] ([1]) [Y]

(Registration)

(Counter CO) (16-bit)

Actual System Operation

Watch the ladder monitor screen and confirm the following operations.

- 1) Turn ON X0 to enable the master control program (from MC to MCR) and permit actual system operation. (enable conveyor operation)
- 2) Y70 (buzzer) lights for 3 seconds after X2 (workpiece sensor) turns ON, then Y71 lights (start conveyor).
- After X3 is turned from OFF to ON six times (6 products moved to workpiece), Y71 goes out (stop conveyor).
 X2 (no workpiece detected) turns OFF.
- 4) Repeat steps 2) and 3) (equivalent to cycle operation).
- 5) Press X1 to disable the master control program (from MC to MCR) and end actual system operation. (stop conveyor operation) Operation cannot be restarted after X1 is pressed during operation. Repeat the operations from step 1) (enable conveyor operation).

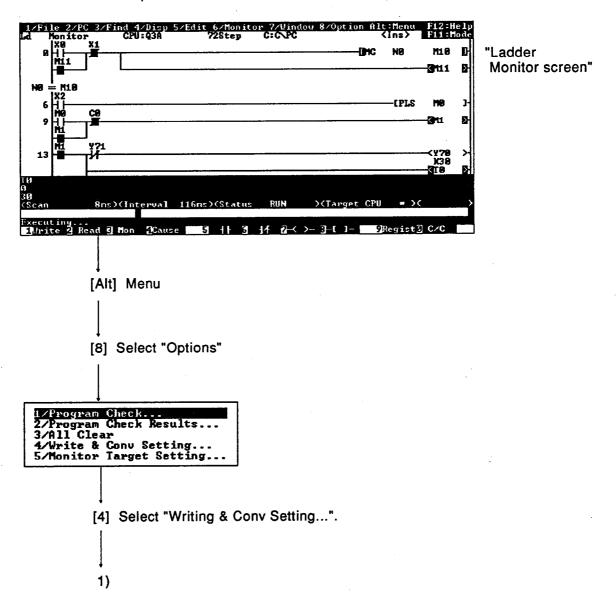
3.5.4 Modifying program while monitoring CPU operation (online program change (write during RUN))

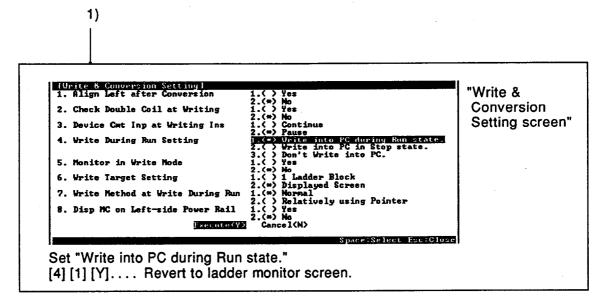
Use online program change (write during RUN) to modify a program written to the QnACPU.

As an example, the timer T0 set value K30 (3 seconds) is changed to K60 (6 seconds).

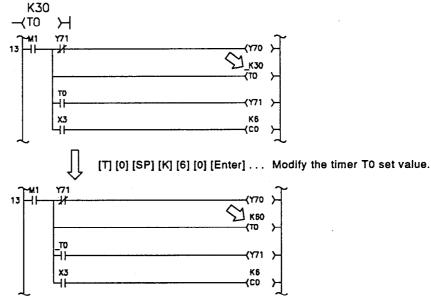
Before modifying the program, set the "Write & Conv Setting...".

(1) Set the "Write & Conv Setting..." to "Write into PC during RUN state.". Operations from ladder monitor screen (similar to Section 3.5.3 (2)).





- (2) Modify program
 Use the following operations to modify the program.
 - (a) [F1] [Insert]... Set the ladder monitor screen to the write (Write) (Overwrite) mode.
 - (b) Press the cursor keys to move the cursor immediately in front of



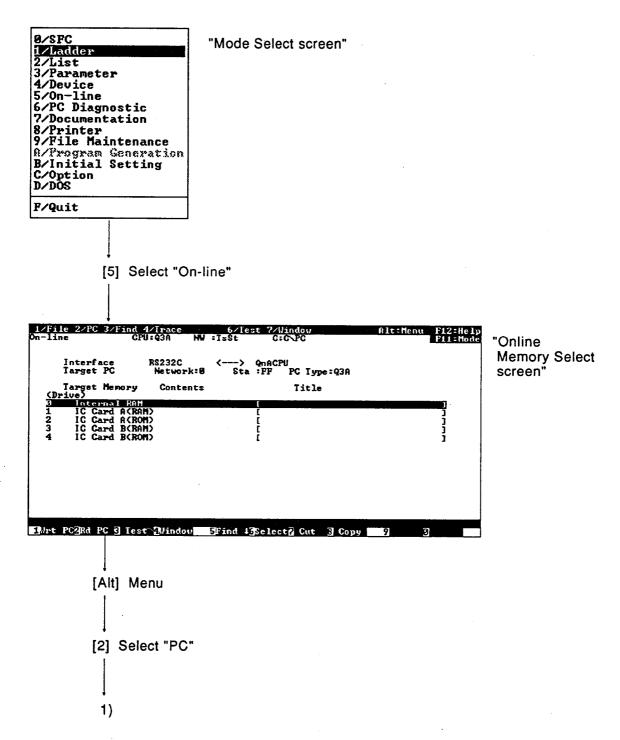
(c) Press [F4] key to convert the program.
(Convert)

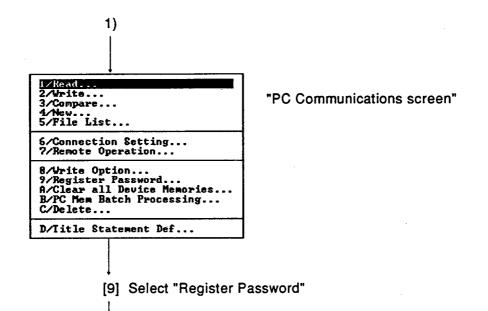
A message is displayed in the message field during conversion. Another message indicates that the operation is complete and the step number was changed "Write into PC during RUN state." ends.

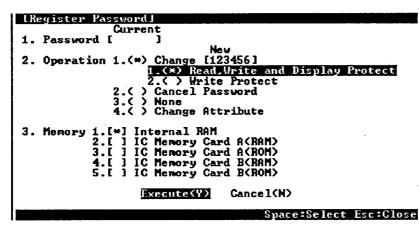
(3) Check actual system operation
Follow step (2) described in Section 3.5.3 (2) "Actual System Operation"
to check that the time Y70 lights has changed from 3 seconds to 6 seconds.

3.5.5 Registering a password

A password can be registered to protect access to the programs in the QnACPU. It can inhibit reading, writing, and display or be used to write protect the programs so that they cannot be accidentally overwritten. Operation from the mode select screen.







"Register Password screen"

The password can be designated as up to 6 alphanumeric characters.

Stop the QnACPU

Register the password "123456" in the QnACPU to inhibit reading, writing, and display (default) for the internal RAM memory (default).

[2] [1] [2] [3] [4] [5] [6]

[↓]

[Y]

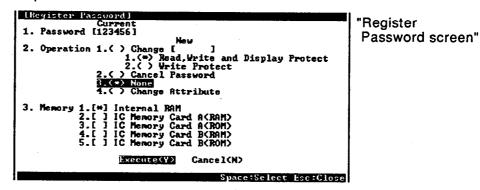
(New) (Password: 123456) (

(Read, Write and (Execute)
Display Protect)

A message is displayed in the message field when the registration operation is complete.

POINTS

- (1) Check the password operation after temporarily quitting the IBM PC/AT GPP functions. They password does not become effective until the GPP functions are quit.
- (2) Follow the procedures below to initially set the password to access a QnACPU for which an password has been set. Repeat the operations on the previous page to display the register password screen.



Set the password and select "None"

```
[1] [1] [2] [3] [4] [5] [6]
(Current) (Registered password: 123456)
[↓] [SP] [↓] [↓] [↓] [↓] [SP] [Y]
(Operation) (Move cursor to "None") (Select) (Execute)
```

A message is displayed in the message field when the operation is complete.

If the password is incorrect, a message indicates that the file cannot be accessed and file access is not permitted.

(3) If the password is completely forgotten, the PC memory must be formatted, by the method described below.

```
[PC communications screen]

[B] [2] [Y] [Do you want to format internal RAM? [Y]

(Batch operation)(Format) (Execute) (Yes)
```

A message is displayed in the message field when the formatting of the QnACPU internal RAM is complete (the memory contents have been cleared).

3.5.6 Checking responses to forced faults (PC diagnosis)

The output module is forcibly removed during QnACPU operation to simulate the AY42 (64-point) output module falling out of the PC CPU system in Section 3.1.

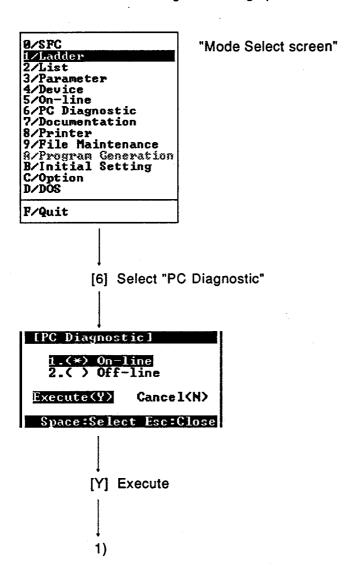
(1) Check QnACPU error indicator flashing and error message display

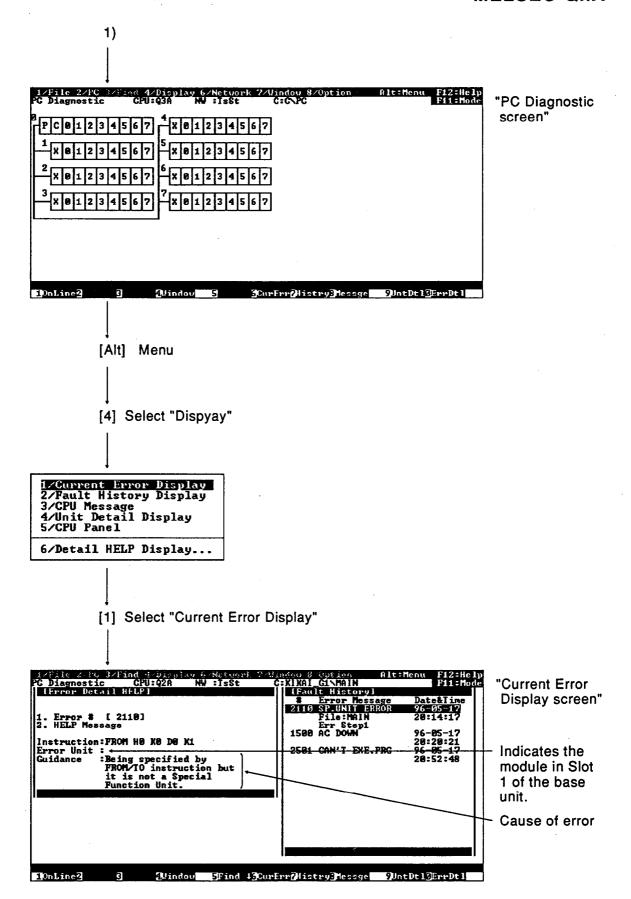
Check that the error indicator flashes and that the following message appears in the message display window:

Display message: UNIT VERIFY ERR.

(2) Determine cause using PC diagnosis

Follow the procedure below to determine the cause of the error with PC diagnosis using operations from the mode select screen.





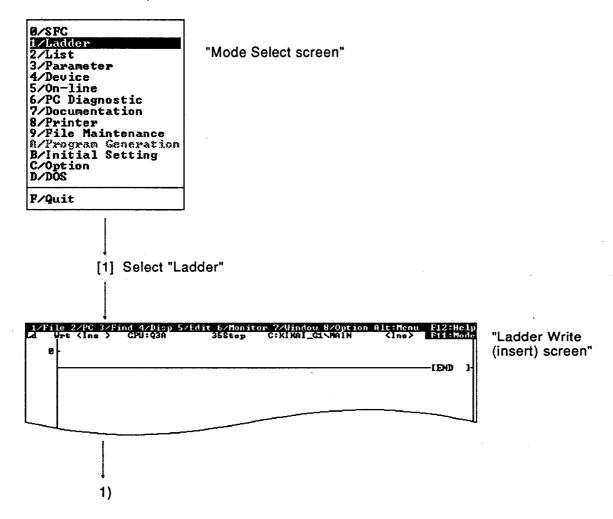
(3) Checking the module causing the I/O module verification error

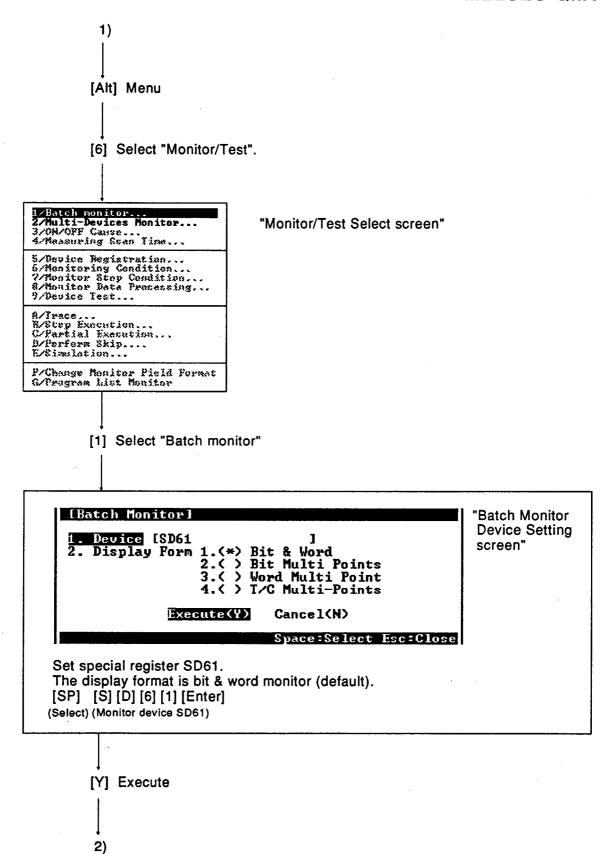
The PC diagnosis indicated the cause of the error as "Being specified by FROM/TO instruction but it is not a Special Function Unit." (unit verification error). Next, monitor the following special registers to determine the module causing the error.

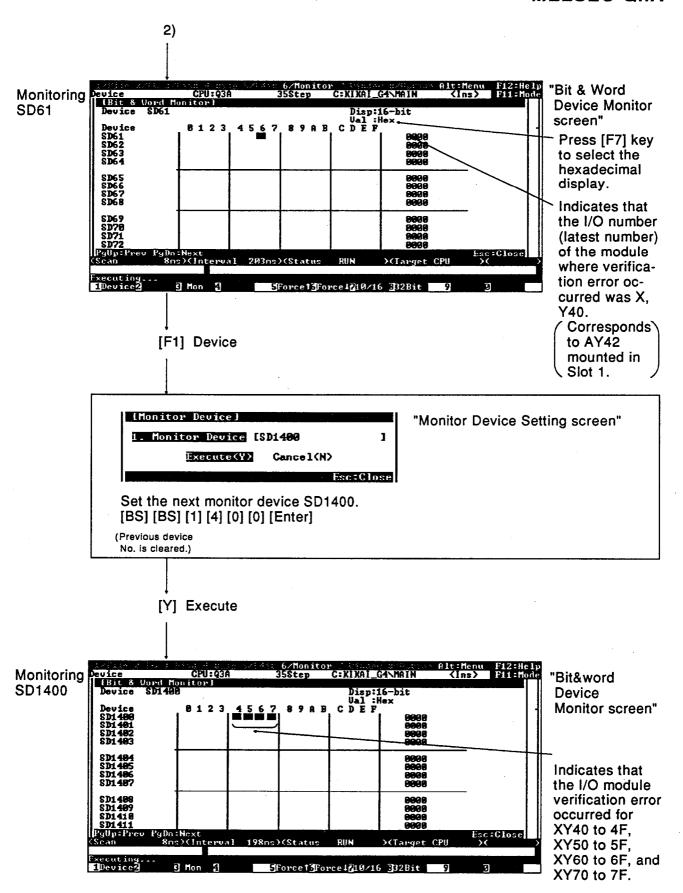
Special Register	Name	Function
SD61	I/O module verification error module number	Stores the I/O signal (latest number) of the module where the I/O module verification error occurred.
SD1400 to SD1431	I/O verification table	Stores the I/O module verification error information in 16-bit units as a bit pattern. 0: no error 1: error

Monitor SD61, then SD1400.

Operate from the mode select screen.





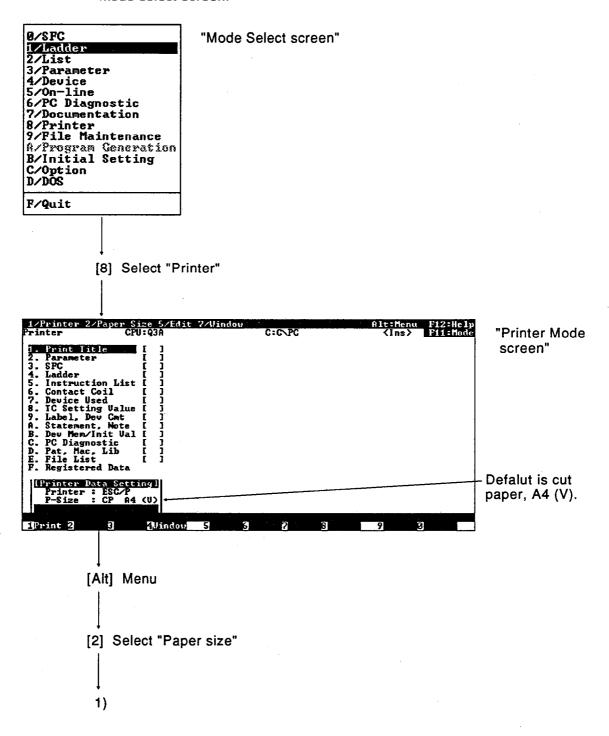


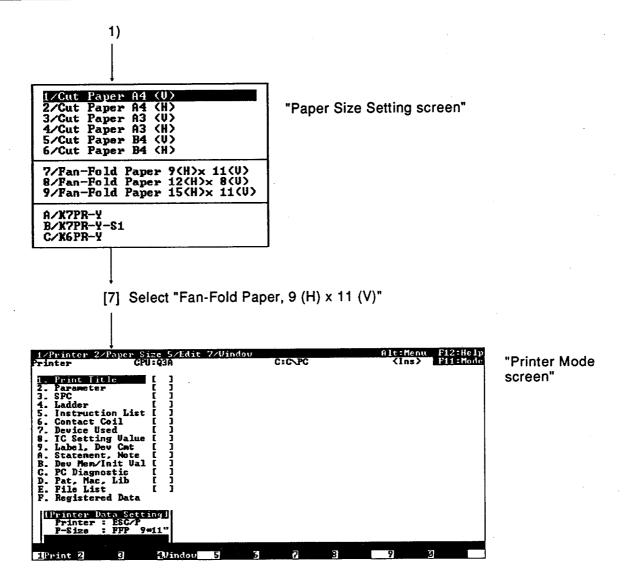
3.6 Printing the Program

Print out the program created in Section 3.4.2 plus the comments, machine names, and contact/coil destinations created in Section 3.4.3 on a PC-PR201H printer.

3.6.1 Setting the printer and paper size

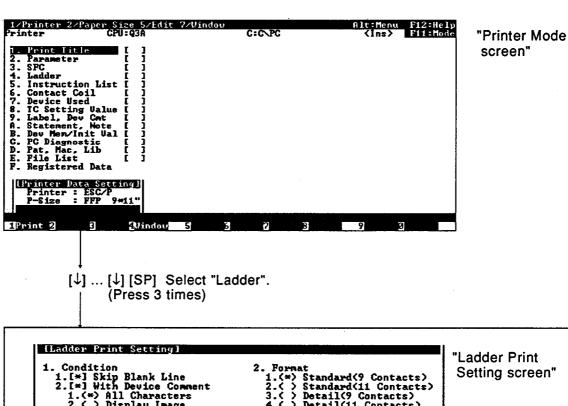
Set the type of printer used and the paper size using operations from the mode select screen.

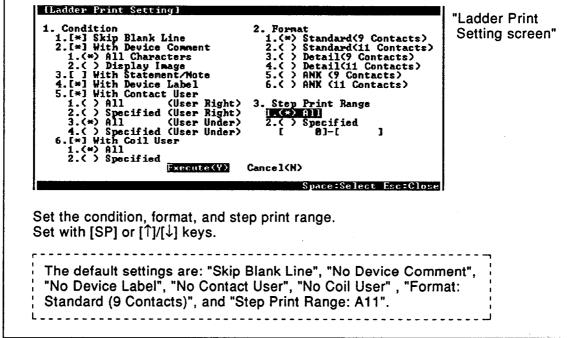




3.6.2 Printing out the ladder program

Select "Ladder" as the print item, then set the print format, including printing conditions, printing method, whether headers and footers are added, and the print start position, using operations from the printer mode screen.





1)

```
1)
         [F1] Print
  [Printing]
                                                                                       "Printing screen"
   1.(*) - ** -

1.(*) - ** -

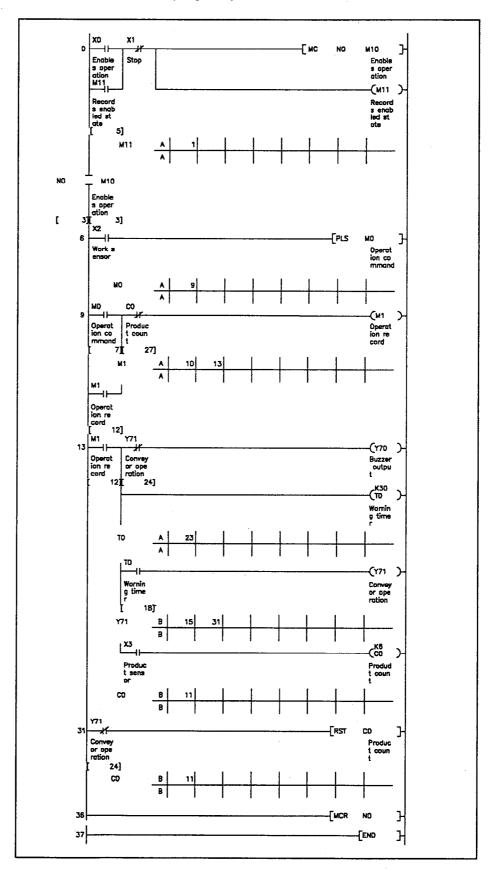
2.( ) ** - **

3.( ) Block $ - **

4.( ) No Printing
                                   Initial[ 1]
Initial[ 1]-[ 1]
Initial Value Block # -[ 1]
   2. Header
1.< > Common
2.< > Per Item
3.<*> No
                           4. Print Inter-line Space 6. Left Margin [ 6]Character 2.(*) No
                                                              7. Top Margin [ 2]Row
                          5. Open New Page
1.(*) Yes
2.( ) No
   3. Footer
1.( > Common
2.( > Per Item
3.(*) No
                            Execute(Y)
                                             Cancel(N)
                                                      Space:Select Esc:Close
 Set the print execution conditions. Set the left and top margins only, leaving
 other items as default values.
 Set left margin.
 [6] [SP] [Enter]..... 6 characters
 Set top margin.
 [ ] [SP] [2] [Enter].... 2 rows
   The default settings are: "Page Type = -**-", "Header: No",
   "Footer: No", "Print Inter-line Space: No", "Open New Page: Yes".
        [Y] Print
Executes printing. All right?
           Yes(Y)
                       No (N)
         [Y] Yes ... Start printing
```

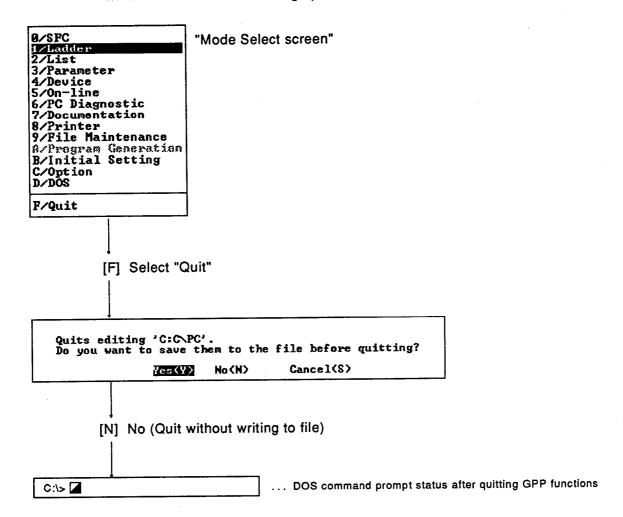
3.6.3 Sample ladder program print-out

An example of a ladder program print-out is shown below.



3.7 Quitting GPP Functions

Quite the GPP functions using operations from the model select screen.



4. MAKING THE MOST OF YOUR QNACPU (1)

This chapter describes methods to make the best use of your QnACPU, which differ from the conventional ACPU operations.

4.1 System Configuration

The system configuration shown below is used for the descriptions in this chapter.

		X00 to X0F	X10 to X1F	Y20 to Y2F	Y30 to Y3F	X/Y40 to X/Y5F	X/Y60 to X/Y7F
A62P	QЗАСРU	AX40	AX40	AY40	AY40	A68AD	A62DA

Memory card: Q1MEM-512SE

4.2 Programming for Individual Control Processes

The QnACPU handles sequence programs as files, so that sequence programs can be divided up into control processes for program design. This section describes this method of programming.

4.2.1 File structure

(1) Program execution can be matched to the type of control.

Programs can be selected from four types: initial execution, scan execution, low-speed execution, and standby.

These programs can be set to run when required, thereby reducing the scan time of the permanently executing scan program.

(a) Initial execution (initial program)

A program executed once only when the QnACPU is set to RUN. Used for initialization.

(b) Scan execution (scan program)

A permanently executing program.

Equivalent to a conventional program running from step 0 to END.

Can include sub-routine programs and interrupt programs.

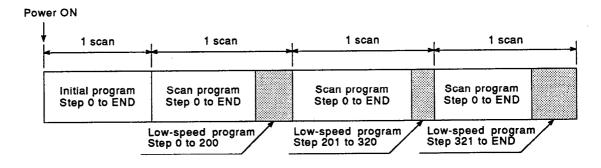
(c) Low-speed execution (low-speed program)

A program which is not entirely executed each scan but which is executed over several scans, using the constant scan surplus time or a set time.

Used for programs run at low frequency, such as scheduled inspection programs.

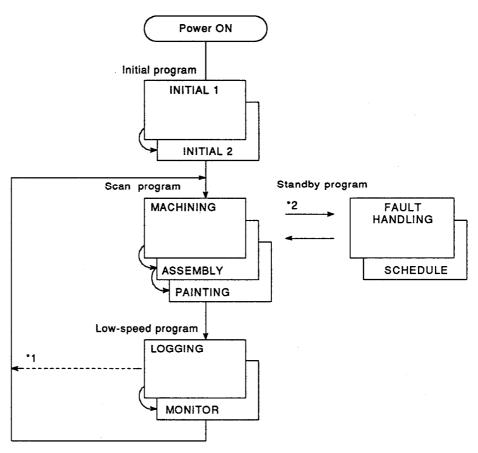
(d) Standby (standby program)

A program which is executed when a certain start condition is met, such as a sub-routine program or an interrupt program. Used when the sub-routine programs and interrupt programs are held as library data and are handled separately to the main program.



(2) File Management

The sequence program and other data handled by the QnACPU is managed as files. Whereas programs were conventionally handled as main programs and sub-programs, the QnACPU allows structured programming, such that each type of program can be broken down for individual programmers or processes.



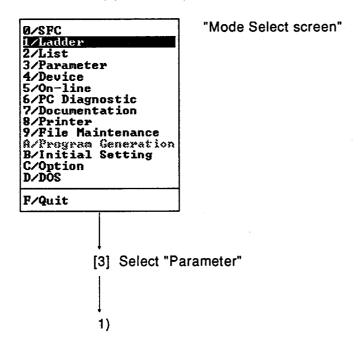
- *1 If constant-scan or low-speed program execution time is set. *2 On request

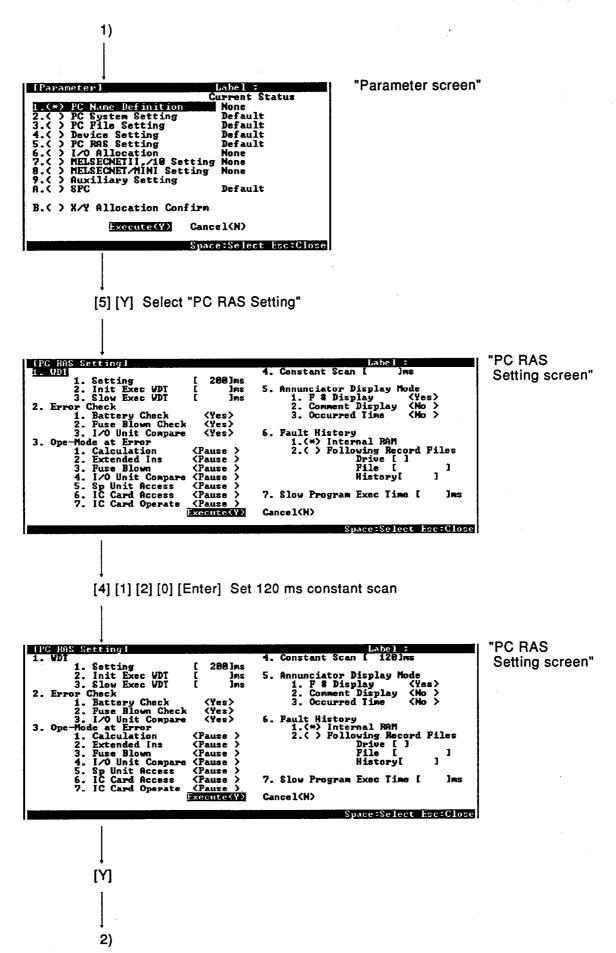
4.2.2 Sample program

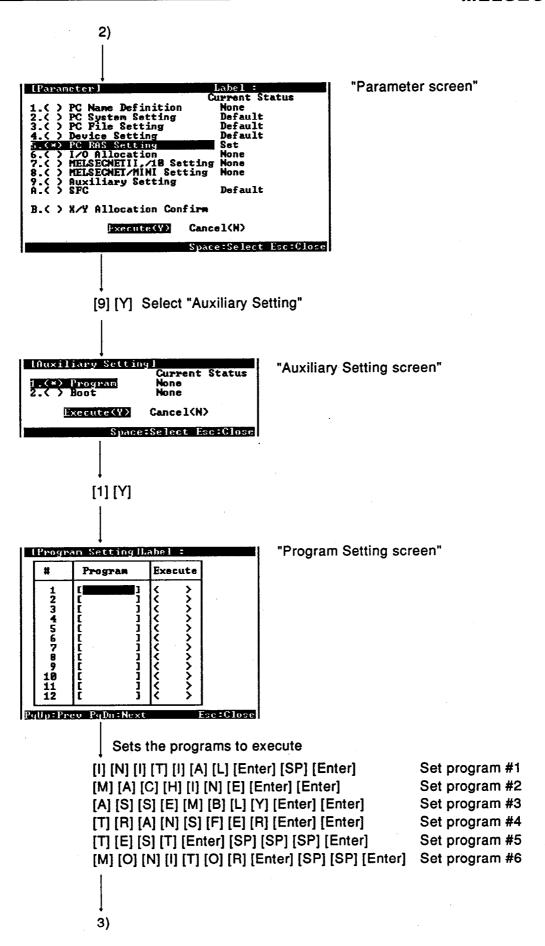
The example below executes the files listed in the table.

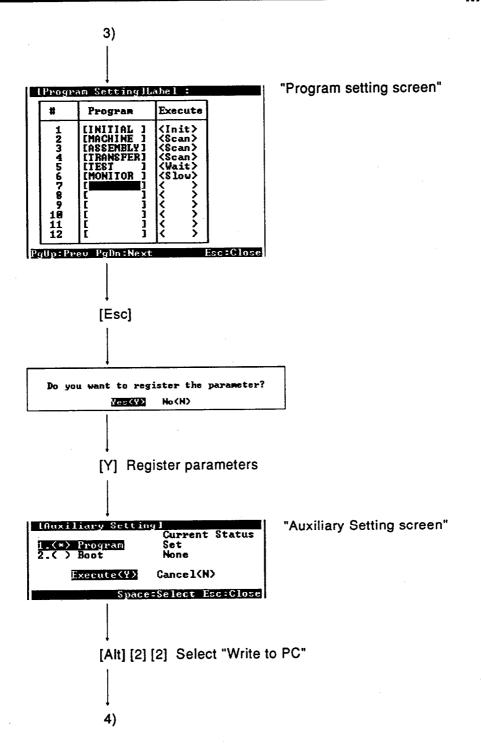
File Name	Туре	Description			
INITIAL	Initia	Program to set the initial data.			
MACHINING	Scan	Program for machining.			
ASSEMBLY	Scan	Program for assembly.			
TRANSPORT Scan		Program for transporting.			
INSPECTION	Standby	Program for inspection.			
MONITOR	Low-speed	Program for monitoring.			

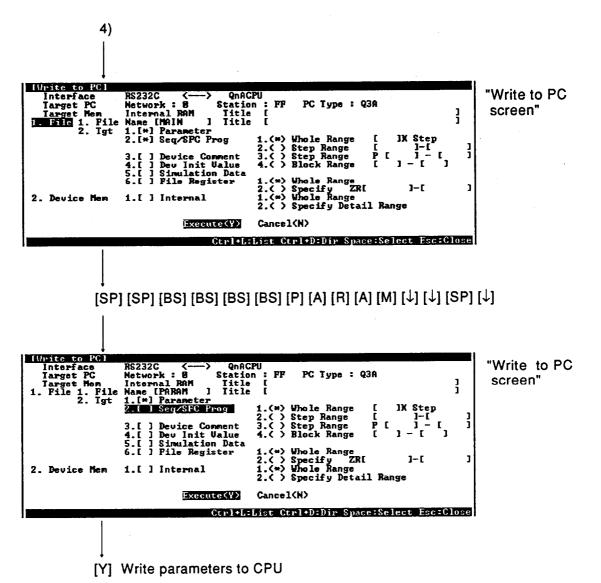
- Create each program and write it to the CPU.
 Set the RUN/STOP key switch to STOP.
 See Chapter 3 for details on programming and writing to the CPU.
- (2) Set the parameters to execute multiple files, and write them to the CPU.











(3) Execute the programs

Turn the CPU key switch from "STOP" to "RESET" to "RUN".

*1 If the key switch is turned from STOP to RUN.

The CPU displays a message, as follows, and checks the program.

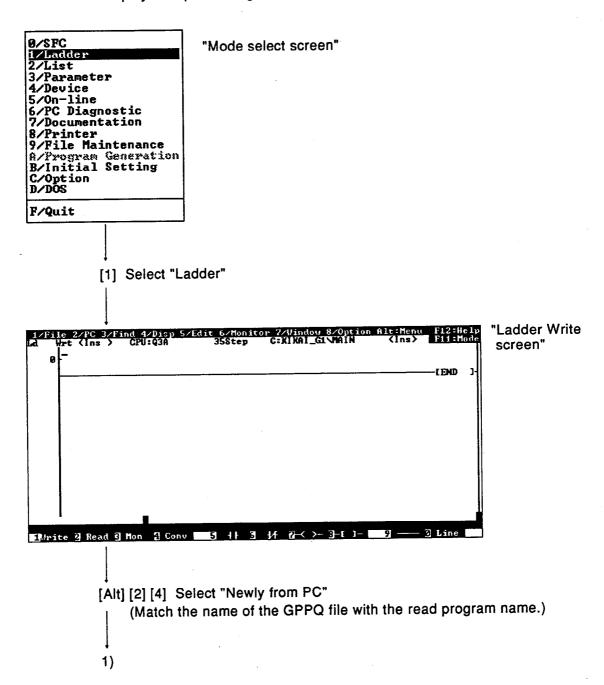
Q2ACPU, Q2ACPU-S1: RUN LED flashes

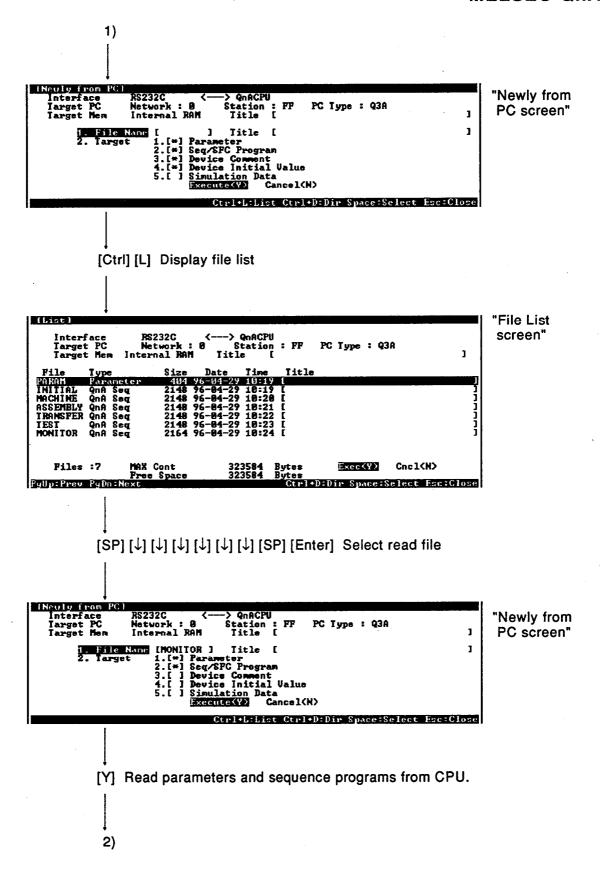
Q3ACPU, Q4ACPU : "PRG. CHECK!!" displayed

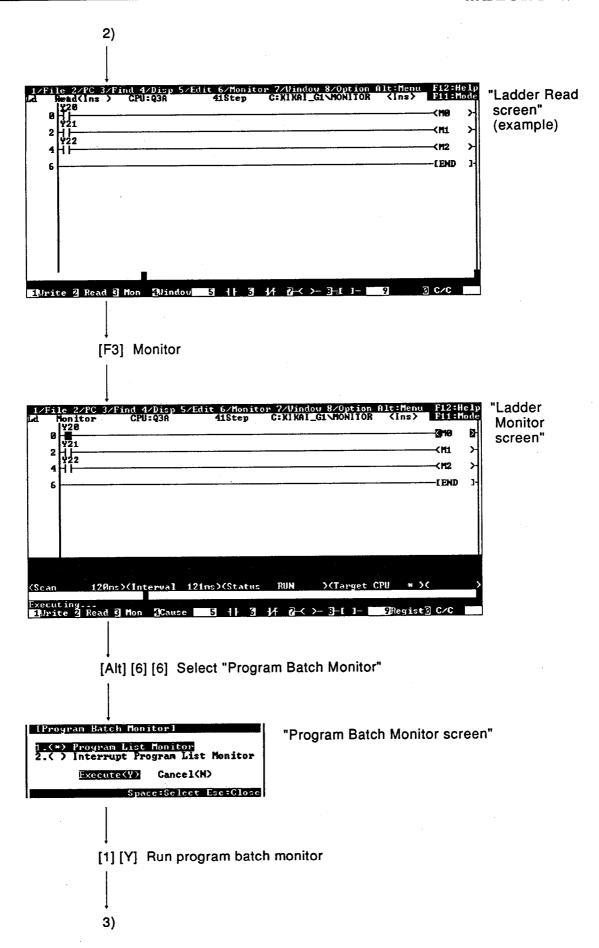
The CPU runs the programs when the key switch is set from STOP to RESET to RUN.

4.2.3 Program list monitor

Displays the processing time for the program executed in Section 4.2.1







3)

PyUp:Prev PyDn:Next

IProgram List Monitorl ⟨Total Scan Time⟩ ←(a) ⟨Program Status⟩ ←(c) Mon Time Max Scan Program Scan Time Ex Times 8.100ms 8.100ms 8.100ms 8.100ms 8.000ms 1 x 1490 x 1490 x 1490 x 0 x 0 x 0 x 0 x 0 x INITIAL MACHINE ASSEMBLY TRANSFER 200ms 128.990ns 128.990ns 8.290ns Scan Init Slow Init Scan Scan 241 241 Scan Wait Slow Wait Wait 8.000ms 8.000ms 8.000ms 8.000ms 8.000ms (Time Details / Scan> ←(b) TEST MONITOR 0.300ms 119.700ms 110.600ms 112.200ms Program END Proc Time Wait Wait Wait Slow Prog Wait for Con

"Program
List Monitor
screen"
(Displayed
values for this
example only.
Actual values
may vary.)

Key to the Screen

(a) "Total Scan Time"

Displays the watchdog times set with "5. PC RAS Setting" in the parameter mode and the total scan time for each program type.

1) "Mon time"

Displays the watchdog times for the scan programs, initial programs, and low-speed programs.

Esc:Close

A watchdog error occurs if displayed scan time exceeds the CPU watchdog time.

2) "Max Scan"

Displays the total of the times displayed in "Time Details / Scan".

(b) "Time Details / Scan"

Displays the scan time details.

1) "Program"

Displays the total execution time for the scan programs.

2) "END Proc Time"

Displays the END processing time.

3) "Slow Prog"

Displays the total execution times set for low-speed programs, if low-speed program execution times are set.

4) "Wait for Con"

Displays the constant scan wait time if constant scan is set. However, "0.000 ms" is displayed if a low-speed program execution time is also set.

(c) "Program Status"

Displays the execution status of the programs selected with "9. Auxiliary setting" in the parameter mode.

1) "Program"

Displays the program names in the order of the set parameters.

2) "Exec"

Displays the type of program set with the parameters.

3) "Scan Time"

Displays the actual scan time (present value). The scan time is displayed as "0.000 ms" when program operation is stopped (standby status).

4) "Ex Times"

Displays the number of executions as a value from 0 to 65536, starting from zero when measurement was started. The number of executions value is held when program operation is stopped.

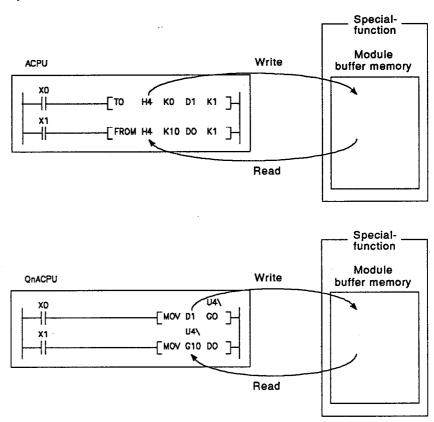
4.3 Easy Programming

4.3.1 Direct access of special-function module buffer memory as devices

The ACPU used FROM/TO instructions for buffer memory read and write operations.

The QnACPU simplifies sequence programs by allowing direct access of a special-function module buffer memory as devices.

Example Accessing buffer memory addresses 0 and 10 of the special-function module mounted at X/Y40.



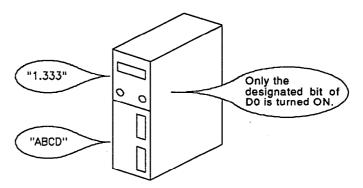
CAUTION

Processing speed with direct access is the same as with the FROM/TO instructions.

Frequently used buffer memory contents should be temporarily moved to a data register to increase processing speed.

4.3.2 Accessing word devices as bit units and using differential contacts

Bit designation can be used for real numbers, character strings, and word devices.



(1) Bit designation

Handles a part of word device data as a bit device.

```
When D0, bit 3 is ON, R3, bit 5 turns ON.
```

(2) Differential contact

Uses a leading edge or trailing edge as an input device.

4.4 Convenient Functions When Using QnACPU

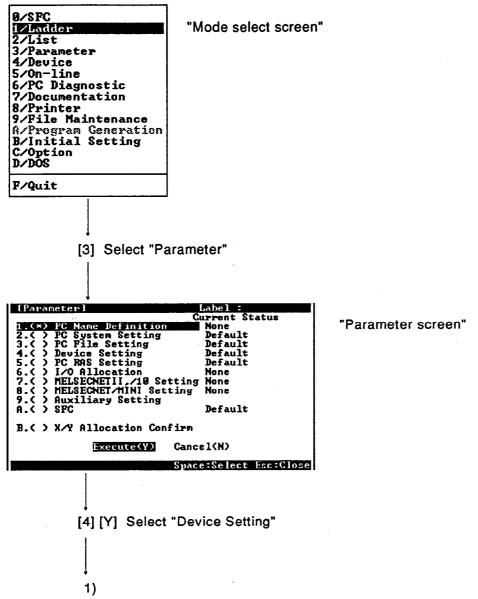
4.4.1 Allocation of optimal device points range for the system used

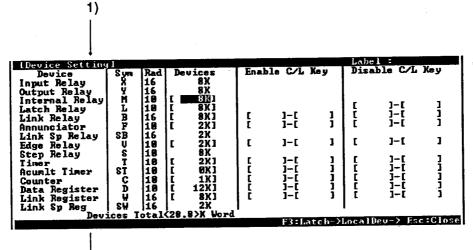
The number of device points was fixed with the conventional ACPU. However, the QnACPU allows allocation of the optimal device points range to suit the system used.

The settings are shown in the table below.

Item	Description	Setting Range	Default Value		
Number of devices	Sets the number of internal device points.	32 k points maximum can be set for one device, in a total range of 29 k words (excluding device X, Y, S).	X : 8 k points (fixed) Y : 8 k points (fixed) M : 8 k points L : 8 k points B : 8 k points F : 2 k points SB : 2 k points V : 2 k points S : 8 k points (fixed) T : 2 k points ST : 0 k point C : 1 k point D : 12 k points W : 8 k points SW : 2 k points		
Latch range (latch clear key en- abled)	Sets the latch range which can be cleared by the latch clear key.	Only one range per device	No setting		
Latch range (latch clear key dis- abled)	Sets the latch range which cannot be cleared by the latch clear key.	Only one range per device	No setting		
Local device range	Set the range of local devices enclosed in the a program.	Only one range per device	No setting		

(1) Change internal relays, M, to 10 k points, and data registers, D, to 1 k points. (D0 to D500: latch clear key enable; D501 to D1023: latch clear key disable)





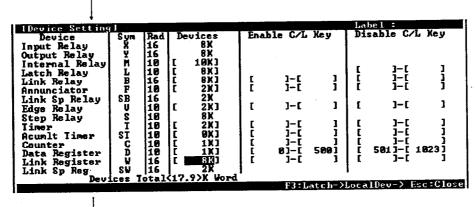
"Device Setting screen (default values)"

[←] [Del] [1] [0] [Enter] Change internal relays (M) from 8 k (default) to 10 k. [↓] ... Move cursor to data register item.

 $[\leftarrow]$ [Del] [Del] [1] Change data registers (D) from 12 k (default) to 1 k. $[\rightarrow]$ $[\rightarrow]$ [0] $[\rightarrow]$ [5] [0] [0] Set latch clear key enable latch range from D0 to

D500.

 $[\rightarrow]$ [5] [0] [1] [\rightarrow] [1] [0] [2] [3] [Enter] Set latch clear key disable latch range from D501 to D1023.



"Device Setting screen (set values)"

Do you want to register the parameter?

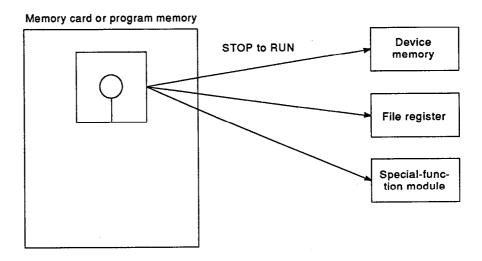
Yes(Y) No(N)

[Y] Register parameter

[Esc]

4.4.2 Setting device values required by the system as initial values

Device initial values preset in the peripheral device can be automatically transferred to the device memory, file registers, and special-function module when the CPU is switched from STOP to RUN. This eliminates the need for a data initial setting program.

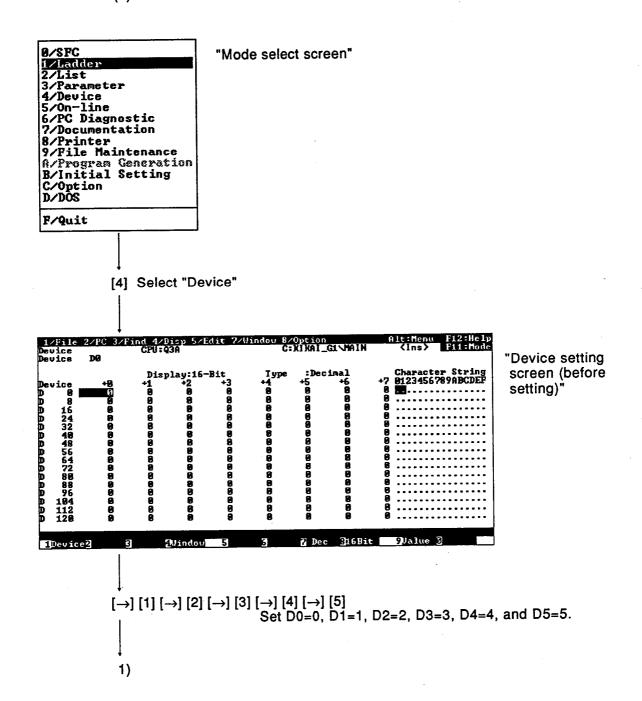


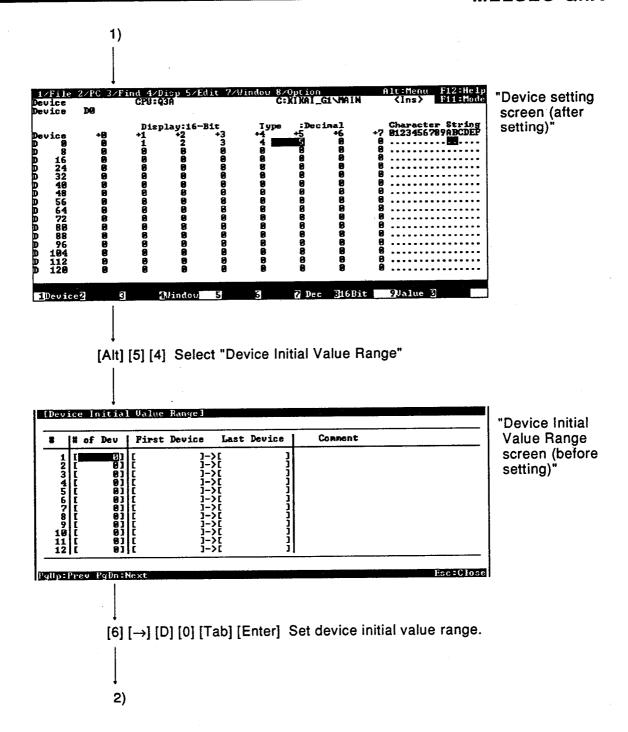
Setting the device initial values

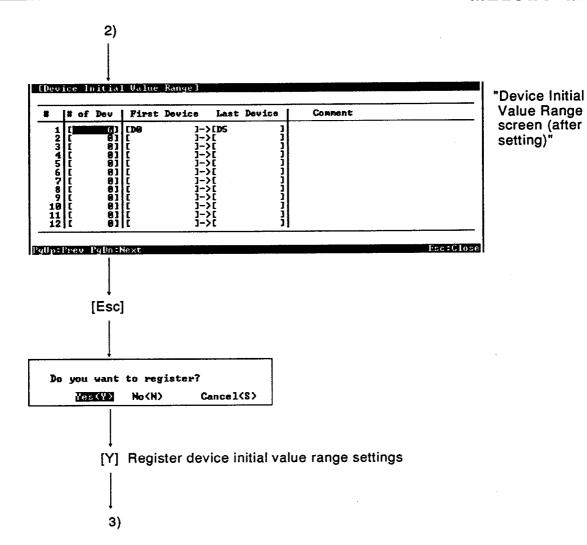
To set the device initial values, the device initial value file must be stored in the CPU program memory or in the memory card. Initial values can be set for the devices listed in the table below.

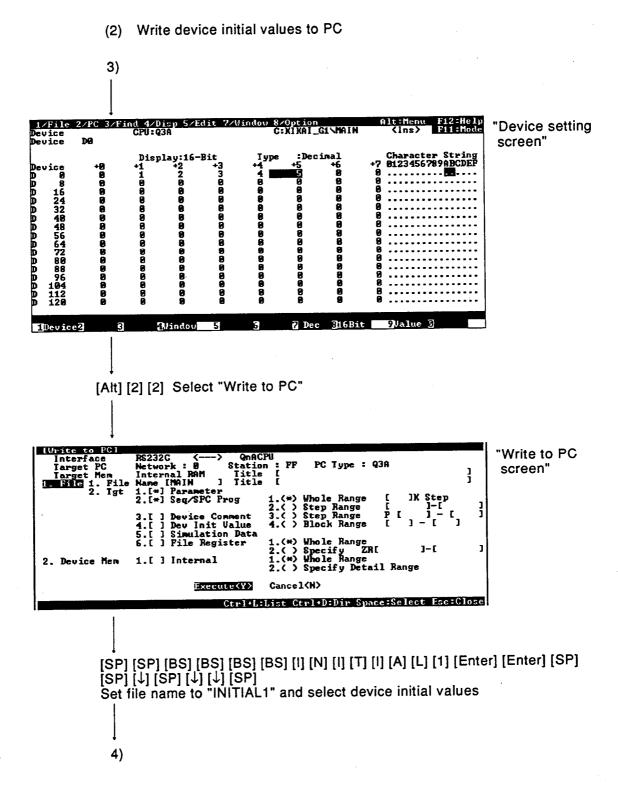
Device Name	Settable/ Not Settable	Device Name	Settable/ Not Settable	Device Name	Settable/ Not Settable	Device Name	Settable/ Not Settable	Device Name	Settable/ Not Settable
X Y M L F S M FX FY V D D Y	x x x x x x x x	T (contact) T (coil) T (present value) C (contact) C (coil) C (present value) ST (contact) ST (coil) ST (present value) D SD	x 0 x 0 x 0 x	FD B SB W SW GR P I N Z	x x x 0 0 x 0 x	SZ S TR BL U J ZR	x x x x x	U /G J /X J /Y J /B J /SB J /SW BL /S BL /TR	0 X X X 0 0 X X

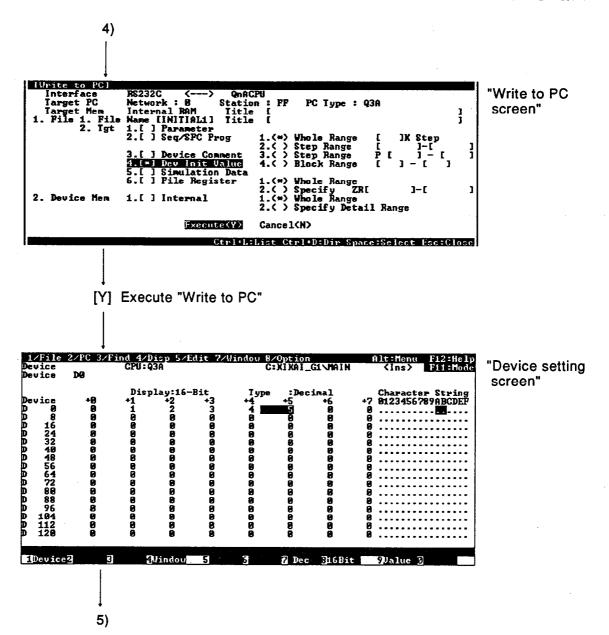
(1) Set device initial values: D0=0, D1=1, D2=2, D3=3, D4=4, D5=5





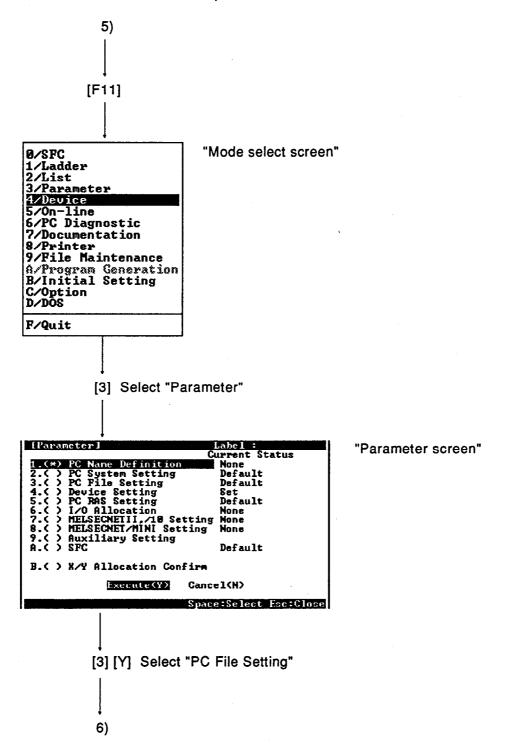


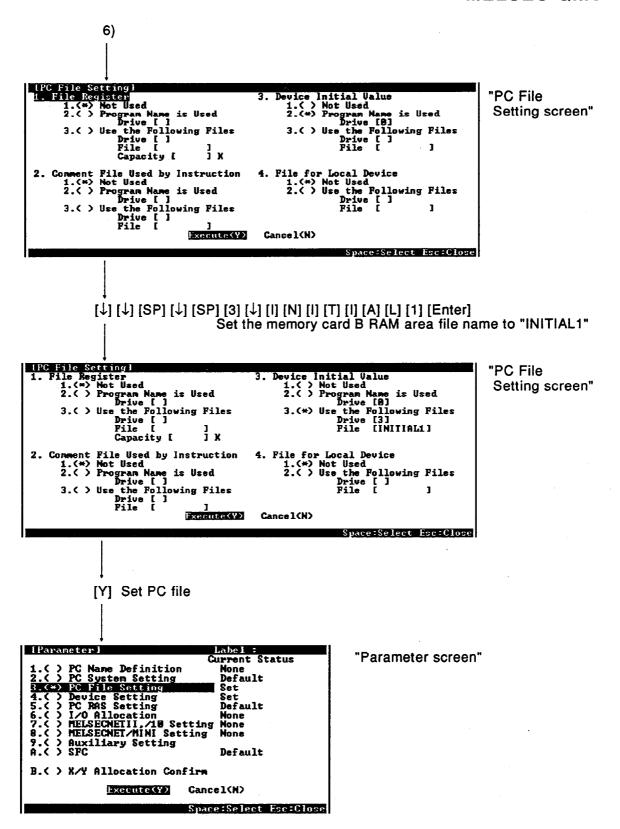




(3) Set parameters

Set with the parameters which device initial value file is effective.





(4) Execute

Device initial values written when CPU is switched from STOP to RUN or when the power is turned ON.

4. MAKING THE MOST OF YOUR QNACPU (1)

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(5) Cautions

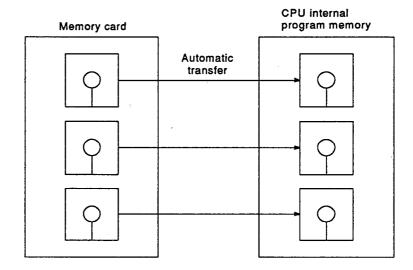
Device initial values take priority when the settings overlap a latch range.

4.4.3 Boot operation with a program transferred from memory card to QnACPU internal memory

A memory card is required for boot operation.

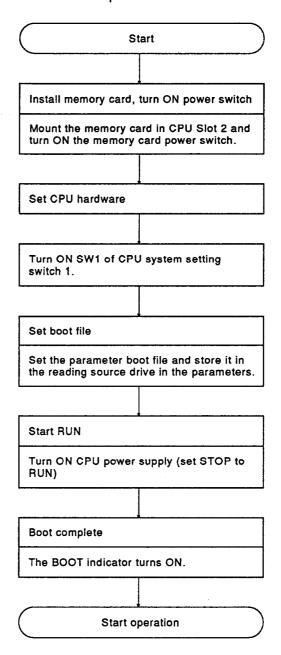
Boot operation is possible by setting the system setting switch and the parameter boot file.

Boot operation is a function for automatic transfer from the memory card to program memory, as defined in the boot file settings, when the power is turned on or the CPU is switched from STOP to RUN.



Procedure to Set Boot Operation

The procedure to set boot operation is shown in the flowchart below.



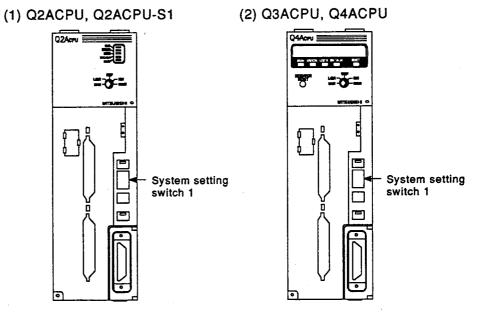
Boot operation is described using the files in the table below.

File Name	Туре	Description
INITIAL	Device initial value	Device initial values
MACHINING	Sequence	Program for machining
ASSEMBLY	Sequence	Program for assembly
TRANSPORT	Sequence	Program for transporting

(1) Set switches

Turn ON SW1 of system setting switch 1.

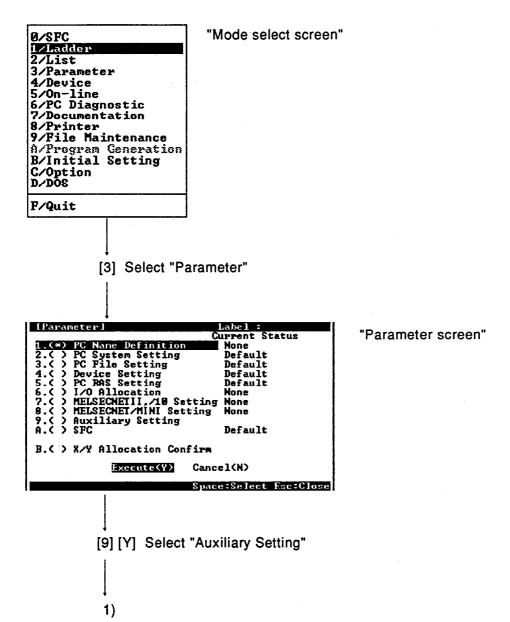
Setting				
Turn	rn ON to enable boot operation			
Desi	gnate para	meter enat	oled param	neter drive
	NO4	NO3	NO2	Storage area
	OFF	OFF	OFF	Internal memory (Drive 0:)
	OFF	OFF	ON	Slot 1 RAM (Drive 1:)
	OFF	ON	OFF	Slot 1 ROM (Drive 2:)
	OFF	ON	ON	Slot 2 RAM (Drive 3:)
	ON	OFF	OFF	Slot 2 ROM (Drive 4:)
	ON	OFF	ON	
	ON	ON	OFF	Do not use
	ON	ON	ON	
		Designate para NO4 OFF OFF OFF ON ON	Designate parameter enab	Turn ON to enable boot operation Designate parameter enabled parameter of the parameter of

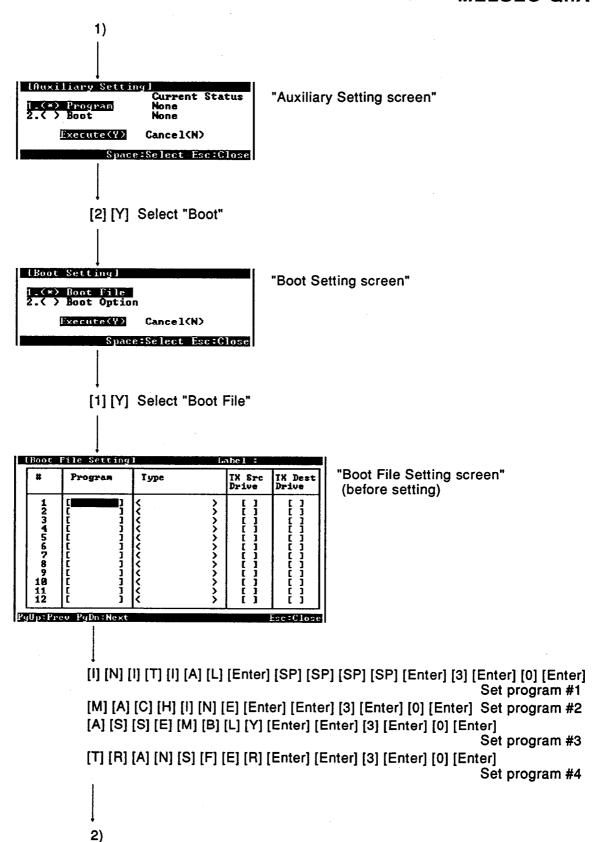


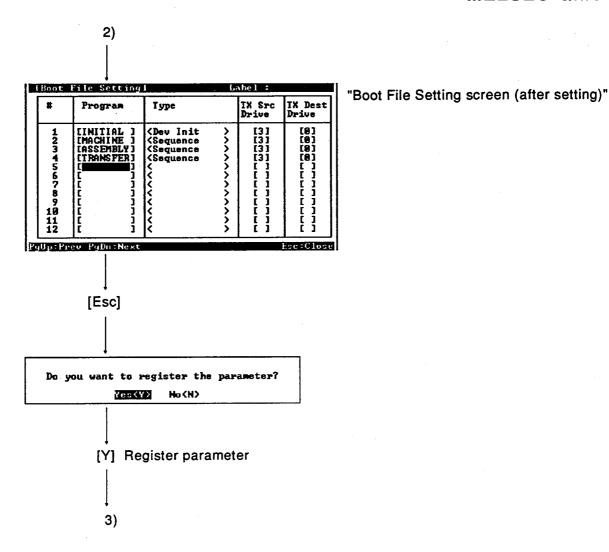
* View with front cover open

(2) Set boot file

Set the parameter boot file.



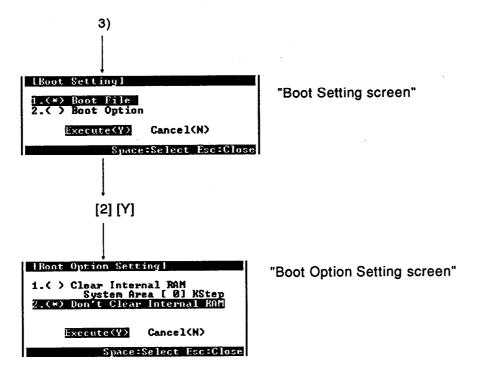




(3) Set boot options

During boot operation the ACPU memory contents are cleared but for the QnACPU, program files are written to a free area of internal memory.

Set the boot options as shown below to clear the internal memory. In this case, the parameters are also cleared, so make sure that the CPU system setting switch 1 does not select the internal memory (drive 0) as the enabled parameter drive.



(4) Execution

Boot operation is executed under the following conditions:

- Power turned ON (reset) when the key switch is in the RUN position:
- · CPU is switched from STOP to RUN.

The sequence program is executed immediately after boot operation is complete.

(5) Cautions

- During boot operation, a program file with the same name at the transfer destination is overwritten.
- During boot operation, up to 2 or 3 seconds are required to reach RUN status.
- Boot operation is also executed after a power interruption occurs.
 Therefore, after boot operation has been completed once, turn OFF SW1 of CPU system setting switch 1.
- The transferred file contents may be damaged if the power is turned off or the module is reset during program transfer from the memory card to the internal memory card (boot operation).

4.4.4 Remote operation of the QnACPU from a distant location

Remote operations of the QnACPU are possible, as shown in the table below.

The relationship between the key switch positions and the permitted remote operation is also shown in the table.

Remote Operation Key Switch	RUN	STEP-RUN	STOP	PAUSE	RESET	Latch Clear
RUN	RUN	STEP-RUN	STOP	PAUSE	Remote operation NG	Remote operation NG
STOP	STOP	STOP	STOP	STOP	RESET	Latch clear

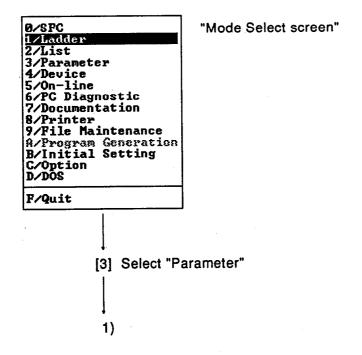
(1) Remote RUN/STOP

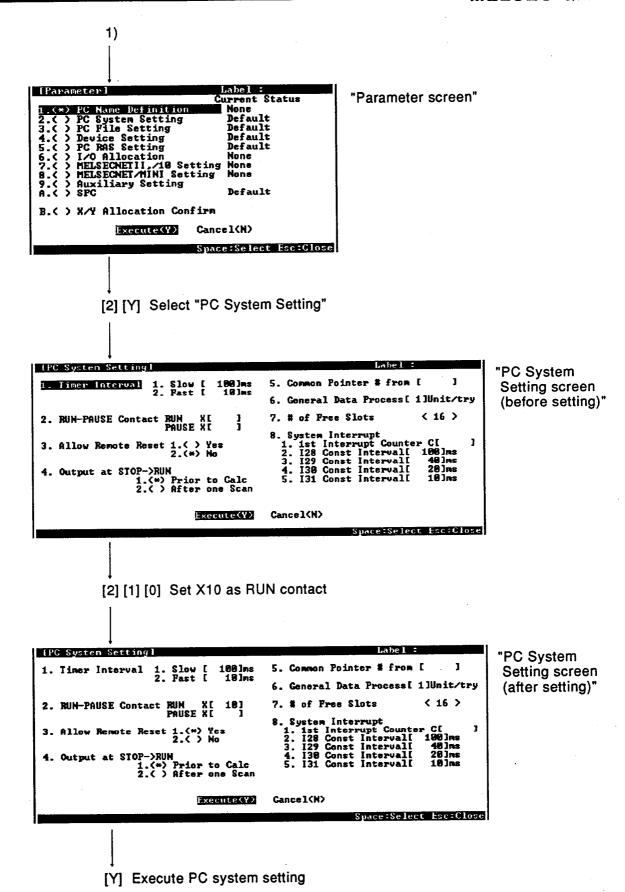
Set the key switch in the RUN position to carry out remote RUN and STOP.

Two methods are available for remote RUN and STOP:

(a) Method using remote RUN contact

Set the remote RUN contact (X) with the parameters. The CPU is in STOP status when the contact is ON or RUN status when the contact is OFF.

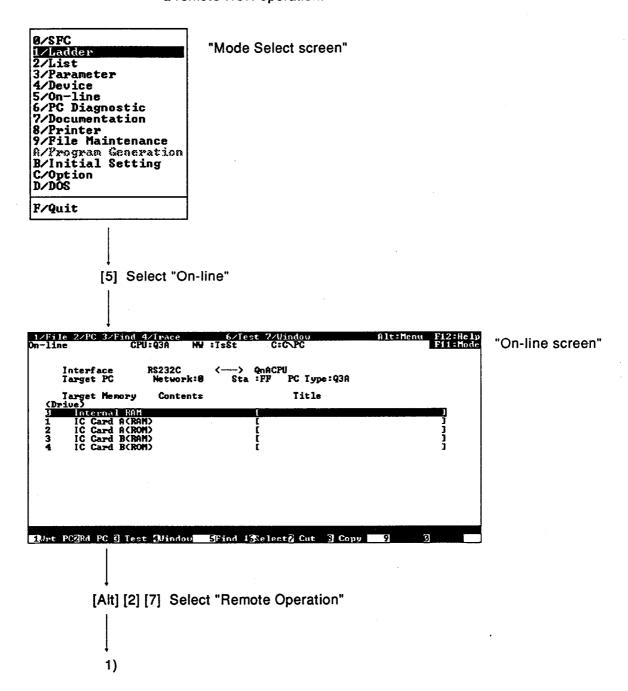


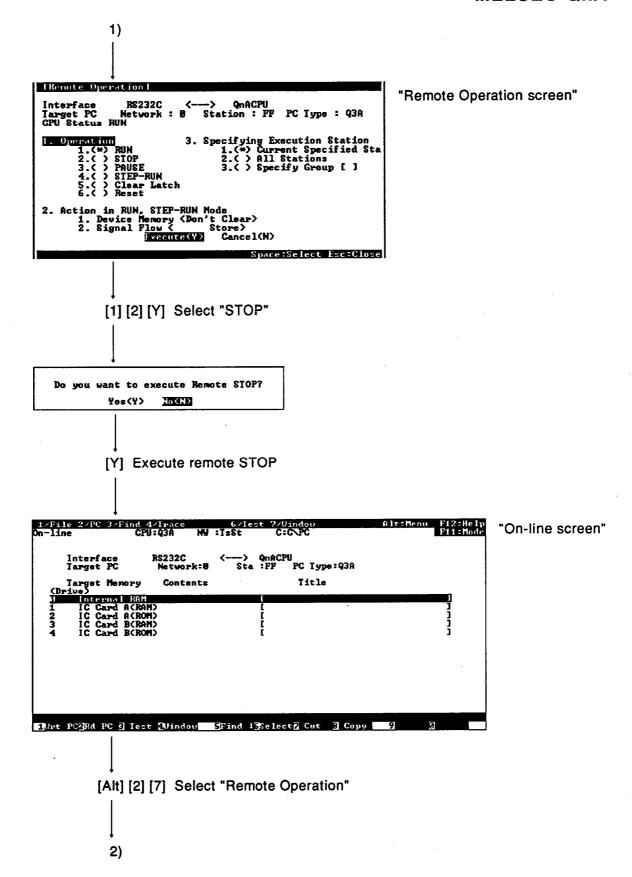


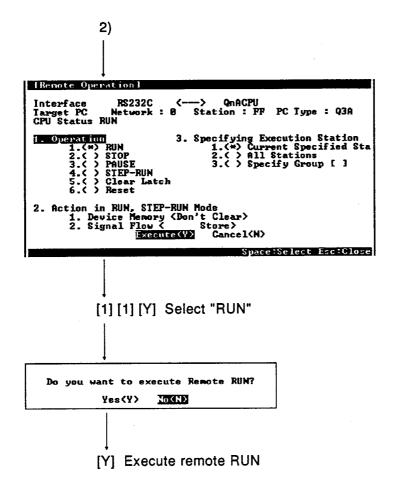
(b) Method using operations from peripheral device or special-function module

Operation is controlled with RUN and STOP instructions from a peripheral device.

The example below shows a remote STOP operation followed by a remote RUN operation.







"Remote Operation screen"

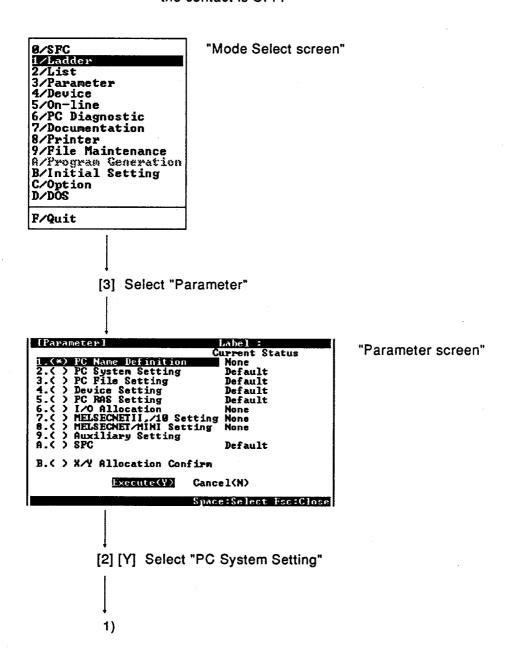
(2) Remote PAUSE

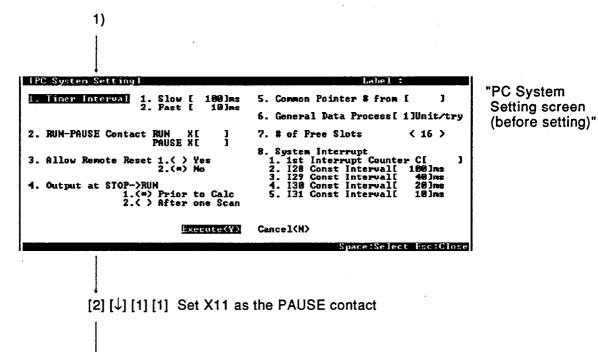
When the remote PAUSE is executed, the CPU stops and the I/O status is held.

Set the key switch in the RUN position to carry out remote PAUSE. Two methods are available for remote PAUSE.

(a) Method using remote PAUSE contact

To use the remote PAUSE contact, turn ON the remote PAUSE enable coil (SM206) with the sequence program. Set the remote PAUSE contact (X) with the parameters. The CPU is in PAUSE status when the contact is ON or RUN status when the contact is OFF.



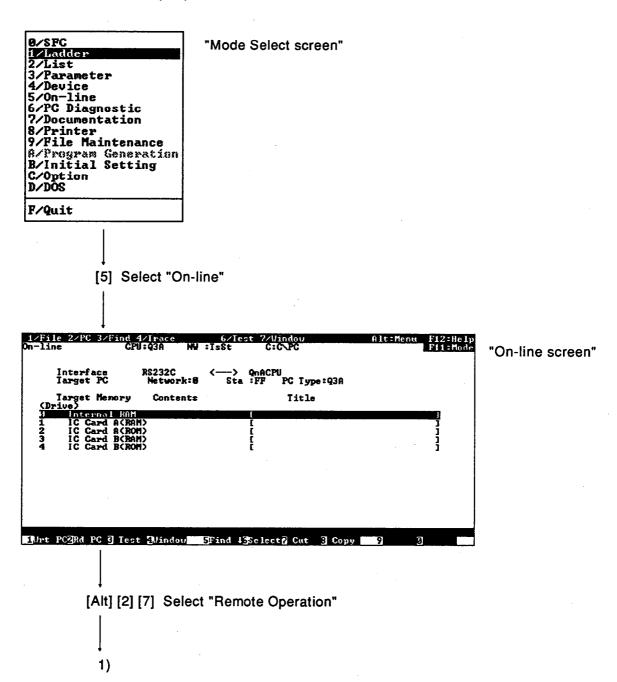


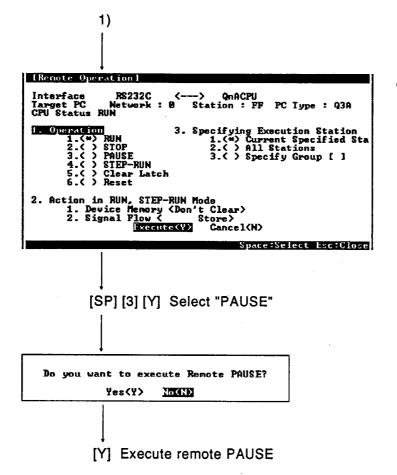
"PC System Setting screen (after setting)"

```
1. Timer Interval 1. Slow [ 190]ms 2. Fast [ 10]ms 5. Common Pointer # from [ ] 6. General Data Process[ 1]Unit/try 2. RUN-PAUSE Contact RUN X[ ] 7. # of Free Slots < 16 > PAUSE X[ 11] 8. System Interrupt 3. Allow Remote Reset 1.( ) Yes 2.(*) No 2.(*) No 2.128 Const Interval[ 100]ms 4. Output at STOP->RUN 4. 138 Const Interval[ 40]ms 4. 138 Const Interval[ 20]ms 5. I31 Const Interval[ 10]ms 5. I31 Const Interval[ 10]ms
```

[Y] Set PAUSE contact

(b) Method using operations from peripheral device Operation is controlled with the remote PAUSE instruction from a peripheral device.

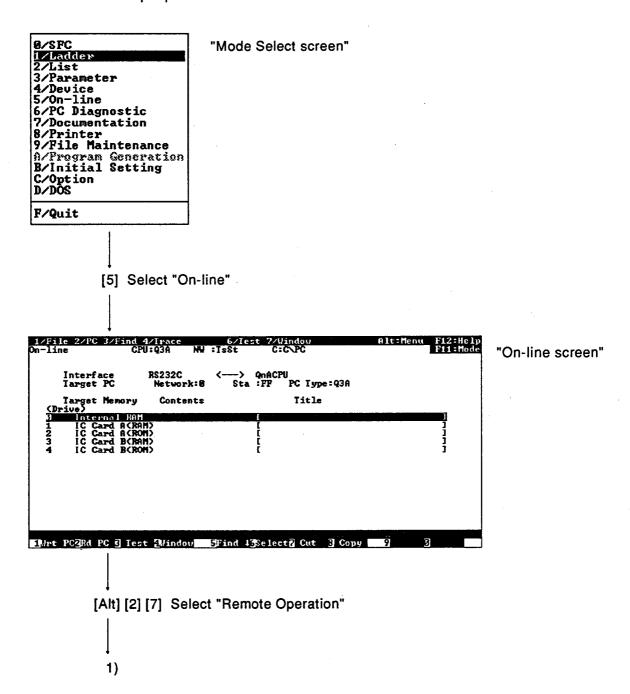


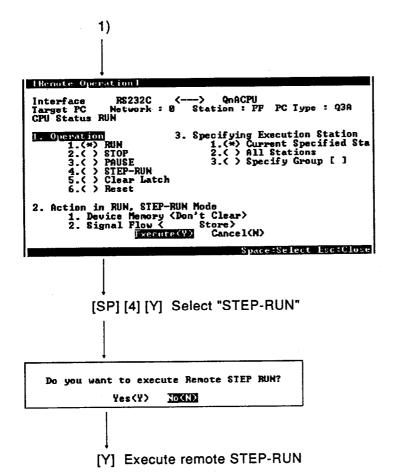


"Remote Operation screen"

(3) Remote STEP-RUN

Operation is controlled with the remote STEP-RUN instruction from a peripheral device.

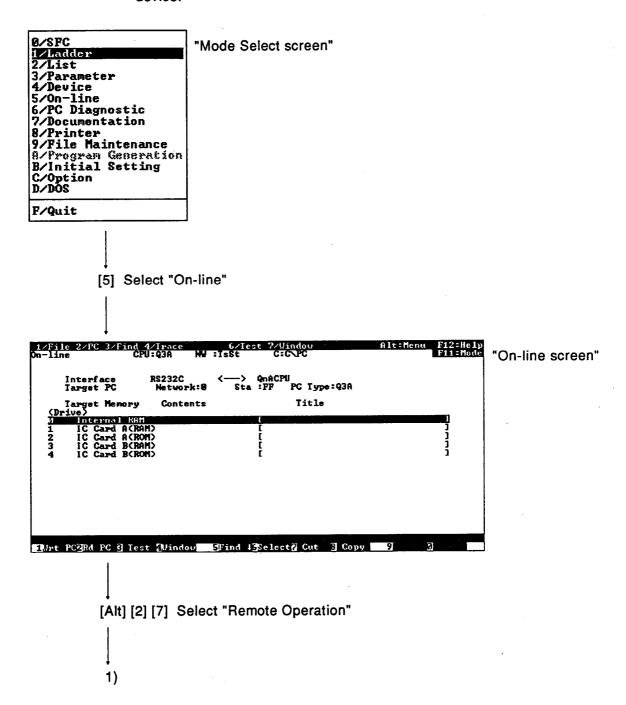




"Remote Operation screen"

(4) Remote latch clear

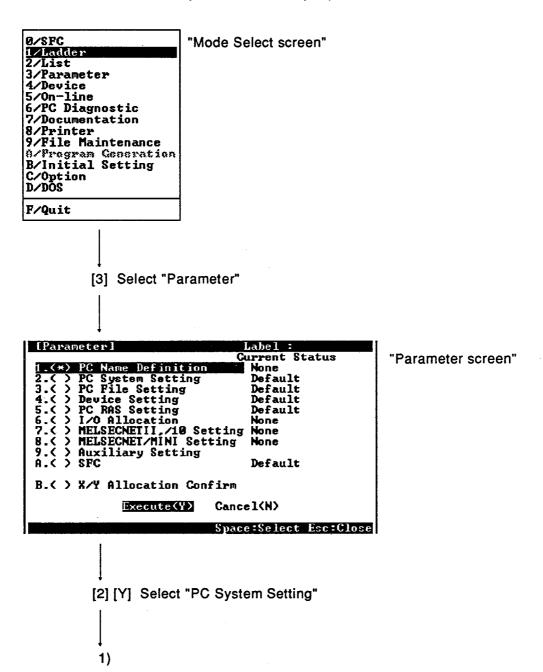
Set the key switch in the STOP position to carry out remote latch clear. The remote latch clear operation is carried out from the peripheral device.

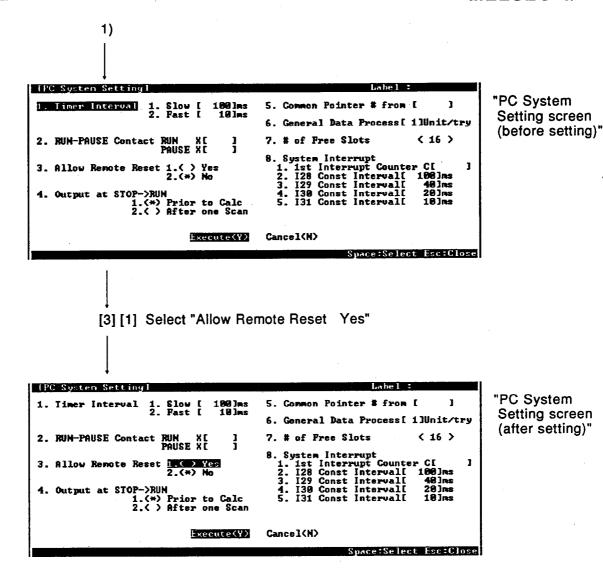


```
1)
[Remote Operation]
                                                                                                    "Remote Operation
Interface F
Target PC Ne
CPU Status RUN
                      RS232C <---> QnACPU
Network: 0 Station: FF PC Type: Q3A
                        RS232C
                                                                                                     screen"
                                        3. Specifying Execution Station
1.(*) Gurrent Specified Sta
2.( > All Stations
3.( ) Specify Group [ ]
1. Operation
          1.(*) RUN
2.( ) STOP
3.( ) PAUSE
4.( ) STEP-RUN
5.( ) Clear Latch
6.( ) Reset
2. Action in RUN, STEP-RUN Mode
1. Device Memory (Don't Clear)
2. Signal Flow (Store)
Execute(Y) Cancel
                                                  Cancel(N)
                                                          Space:Select Esc:Close
             [SP] [5] [Y] Select "Clear Latch"
   Do you want to execute Remote L.CLR?
                     Yes(Y)
                                     No (N)
             [Y] Execute Remote L.CLR
```

(5) Remote RESET

Set the key switch in the STOP position to carry out remote RESET. Enable remote RESET with the parameters and carry out the remote RESET operation from the peripheral device.

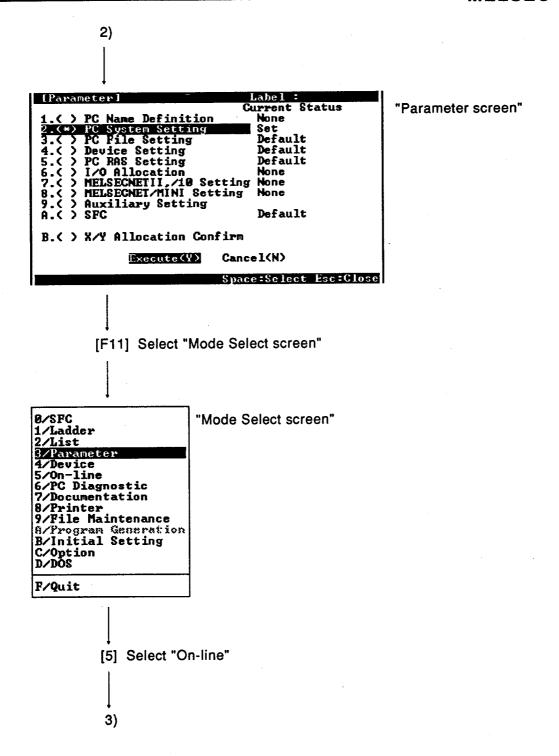


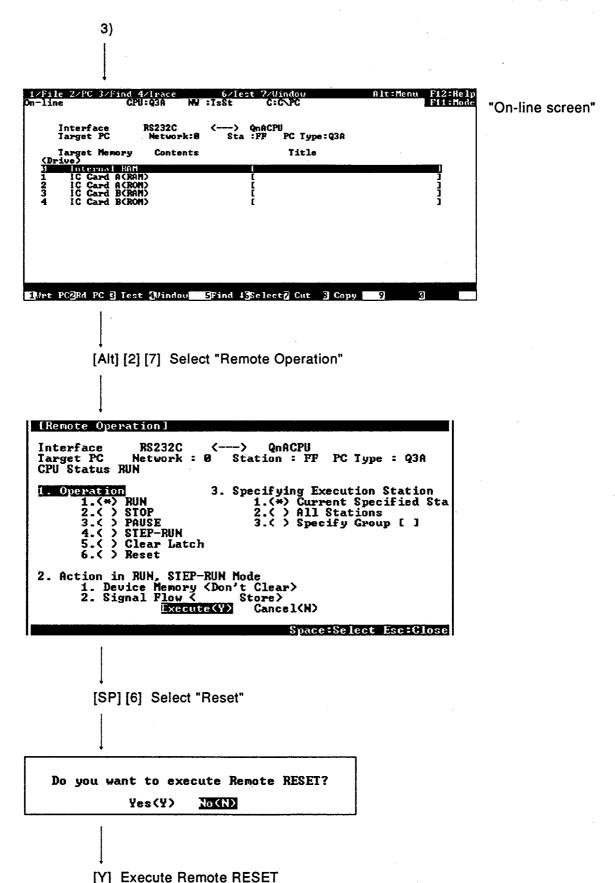


Yes"

[Y] Set "Allow Remote Reset Yes"

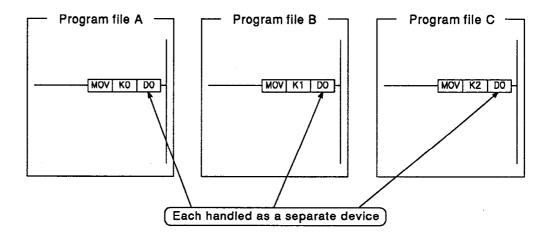
2)





4.4.5 What are local devices?

The QnACPU is able to handle each device in each program file as a separate device. These devices are called "local devices."



- · A memory card is required to use local devices.
- Parameter settings are required to use local devices.
- The following devices can be used as local devices.

M : internal relayV : edge relay

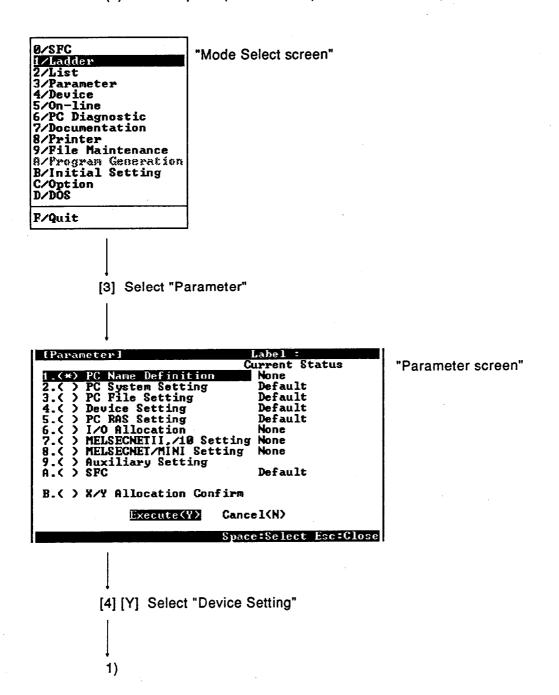
T : normal timer

ST: retentive timer

C : counter

D : data register

(1) Set 1 k point (M0 to M1023) of the internal relays as local devices



2)

1) Device Setting Device Devic Label : Disable C/L Key Enable C/L Key SXYMLBRBUSHICDU 16 110 110 110 110 110 110 110 110 110] C 1-[3 Ľ]-[1 8X 2X 1 F3:Latch->LocalDev-> Esc:Close [F3] Change from latch to local devices

"Device Setting screen (before setting)"

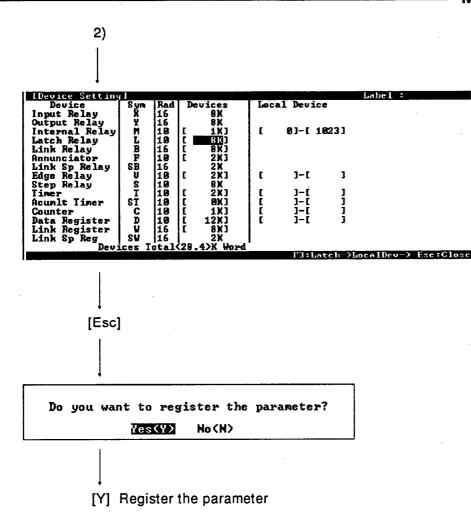
Device Sym Rad Devices Local Device Input Relay X 16 8K Output Relay Y 16 8K Internal Relay M 19 [188] [1-[]	
Output Relay Y 16 8K	
Output Relay Y 16 8K	
! Internal Relau! M 10 [
- - - - - - - - - -	
Latch Relay L 19 [8K]	
Latch Relay L 19 [8K] Link Relay B 16 [8K] Annunciator F 19 [2K]	
Annunciator F 18 [2K]	
Link Sp Relay SB 16 2K	
Edge Relay U 10 [2X] []-[]	
Step Relay S 10 8K	
Timer	
Acumit Timer ST 10 C OK] C J-[]	
Counter C 10 [1X] [1-[] Data Register D 18 [12X] [1-[]	
Link Register W 16 [8K]	
Link Sp Reg SW 16 2K	
Devices Total<28.8>K Word	
F3:Latch->Loca	IDeu-> Esc:Close

"Device Setting screen (before setting)"

[←] [Del] [Del] [1] [Tab] [0] [Tab] [1] [0] [2] [3] [Enter]
Set M0 to M1023 as local devices.

"Device Setting

screen (after setting)"



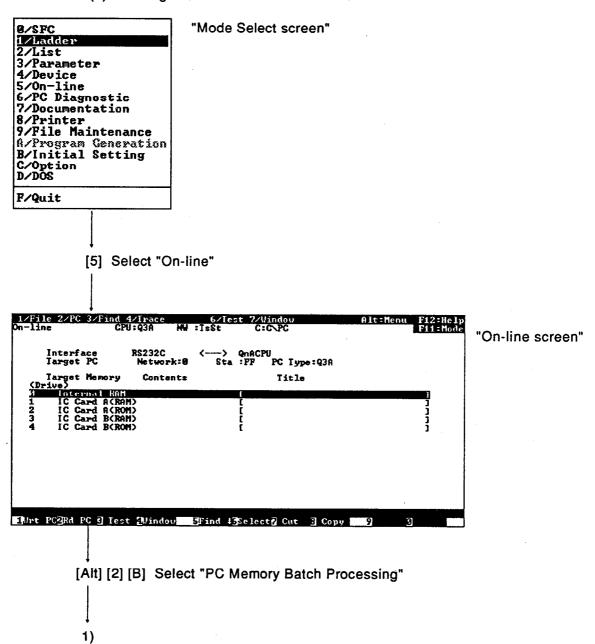
4.5 Easy Program Debugging

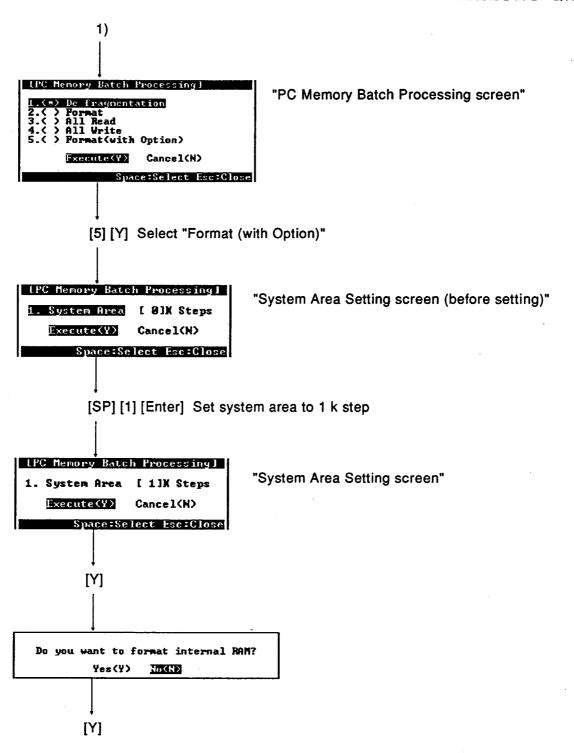
This section describes the easy debugging functions.

4.5.1 Simultaneous monitoring by multiple operators

Monitoring is possible by multiple operators. Setting the other station monitor file in the system settings allows high-speed monitoring. (Setting of the host station monitor file is not required). Each other station monitor file occupies 1 k step.

(1) Setting the other station monitor file





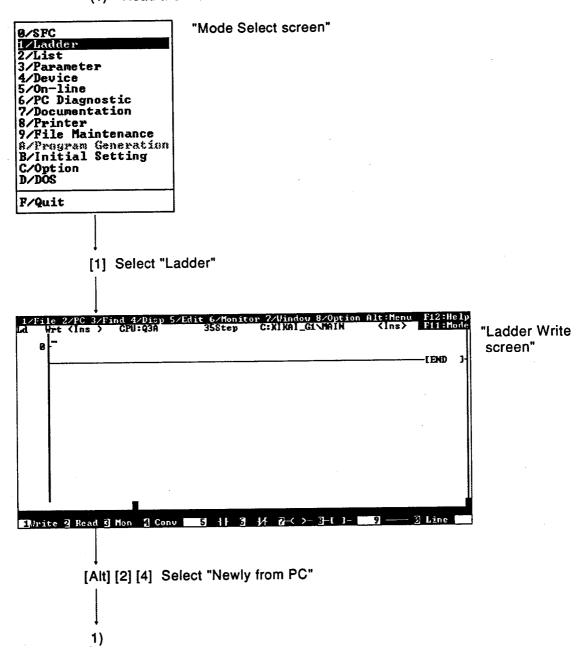
(2) Monitor
See Chapter 3 for information about monitoring.

4.5.2 Monitoring devices at any step

Monitor conditions can be set for precise monitoring of the PC operating status or monitoring of local devices.

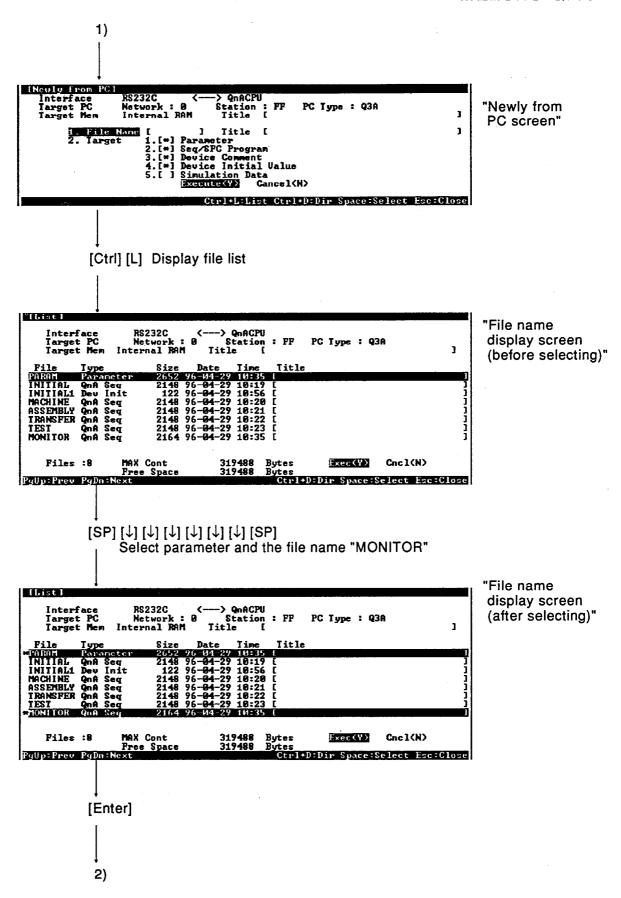
In addition to monitoring during the END processing, the monitor conditions allow monitoring to be set at a step number, a step continuity state, or a device state.

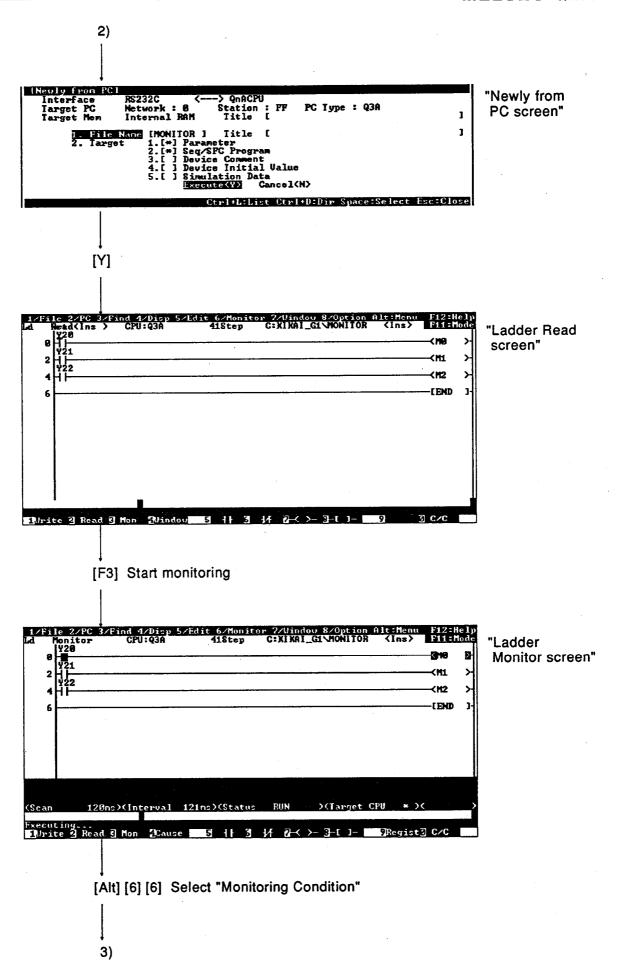
(1) Read the monitored file from the PC CPU

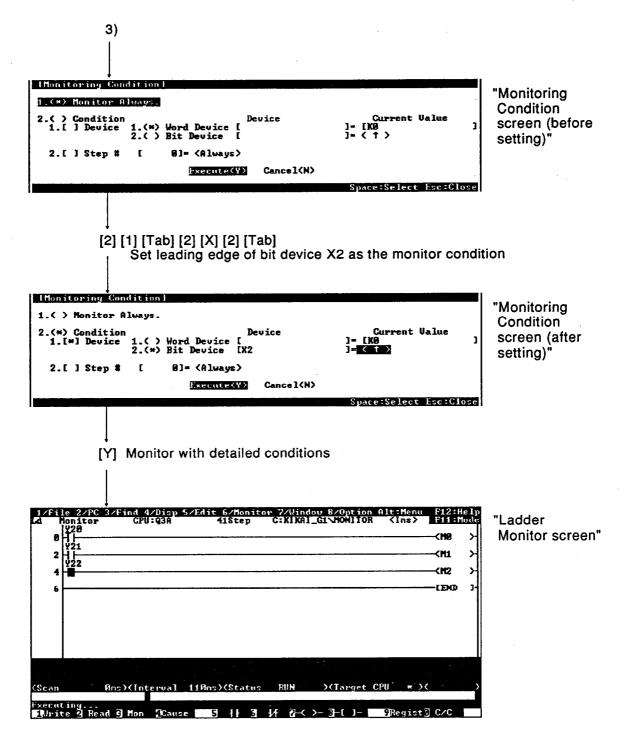


POINT

Select "Newly from PC" to match the GPP file name with the CPU file name.



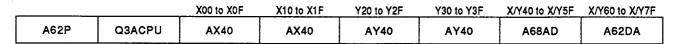


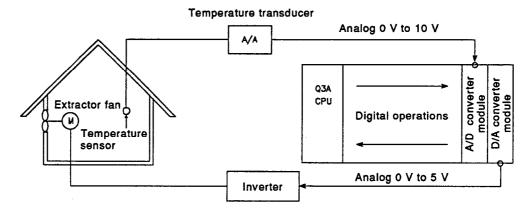


4.6 Programming

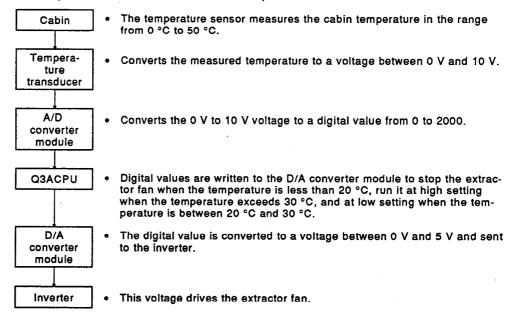
4.6.1 Creating one program

This section shows how to create a program for the imaginary system below.





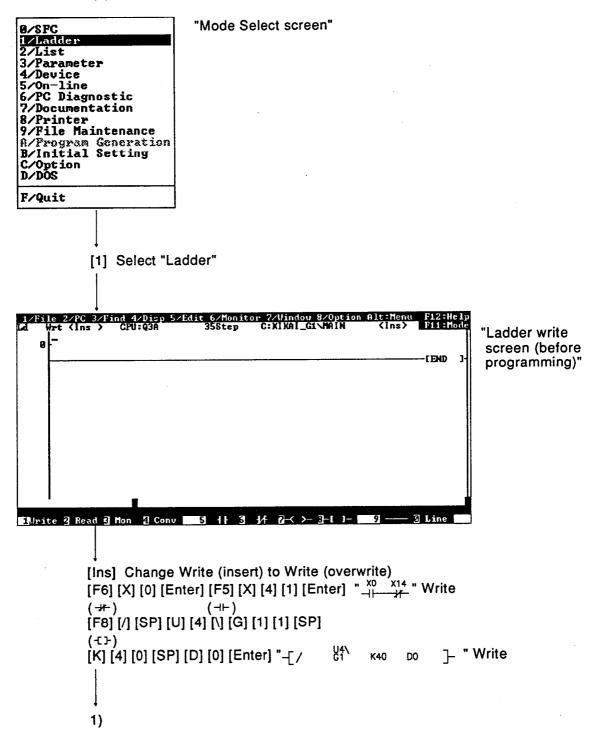
This system maintains a constant temperature in the cabin.

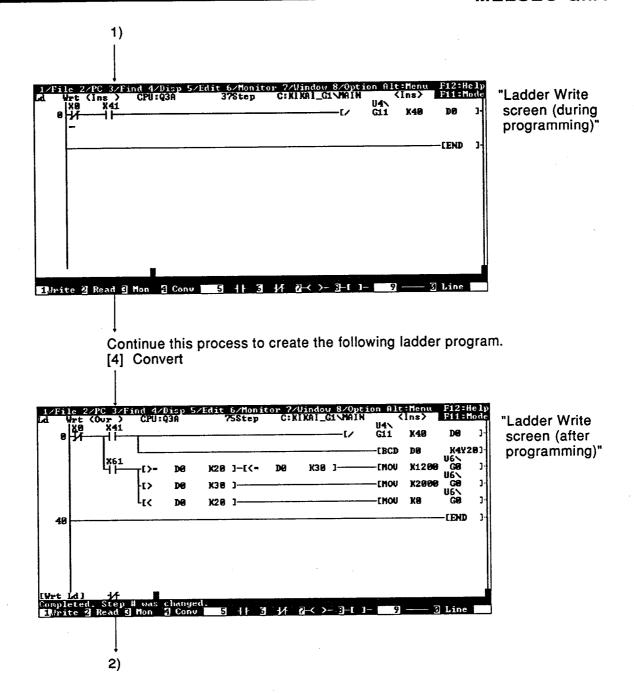


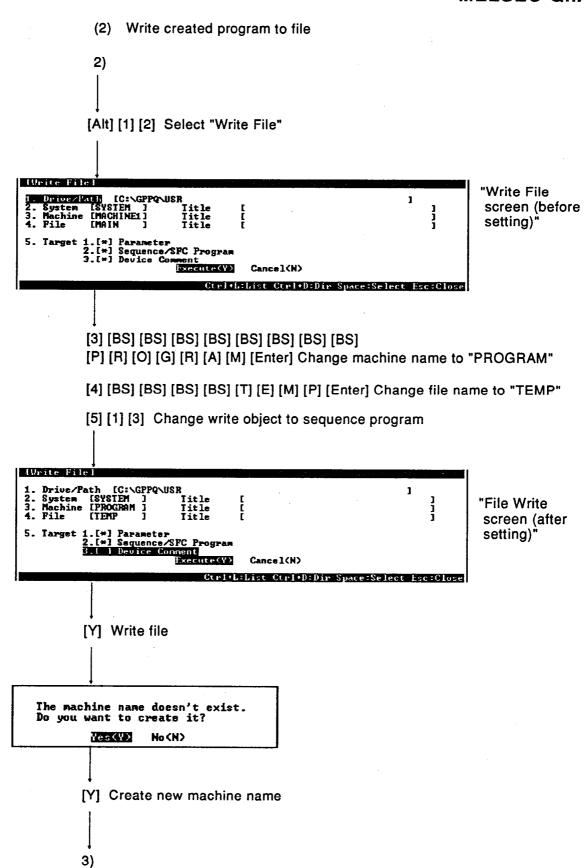
Allocation of devices used

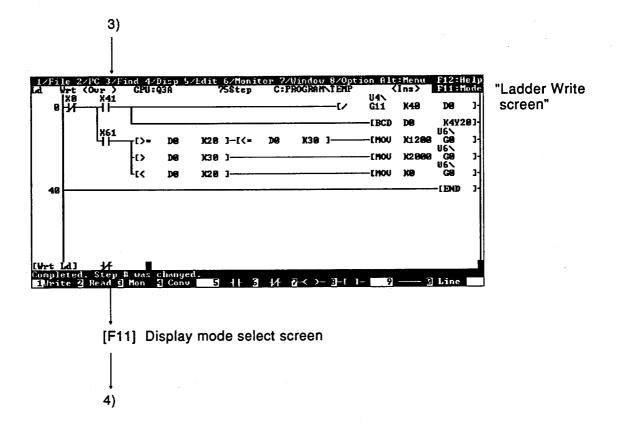
X0	Stop switch
X41	A/D converter READY signal
D0	Stores cabin temperature
X61	D/A converter READY signal
	Cabin temperature display

(1) Create temperature regulation program



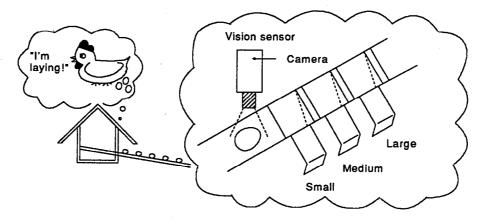




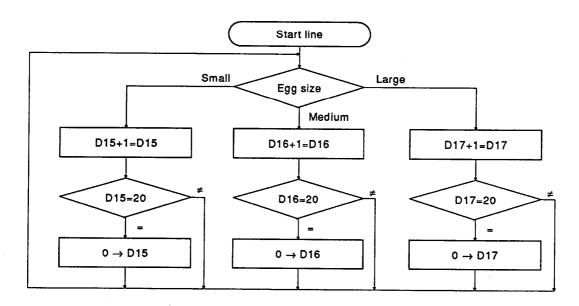


4.6.2 Creating multiple programs

This section shows how to create a program to add extra functions to the system described in section 4.6.1.



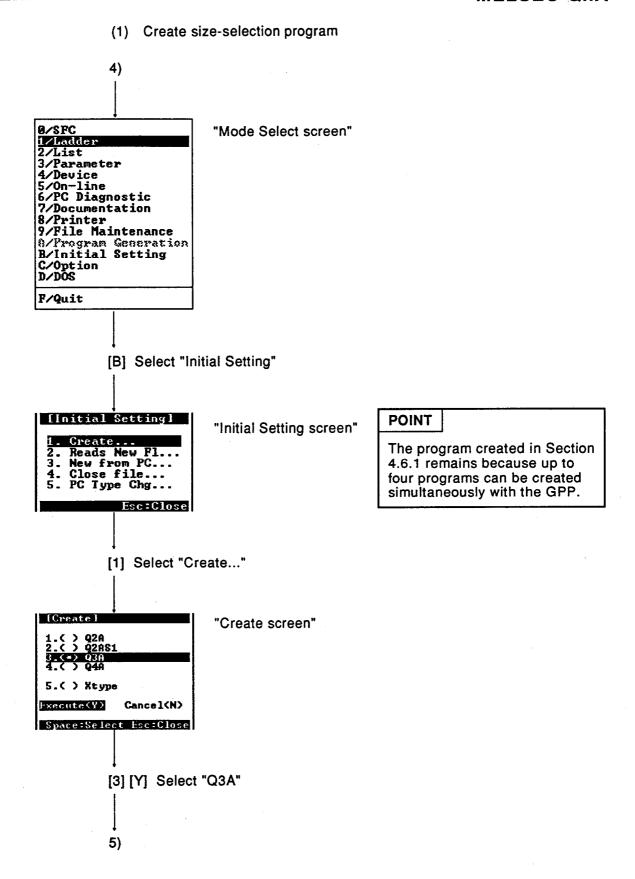
Classifies the eggs laid by the chickens into small, medium, and large sizes, and packs them as tens into boxes.

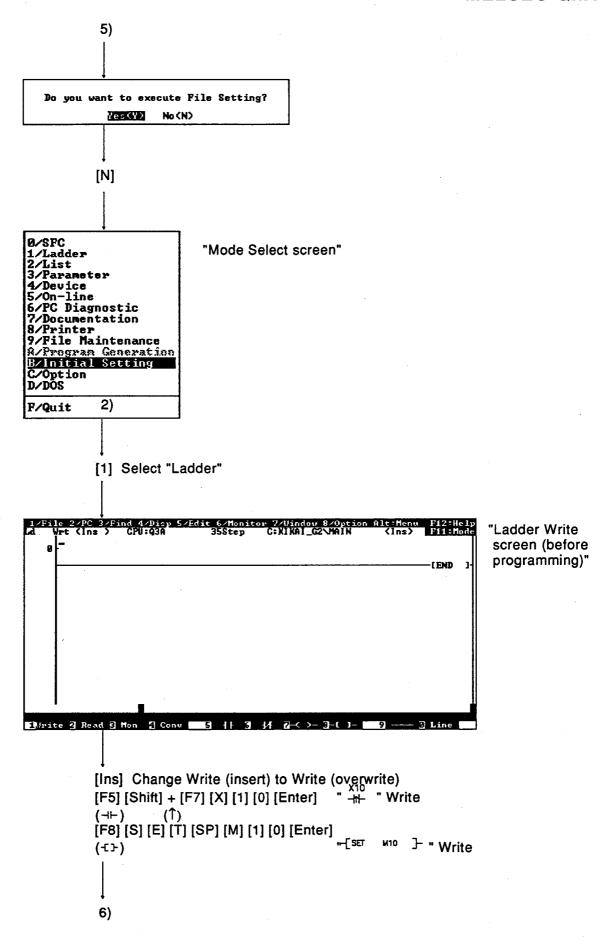


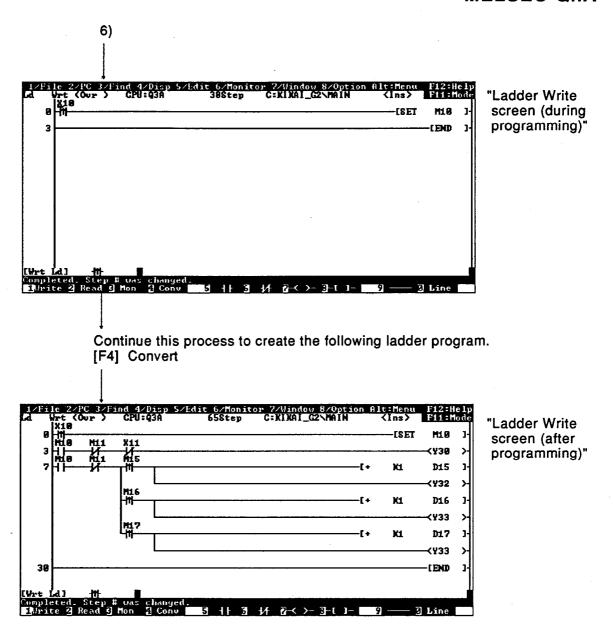
Allocation of devices used

- X10... Line start switch
- X11... Line stop switch
- Y30... Line start output
- M15 .. Small egg counter sensor
- M17 .. Large egg counter sensor
- Y32... Extract small eggs
- Y33... Extract medium eggs
- Y34... Extract large eggs

- M10... Line start internal relay
- M11... Line stop internal relay
- D15 . . . Number of small eggs storage
- M16 .. Medium egg counter sensor D16 ... Number of medium eggs storage
 - D17 . . . Number of large eggs storage
 - Y35 . . . Change to small egg pack
 - Y36 . . . Change to medium egg pack
 - Y37 ... Change to large egg pack



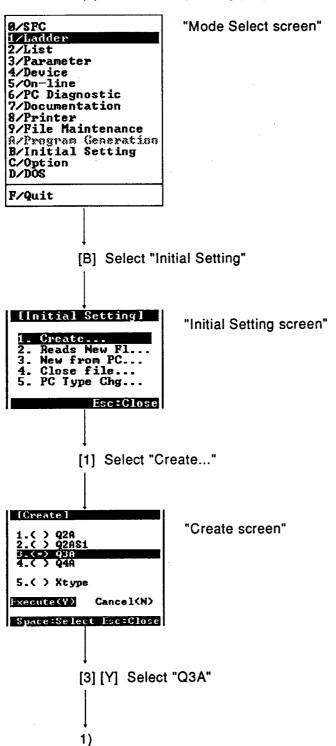


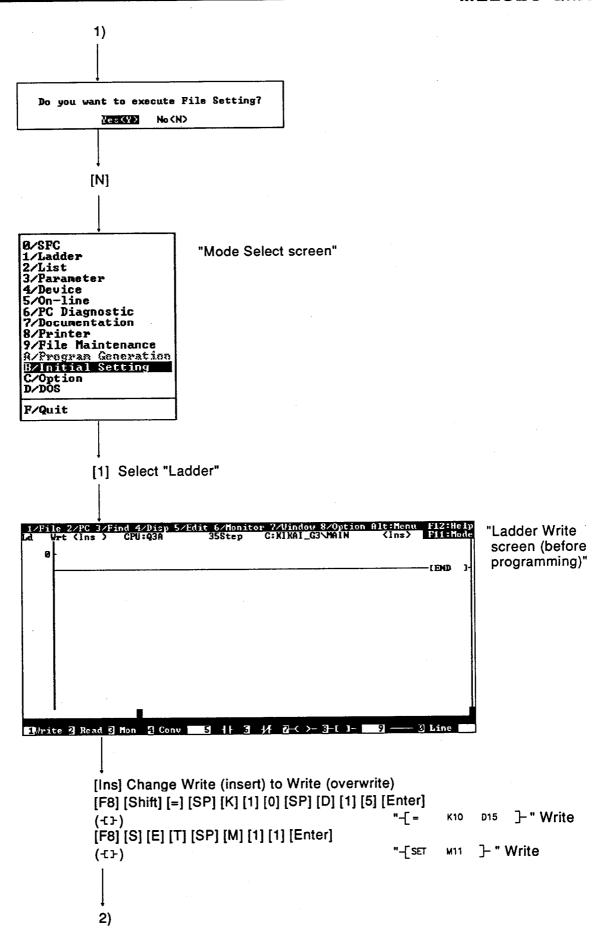


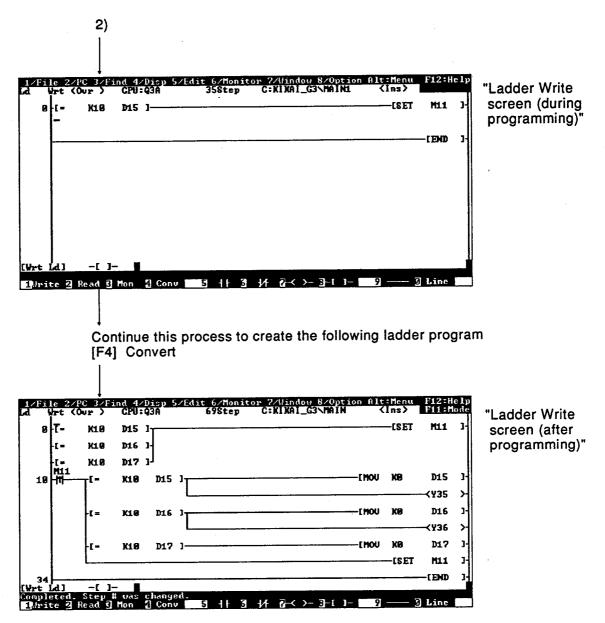
- (2) Set the file name to "SIZE" and write the file.

 The operation is the same as described in Section 4.6.1(2).
- (3) Write program to the CPU. See Section 3.5.2 for details about the write operation.

(4) Create the packing program

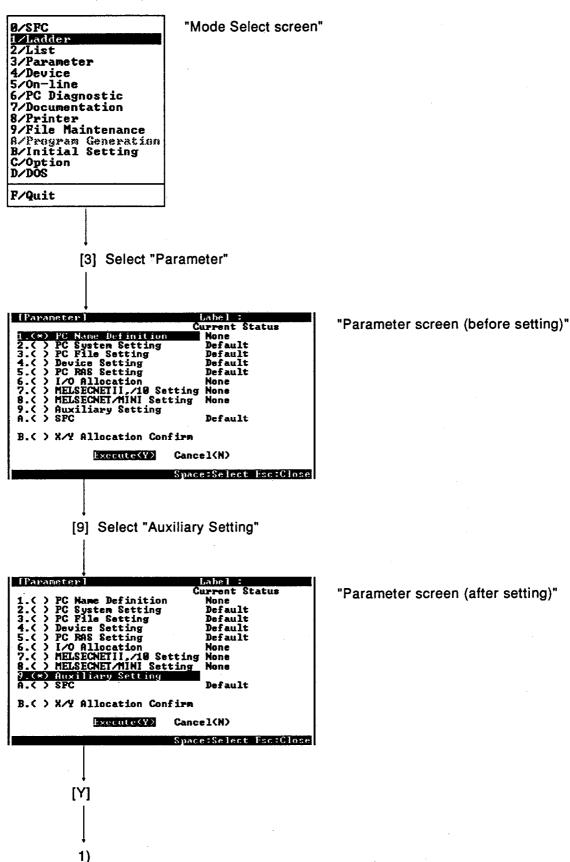


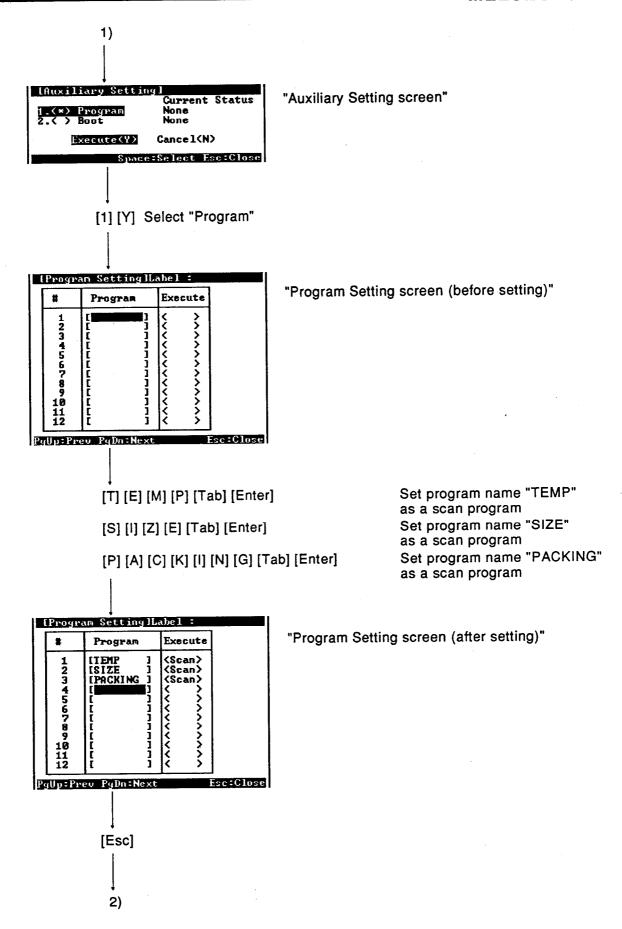


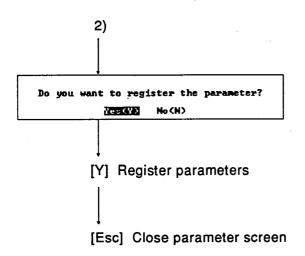


- (5) Set the file name to "PACKING" and write the file.
 The operation is the same as described in Section 4.6.1 (2).
- (6) Write program to the CPU. See Section 3.5.2 for details about the write operation.

(7) Set parameters







(8) Write to CPU. See Section 4.2.2.

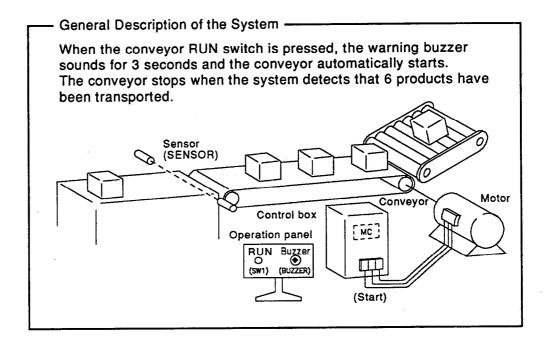
5. MAKING THE MOST OF YOUR QNACPU (2)

5.1 Programming Before I/O Module Configuration is Determined

By adding labels to the sequence program, a QnACPU program can be created before the I/O module configuration is determined.

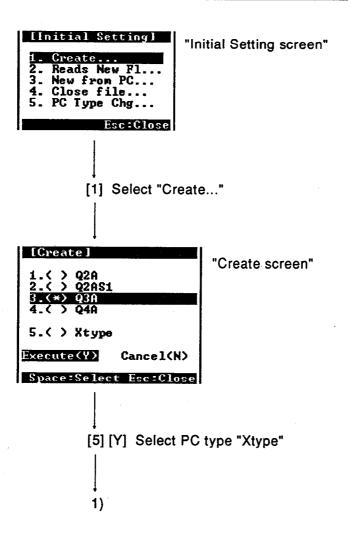
5.1.1 Programming using labels

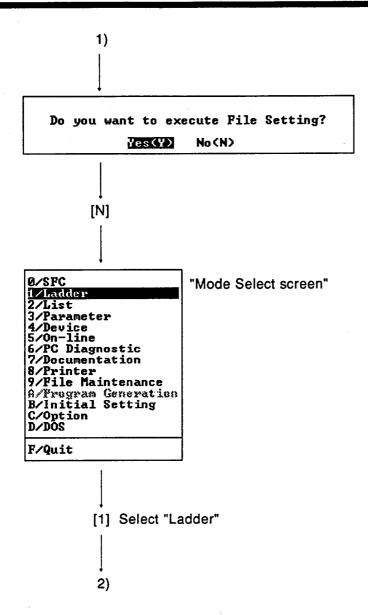
The example shown below is used to explain programming with labels.

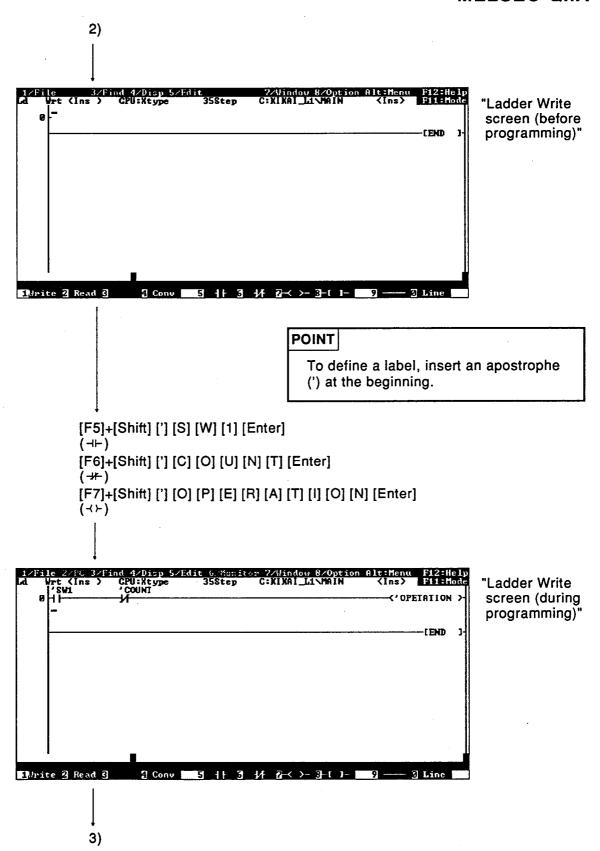


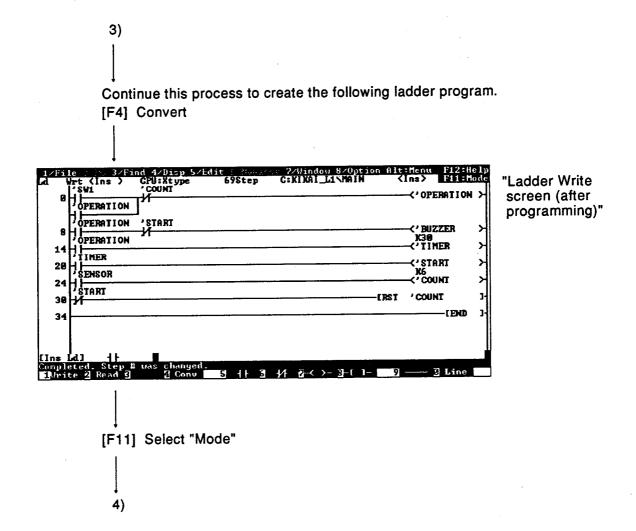
(1) Create the sequence program using the labels below.

Label	Device	Comment
SW1	ХO	Operation command for conveyor
COUNT	Co	6-product product count
OPERATION	Mo	Operation in progress
START	Y71	Operation start command
BUZZER	Y70	Operation start buzzer
TIMER	ТО	3 second timer for buzzer
SENSOR	X1	Product detection

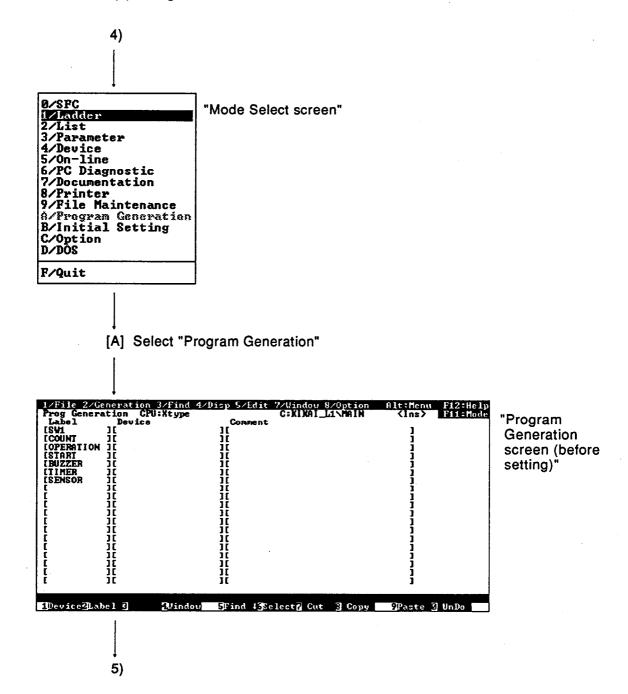


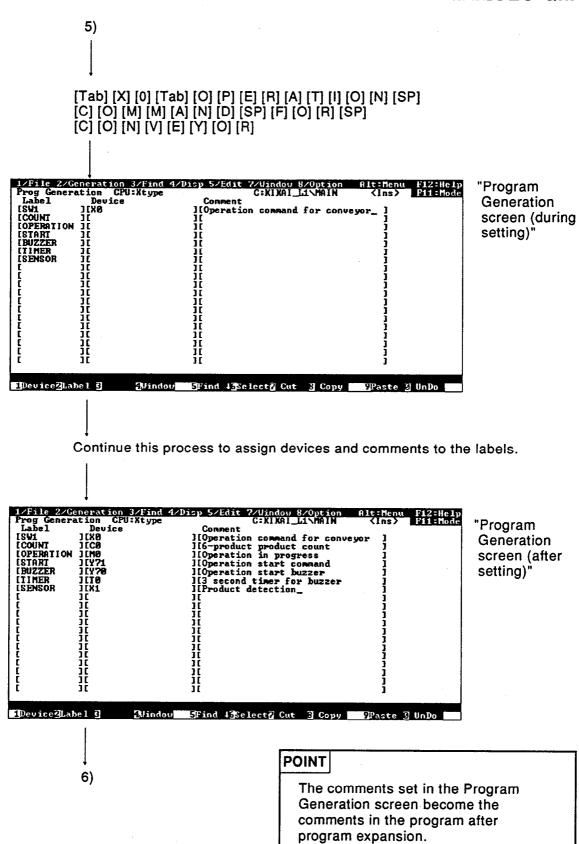


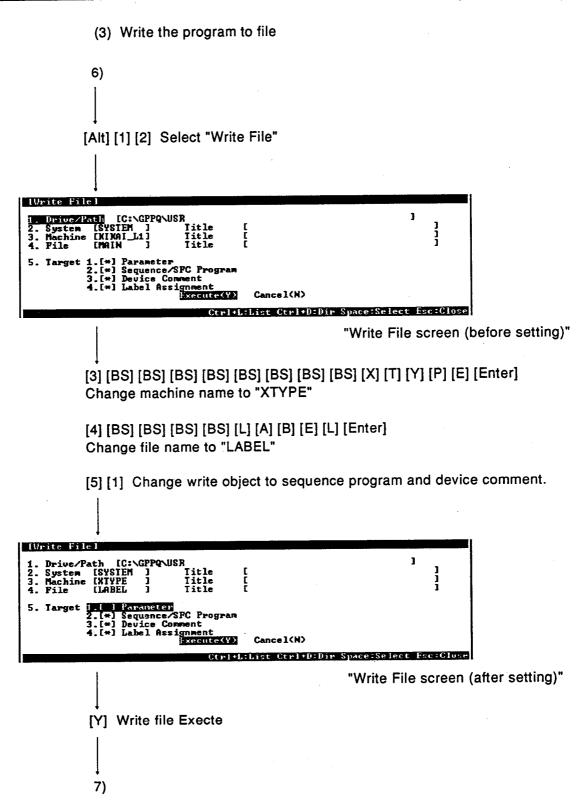


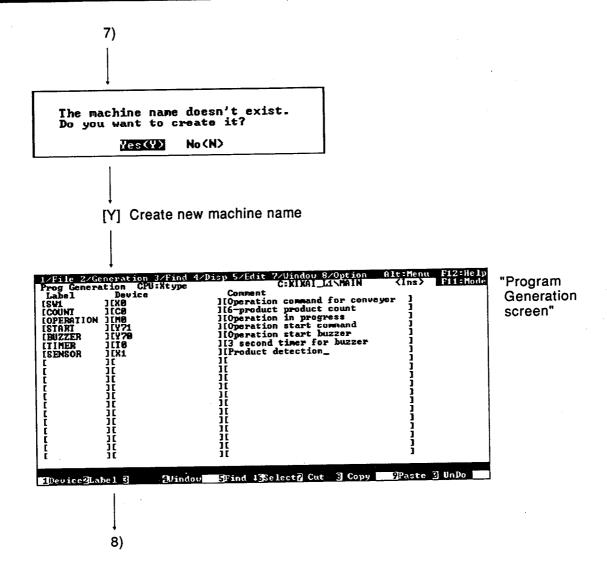


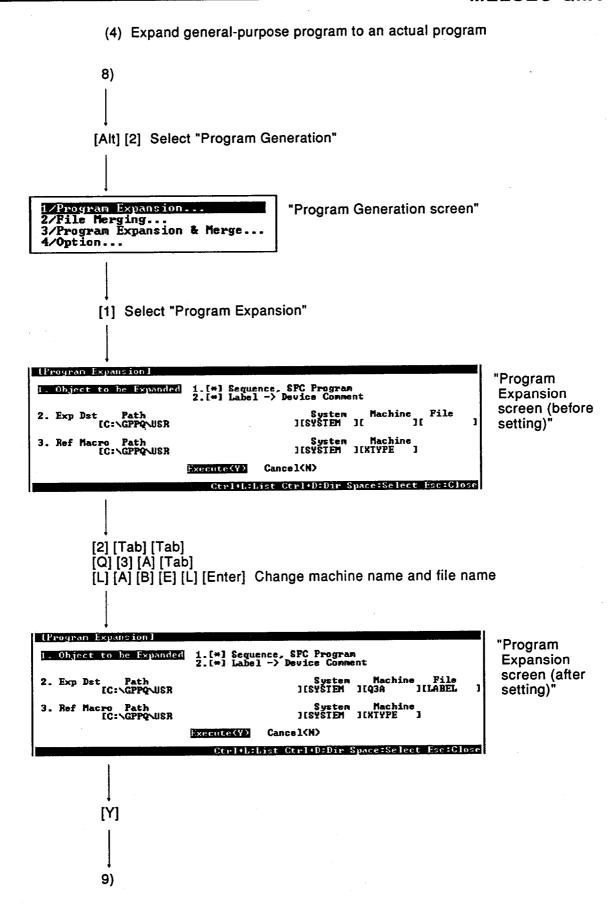
(2) Assign devices and comments to the labels

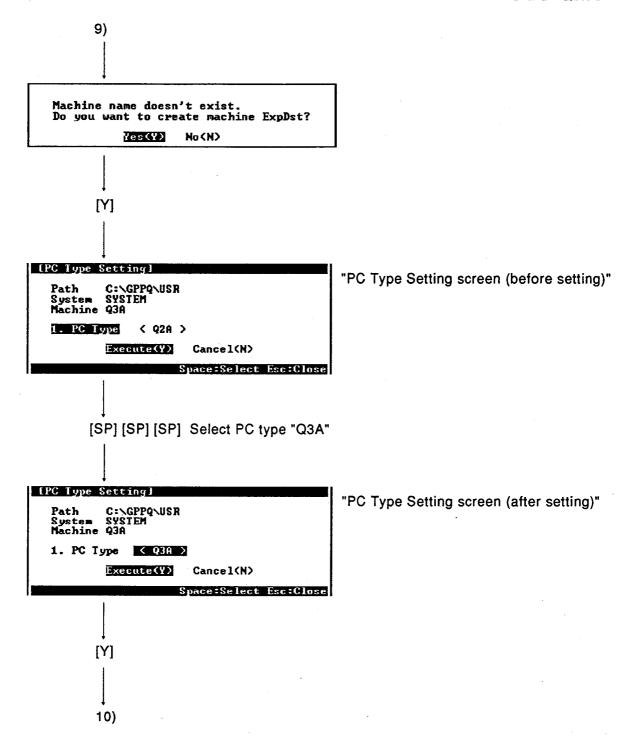


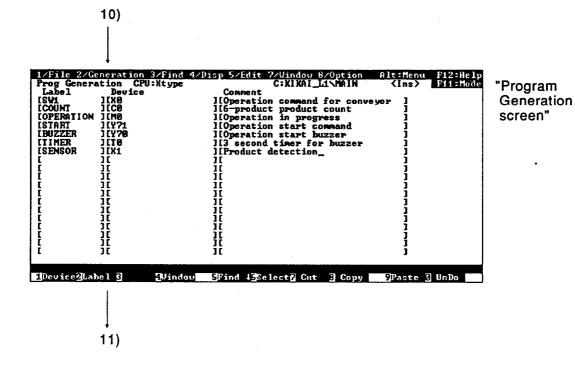


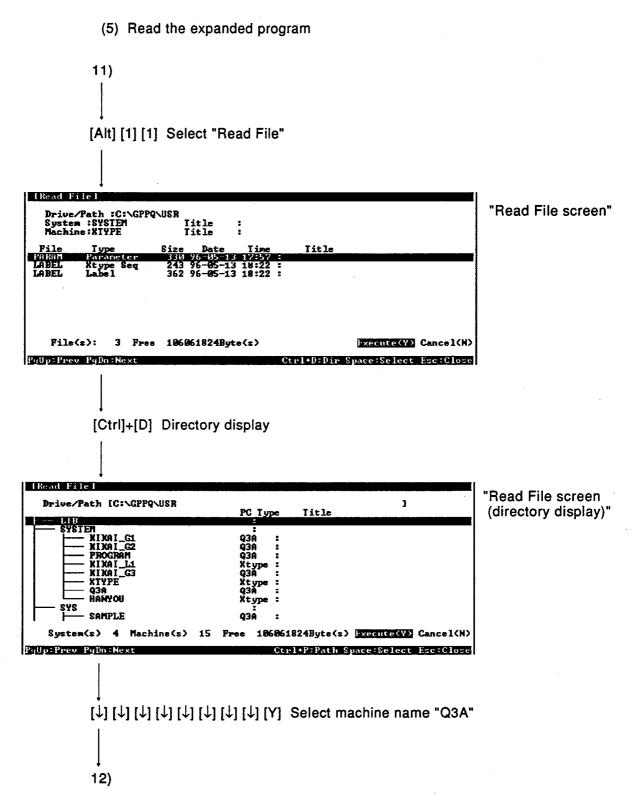


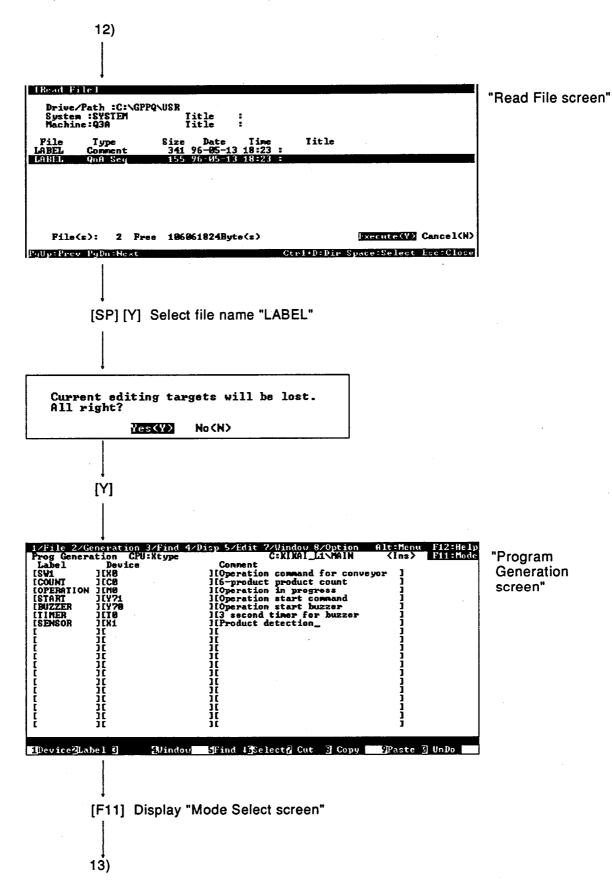


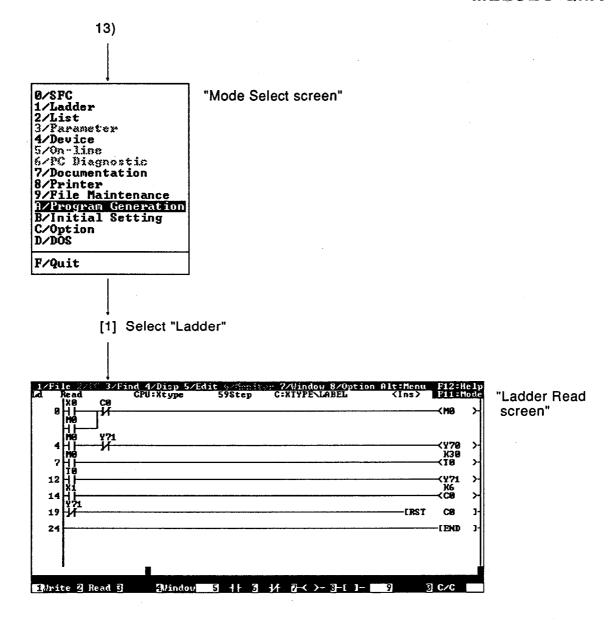










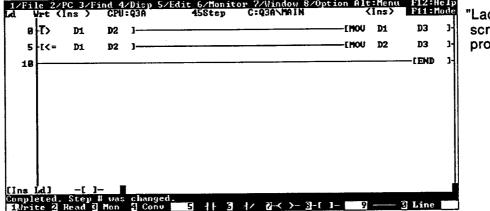


5.2 Utilizing Program Resources

5.2.1 Program standardization with user-created macros

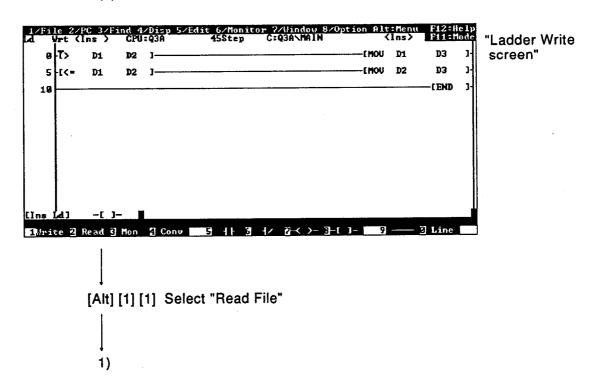
Macro instructions are instructions which read and utilize ladders presaved in a peripheral device.
Using macro instructions improves program productivity.
This section describes how to use the macro instructions.

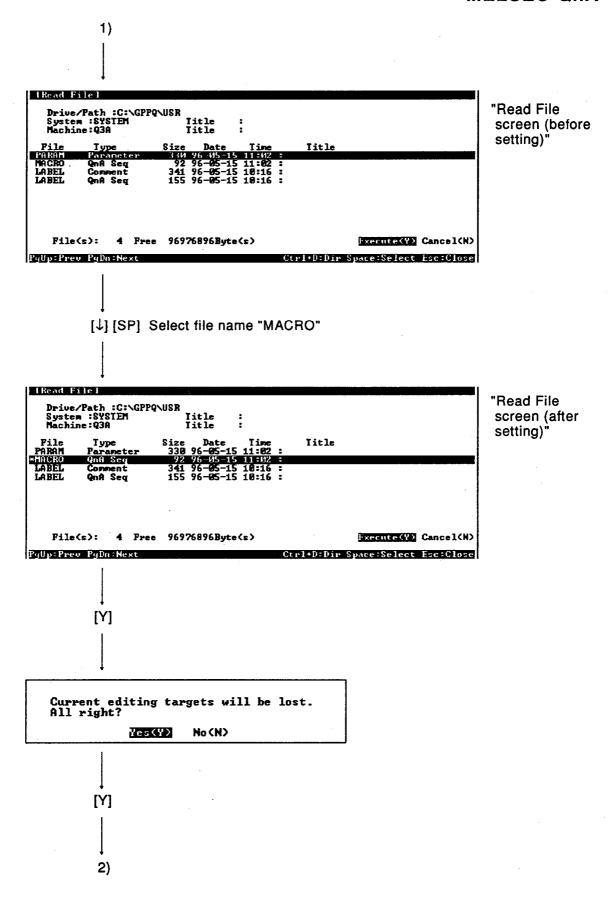
(1) Create the following sequence program and write it to file

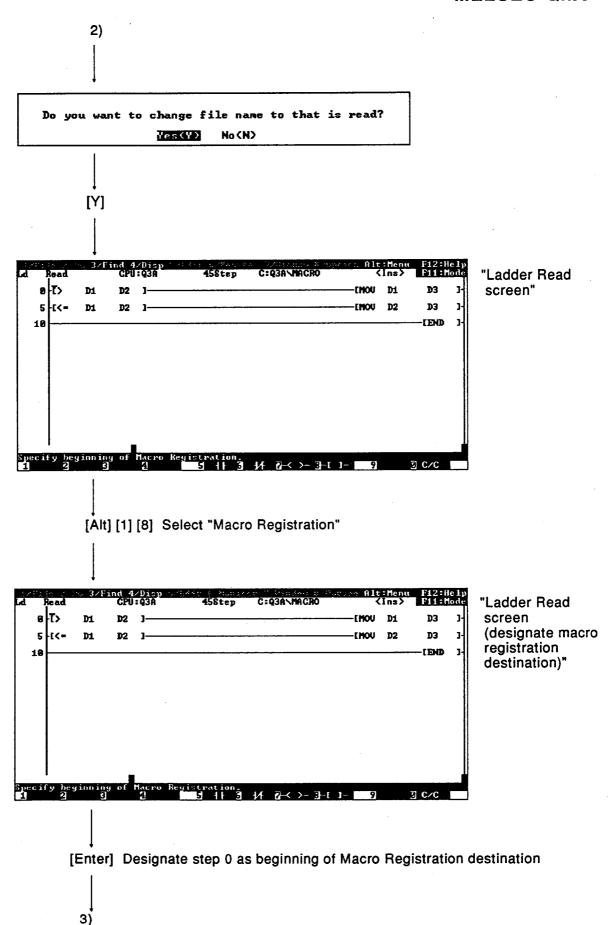


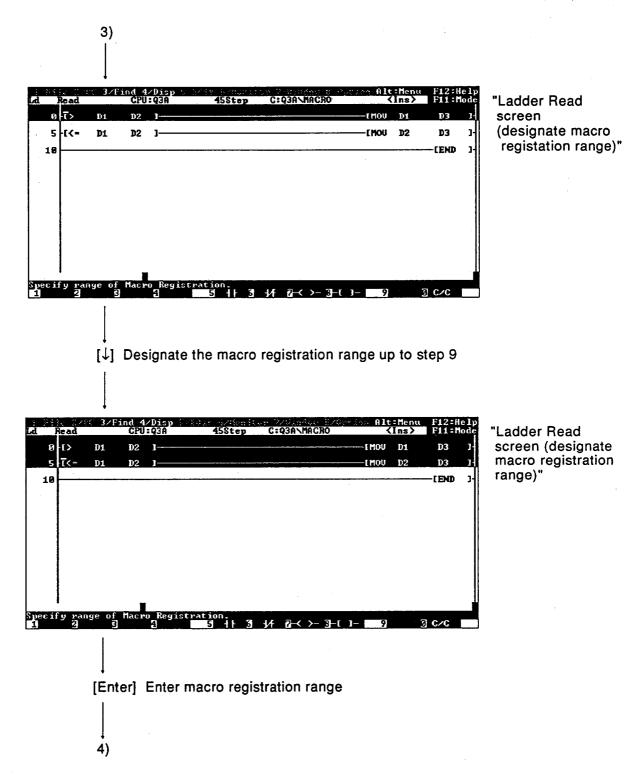
"Ladder Write screen (before programming)"

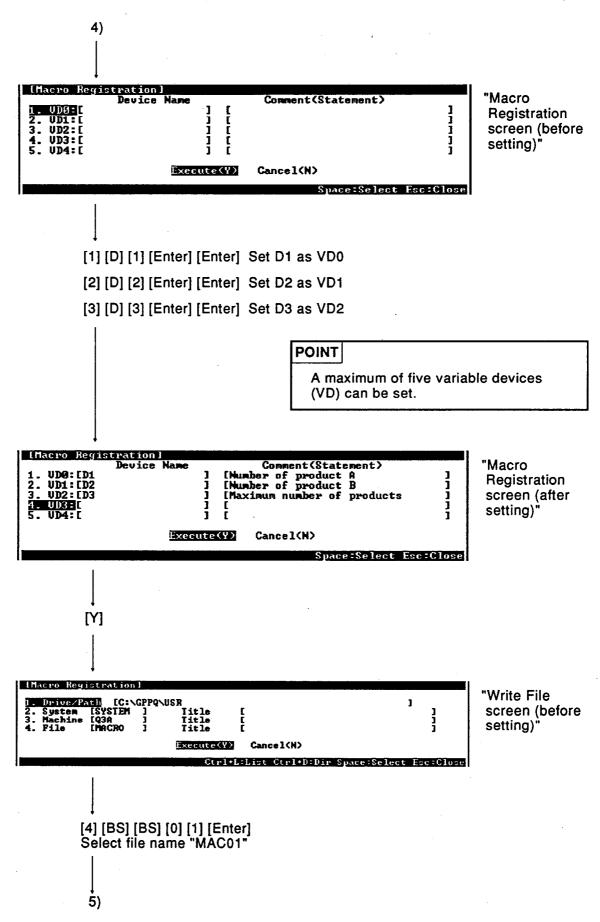
(2) Read the file and register the macro

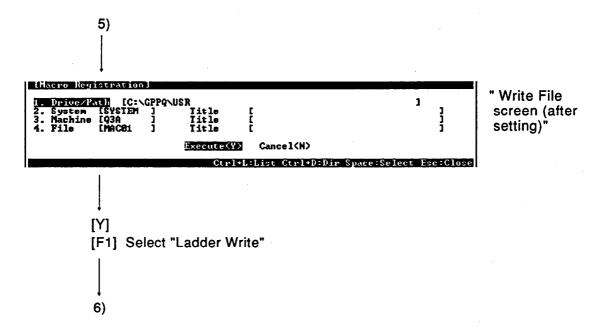




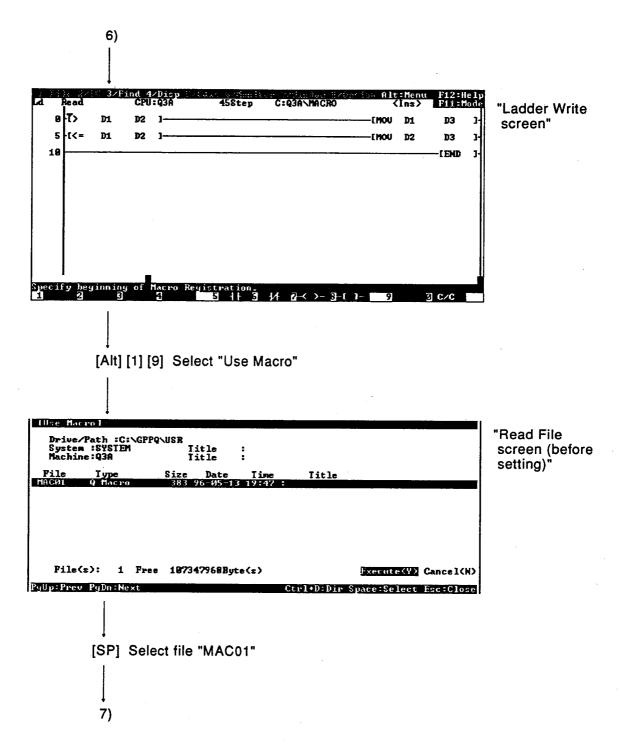


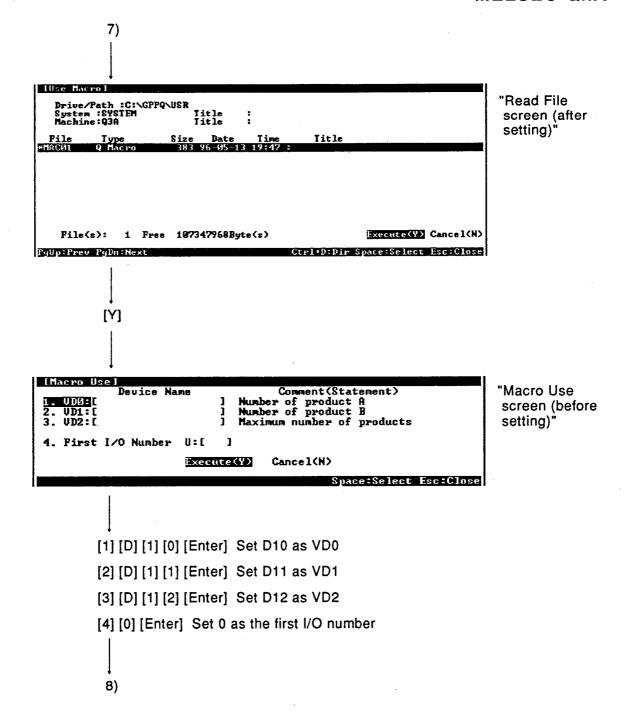


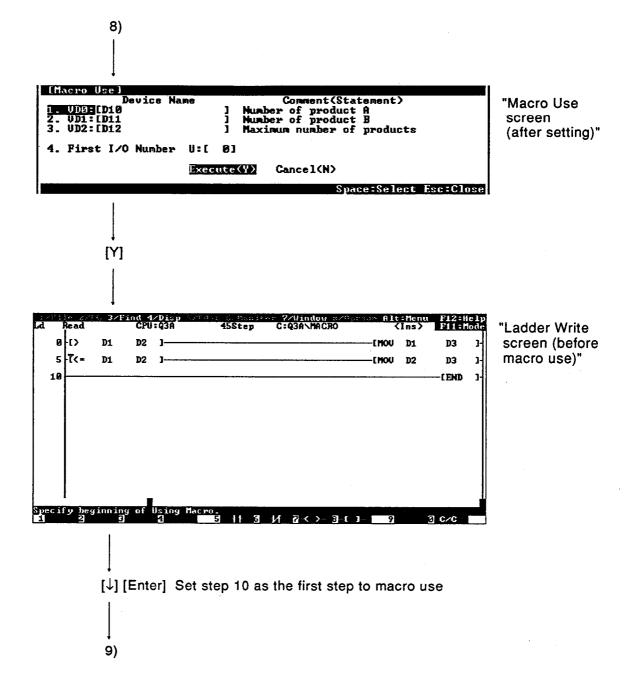


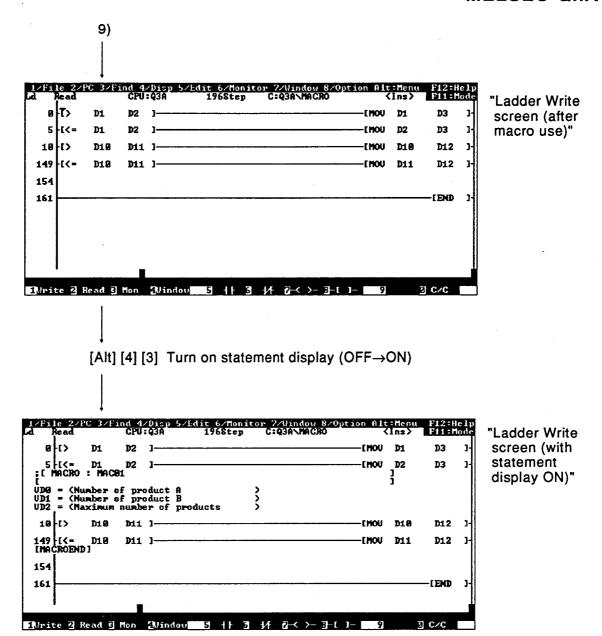


(3) Read a registered macro file and utilize the macro.

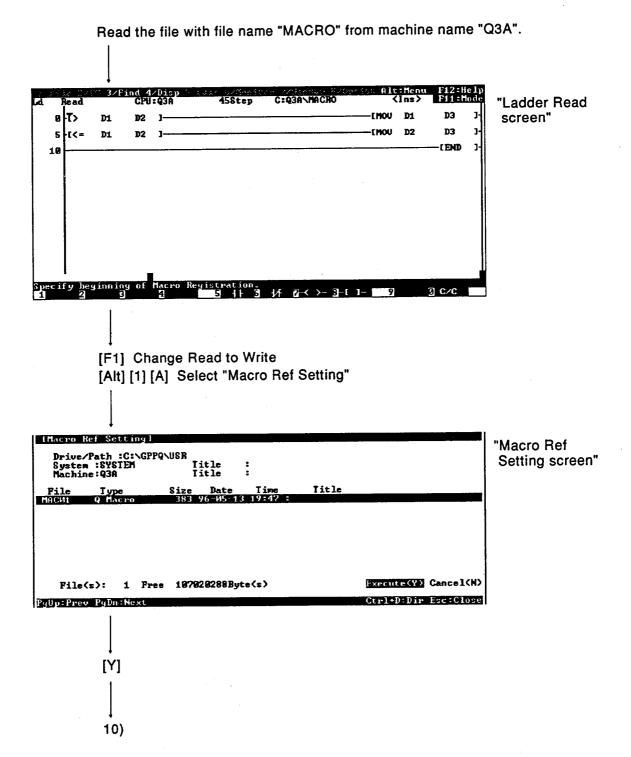


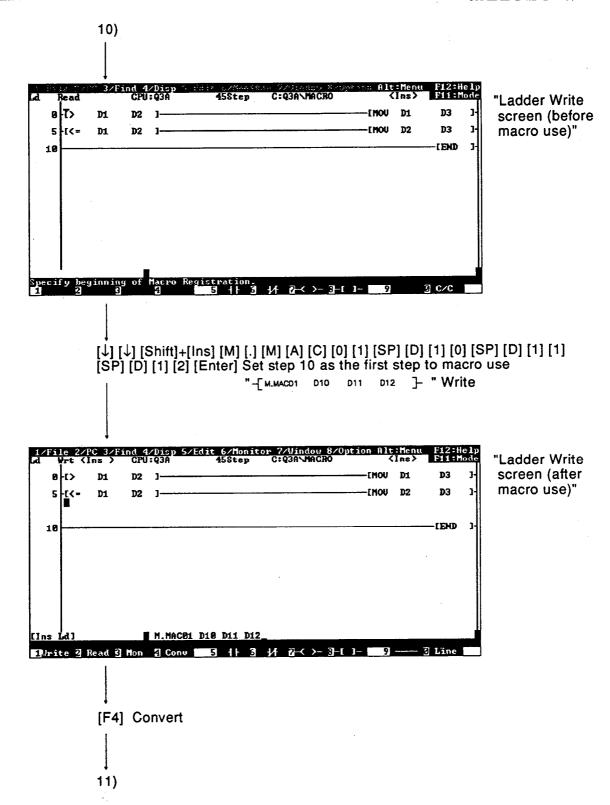






(4) Macro use with macro instruction "M. file name ☐ VD1 ☐ VD2 ☐ D3".





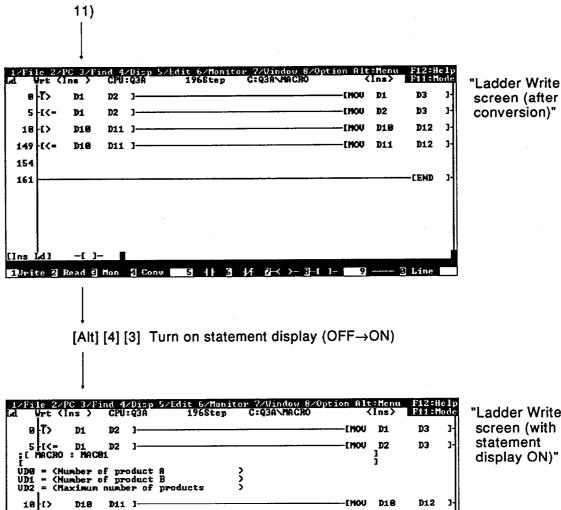
149 [(- D10 [MACROEND]

1Mrite 2 Read 3 Mon

154 161

D11 3-

2 Conv



-[HOU

D11

D12

"Ladder Write screen (with statement display ON)"

QnACPU GUIDEBOOK

MODEL	QNACPU-G-E	
MODEL CODE	13JF10	
IB(NA)66606-A(9607)MEE		



HEAD OFFICE : MITSUBISHI DENKI BLDG MARUNOUCHI TOKYO 100-0005 TELEX : J24532 CABLE MELCO TOKYO NAGOYA WORKS : 1-14 , YADA-MINAMI 5 , HIGASHI-KU, NAGOYA , JAPAN

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